



SPEC® CINT2006 Result

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Itaotec

SPECint®_rate2006 = Not Run

Servidor Itaotec LX211 (Intel Xeon E5410)

SPECint_rate_base2006 = 87.7

CPU2006 license: 9001

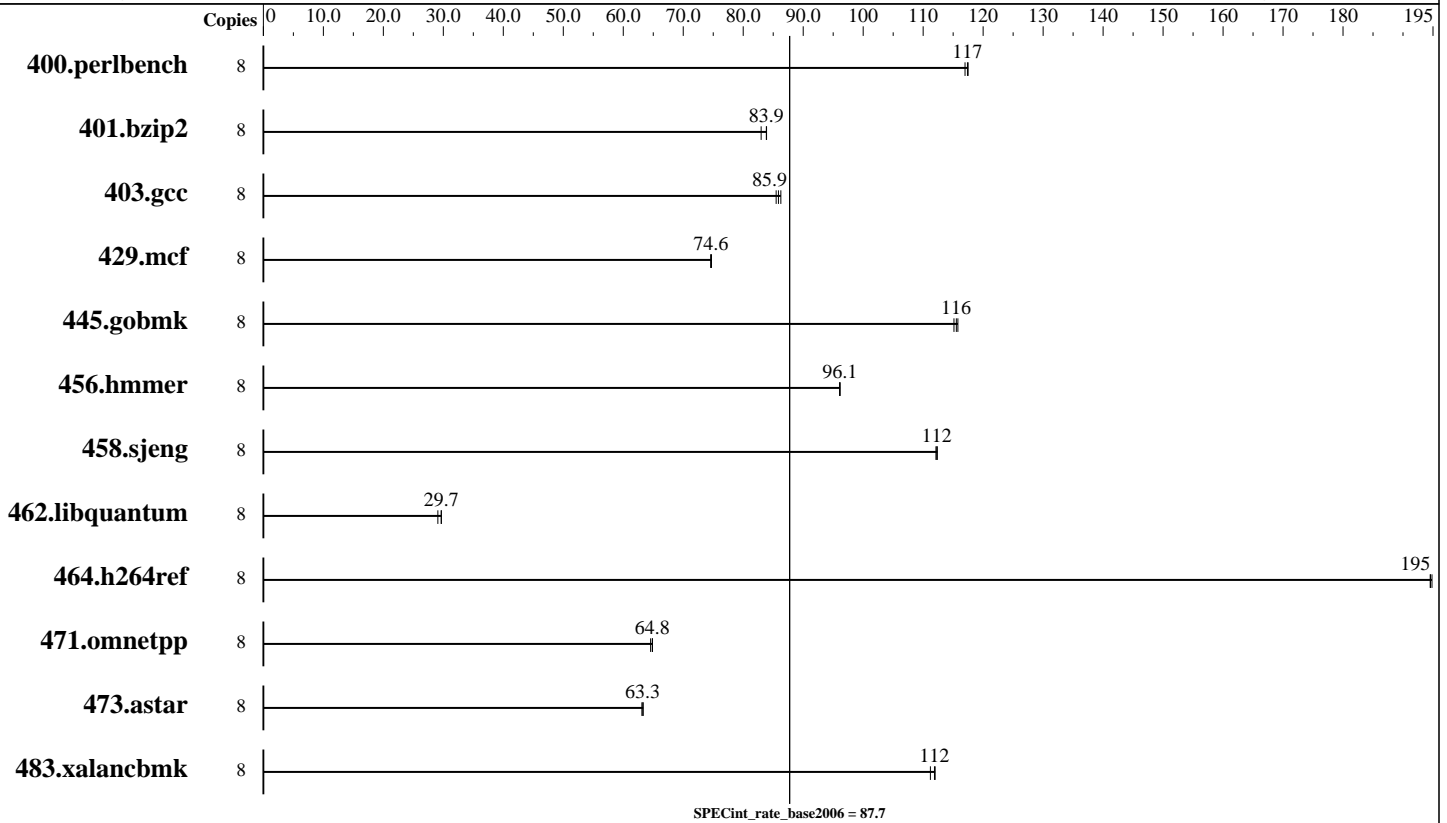
Test sponsor: Itaotec

Tested by: Itaotec

Test date: Mar-2008

Hardware Availability: Dec-2007

Software Availability: Jan-2008



Hardware

CPU Name: Intel Xeon E5410
 CPU Characteristics:
 CPU MHz: 2330
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 12 GB (6 * 2 GB PC2-5300 FBDIMM, CL-5-5-5, ECC)
 Disk Subsystem: 1 x SATA, 200GB, 7200 RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler for Linux version 10.1 Build 20080112 Package ID: l_cc_p_10.1.012
 Auto Parallel: No
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: Not Applicable
 Other Software: Binutils 2.17.10.50
 MicroQuill SmartHeap V8.1



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Results Table

| Benchmark | Base | | | | | | Peak | | | | | | | |
|----------------|--------|------------|-------------|------------|-------------|-------------|-------------|--------|---------|-------|---------|-------|---------|-------|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 8 | 666 | 117 | 665 | 117 | 668 | 117 | | | | | | | |
| 401.bzip2 | 8 | 920 | 83.9 | 930 | 83.0 | 921 | 83.9 | | | | | | | |
| 403.gcc | 8 | 753 | 85.5 | 746 | 86.3 | 750 | 85.9 | | | | | | | |
| 429.mcf | 8 | 977 | 74.7 | 978 | 74.6 | 978 | 74.6 | | | | | | | |
| 445.gobmk | 8 | 725 | 116 | 726 | 116 | 729 | 115 | | | | | | | |
| 456.hammer | 8 | 776 | 96.1 | 777 | 96.1 | 777 | 96.1 | | | | | | | |
| 458.sjeng | 8 | 863 | 112 | 862 | 112 | 862 | 112 | | | | | | | |
| 462.libquantum | 8 | 5698 | 29.1 | 5588 | 29.7 | 5589 | 29.7 | | | | | | | |
| 464.h264ref | 8 | 910 | 195 | 909 | 195 | 910 | 195 | | | | | | | |
| 471.omnetpp | 8 | 771 | 64.8 | 771 | 64.9 | 774 | 64.6 | | | | | | | |
| 473.astar | 8 | 888 | 63.3 | 887 | 63.3 | 890 | 63.1 | | | | | | | |
| 483.xalancbmk | 8 | 496 | 111 | 493 | 112 | 493 | 112 | | | | | | | |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.
'/usr/bin/taskset' used to bind benchmark copies to processors.

Platform Notes

BIOS configuration:
Hardware Prefetch Disabled

Base Compiler Invocation

C benchmarks:
icc
C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



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Itautec

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Base Optimization Flags

C benchmarks:
-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/opt/sh/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Itautec-ic10.1-FP-intel64-linux-flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Itautec-ic10.1-FP-intel64-linux-flags.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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