



# SPEC® CINT2006 Result

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## Fujitsu Siemens Computers

## SPECint®\_rate2006 = Not Run

### PRIMERGY BX620 S4, Intel Xeon L5410, 2.33 GHz

## SPECint\_rate\_base2006 = 94.1

CPU2006 license: 22

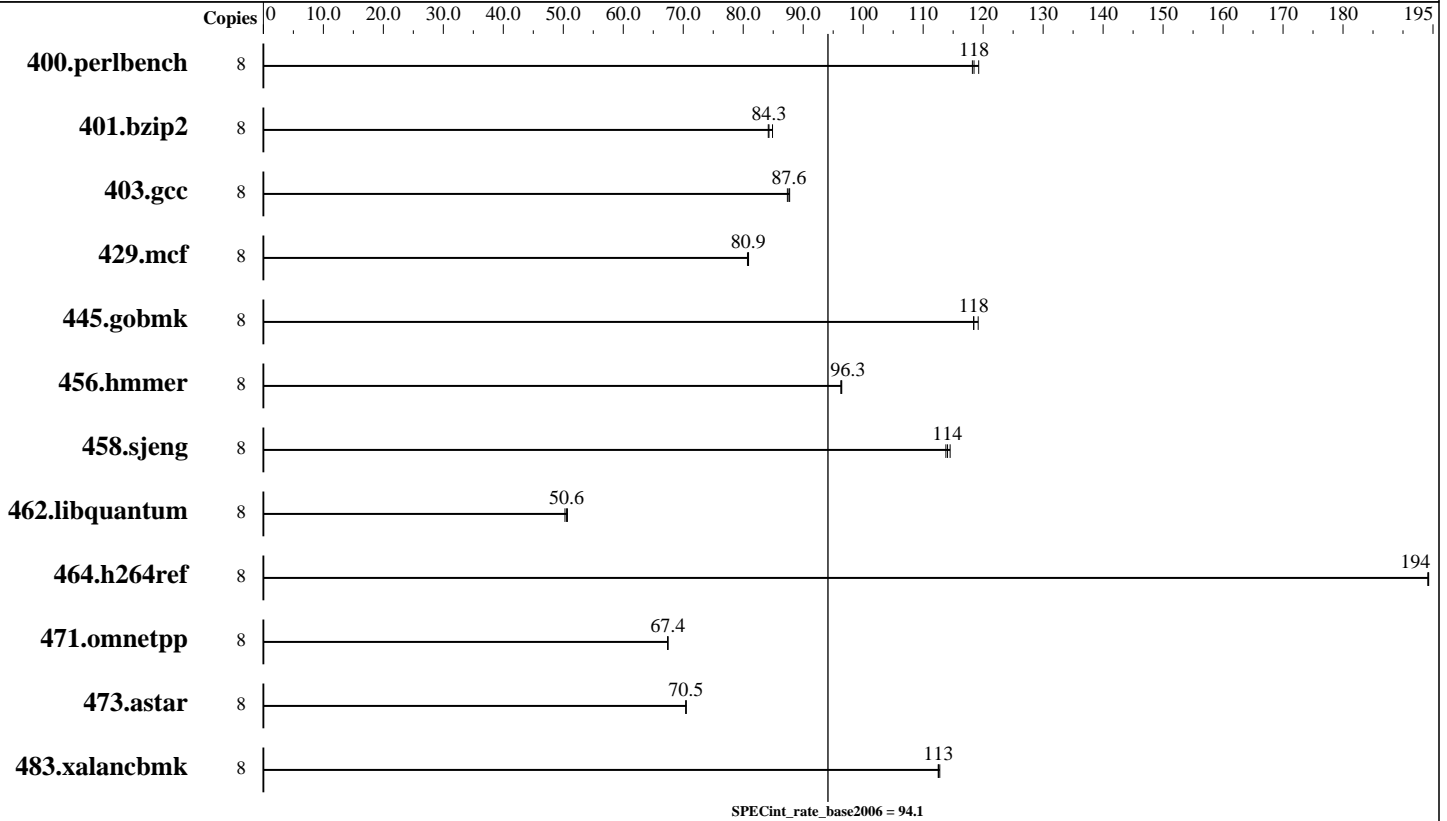
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Jan-2008

Hardware Availability: Feb-2008

Software Availability: Nov-2007



### Hardware

CPU Name: Intel Xeon L5410  
 CPU Characteristics: 1333 MHz system bus  
 CPU MHz: 2333  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (8x2 GB PC2-5300F, 2 rank, CAS 5-5-5, with ECC)  
 Disk Subsystem: Seagate ST973451SS (SAS, 73GB, 15000rpm)  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP1, Kernel 2.6.16.46-0.12-smp  
 Compiler: Intel C++ Compiler for Linux32 and Linux64 Version 10.1 - Build 20070725  
 Auto Parallel: No  
 File System: ext2  
 System State: Multiuser, Runlevel 3  
 Base Pointers: 32-bit  
 Peak Pointers: Not Applicable  
 Other Software: MicroQuill SmartHeap Library, Version 8.1  
 binutils-2.17.tar.gz, Version 2.17



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	655	119	<b>660</b>	<b>118</b>	661	118							
401.bzip2	8	<b>916</b>	<b>84.3</b>	917	84.2	909	84.9							
403.gcc	8	737	87.4	<b>735</b>	<b>87.6</b>	734	87.7							
429.mcf	8	902	80.9	904	80.7	<b>902</b>	<b>80.9</b>							
445.gobmk	8	709	118	704	119	<b>708</b>	<b>118</b>							
456.hammer	8	774	96.4	<b>775</b>	<b>96.3</b>	775	96.3							
458.sjeng	8	<b>849</b>	<b>114</b>	845	114	851	114							
462.libquantum	8	3297	50.3	<b>3279</b>	<b>50.6</b>	3270	50.7							
464.h264ref	8	912	194	911	194	<b>911</b>	<b>194</b>							
471.omnetpp	8	742	67.4	<b>741</b>	<b>67.4</b>	741	67.4							
473.astar	8	<b>797</b>	<b>70.5</b>	797	70.5	797	70.4							
483.xalancbmk	8	<b>490</b>	<b>113</b>	490	113	491	113							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

This result has been produced with binaries provided and compiled by Intel.

BIOS configuration:

Hardware Prefetch = Disable, Adjacent Sector Prefetch = Disable

For information about Fujitsu Siemens Computers please see:

<http://www.fujitsu-siemens.com>

## Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX



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## Base Optimization Flags

C benchmarks:

`-fast -inline-calloc -opt-malloc-options=3`

C++ benchmarks:

`-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap`

## Base Other Flags

C benchmarks:

`403.gcc: -Dalloca=_alloca`

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.01.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.01.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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