



# SPEC<sup>®</sup> CINT2006 Result

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## Acer Incorporated

## SPECint<sup>®</sup>\_rate2006 = 178

## Acer Altos R920 (Intel Xeon E7330)

## SPECint\_rate\_base2006 = 151

CPU2006 license: 97

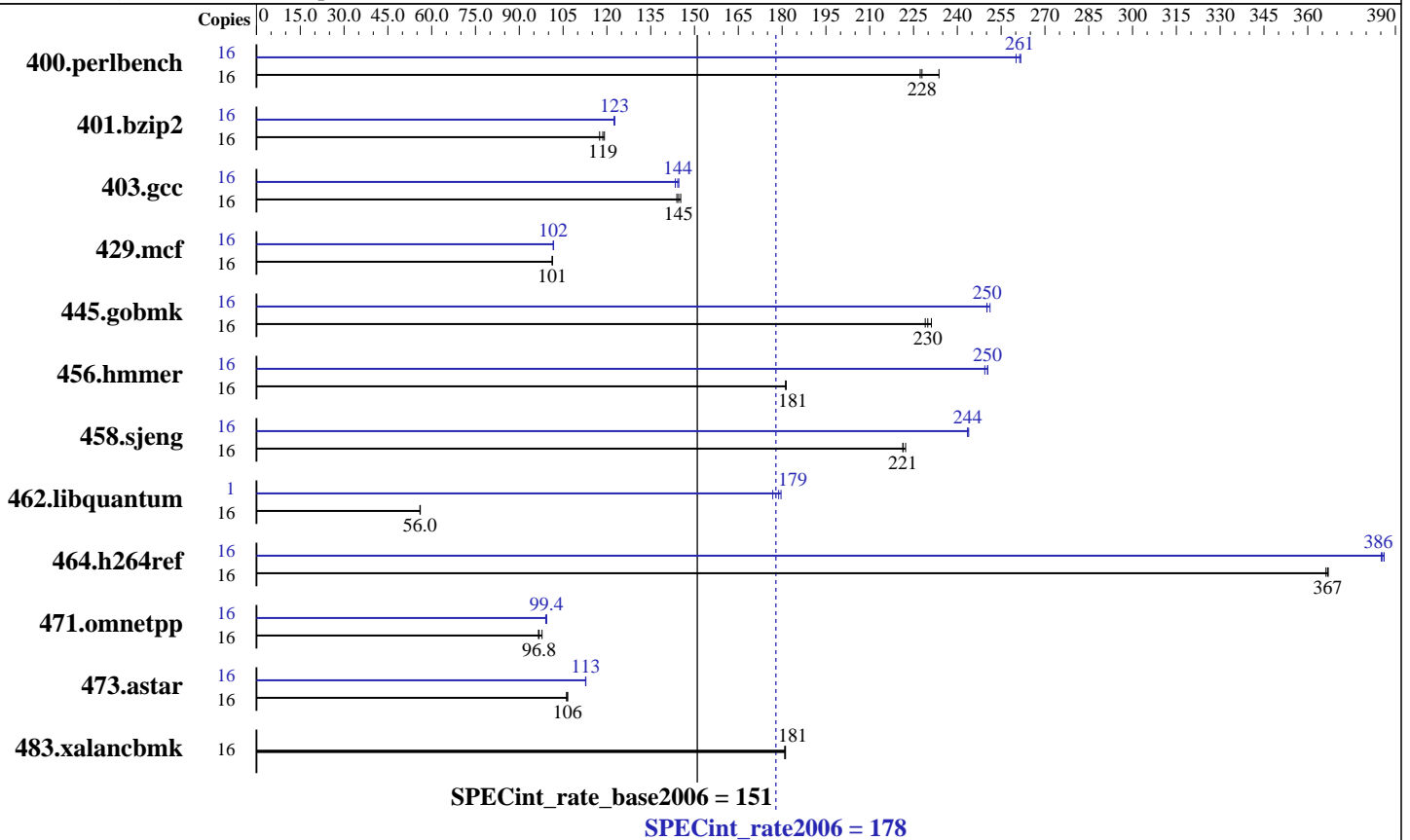
Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Nov-2007

Hardware Availability: Nov-2007

Software Availability: Nov-2007



### Hardware

CPU Name: Intel Xeon E7330  
 CPU Characteristics: 1066MHz system bus  
 CPU MHz: 2400  
 FPU: Integrated  
 CPU(s) enabled: 16 cores, 4 chips, 4 cores/chip  
 CPU(s) orderable: 1, 2, 4 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 6 MB I+D on chip per chip, 3 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (16 x 1048MB ECC FB-DIMM DDR2-667 CL5-5-5)  
 Disk Subsystem: 1 x 73GB 10000PRM SAS HDD  
 Other Hardware: None

### Software

Operating System: SuSE Linux Enterprise Server 10 SP1 EM64T (kernel 2.6.16.46-0.12-smp)  
 Compiler: Intel C++ Compiler for Linux32 and Linux64 version 10.1 Build 20070725  
 Auto Parallel: Yes  
 File System: ReiserFS  
 System State: Multi-user, level 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: MicroQuill SmartHeap Library 8.1



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	669	234	<b>686</b>	<b>228</b>	688	227	16	597	262	601	260	<b>598</b>	<b>261</b>
401.bzip2	16	1314	118	<b>1302</b>	<b>119</b>	1296	119	16	1259	123	1261	122	<b>1259</b>	<b>123</b>
403.gcc	16	<b>891</b>	<b>145</b>	895	144	887	145	16	<b>893</b>	<b>144</b>	898	143	891	145
429.mcf	16	1441	101	<b>1440</b>	<b>101</b>	1440	101	16	1436	102	1434	102	<b>1435</b>	<b>102</b>
445.gobmk	16	733	229	<b>730</b>	<b>230</b>	726	231	16	668	251	<b>671</b>	<b>250</b>	671	250
456.hammer	16	824	181	<b>824</b>	<b>181</b>	823	181	16	599	249	<b>596</b>	<b>250</b>	596	250
458.sjeng	16	<b>874</b>	<b>221</b>	871	222	875	221	16	<b>795</b>	<b>244</b>	795	243	794	244
462.libquantum	16	5909	56.1	<b>5917</b>	<b>56.0</b>	5921	56.0	1	<b>116</b>	<b>179</b>	117	177	115	180
464.h264ref	16	965	367	<b>965</b>	<b>367</b>	967	366	16	919	385	917	386	<b>918</b>	<b>386</b>
471.omnetpp	16	1023	97.8	1036	96.6	<b>1033</b>	<b>96.8</b>	16	1006	99.4	<b>1006</b>	<b>99.4</b>	1010	99.1
473.astar	16	1054	107	<b>1055</b>	<b>106</b>	1058	106	16	<b>996</b>	<b>113</b>	997	113	996	113
483.xalancbmk	16	610	181	<b>610</b>	<b>181</b>	609	181	16	610	181	<b>610</b>	<b>181</b>	609	181

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

### BIOS Settings

Hardware Prefetcher set to OFF

Adjacent Cache Line Prefetch set to OFF

Optimization for high-frequency FSB applications set to OFF

All results used taskset to bind processes to cores.

## Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32

462.libquantum: -DSPEC\_CPU\_LINUX

483.xalancbmk: -DSPEC\_CPU\_LINUX



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## Base Optimization Flags

C benchmarks:

-fast -inline-alloc -opt-malloc-options=3

C++ benchmarks:

-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc  
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib  
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include

456.hmmer: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc  
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib  
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include

C++ benchmarks:

icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

Continued on next page



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## Peak Optimization Flags (Continued)

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias  
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo  
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch  
-opt-streaming-stores always -vec-guard-write  
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=block  
-Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine  
-Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.22.html>



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You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.22.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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