



SPEC® CINT2006 Result

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IBM Corporation

SPECint®2006 = 24.3

IBM System x3550 (Intel Xeon X5365)

SPECint_base2006 = 20.8

CPU2006 license: 11

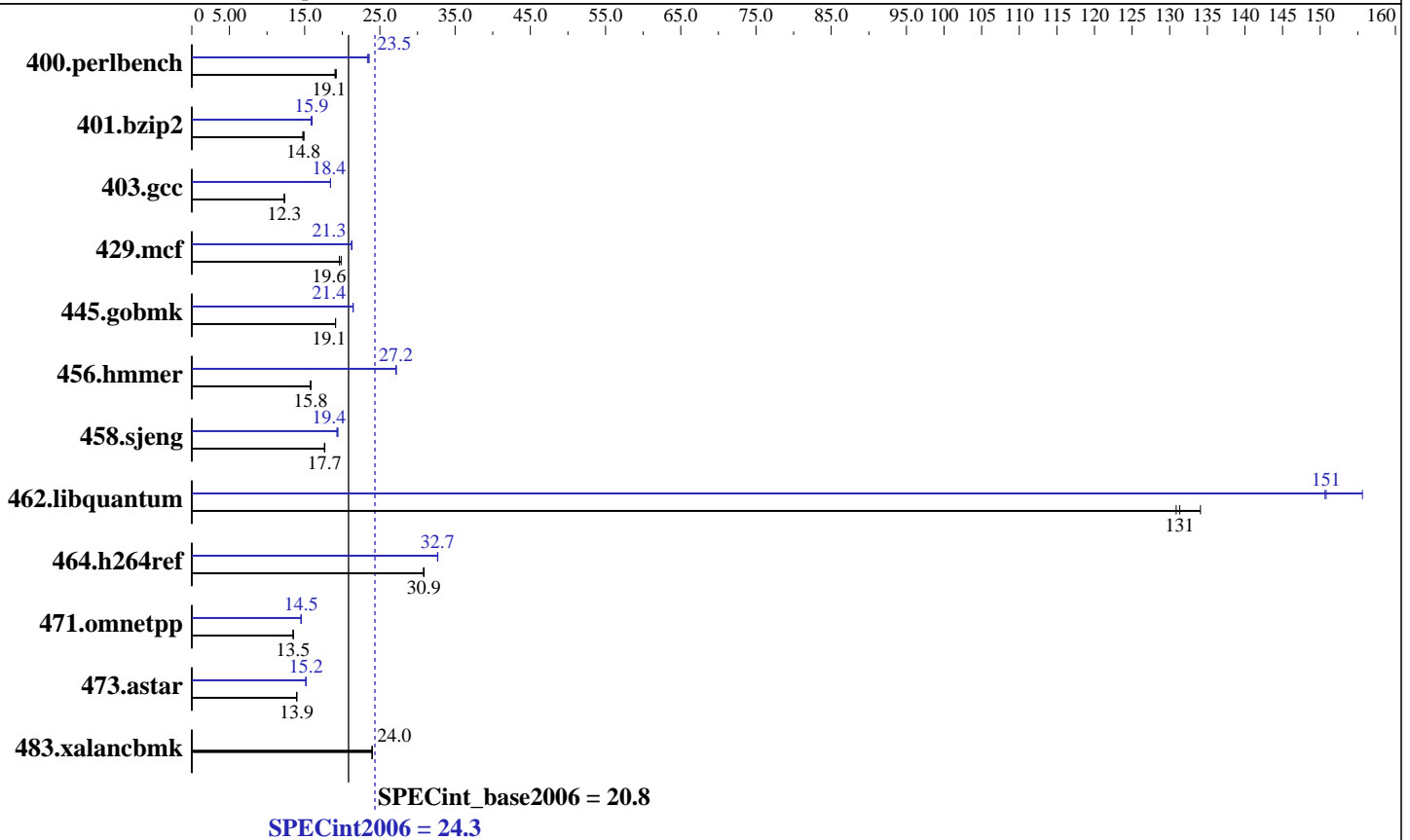
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Aug-2007

Hardware Availability: Sep-2007

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon X5365
 CPU Characteristics: 1333MHz system bus
 CPU MHz: 3000
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8 x 2GB DRR2-5300F ECC)
 Disk Subsystem: 1 x 36 GB SAS, 15000 RPM
 Other Hardware: None

Software

Operating System: SLES 10 (x86_64), 2.6.16.21-0.8-smp
 Compiler: Intel C++ Compiler for Linux version 10.1 Build 20070824
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Multi-user, run level 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: MicroQuill SmartHeap 8.1



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	509	19.2	513	19.0	<u>511</u>	<u>19.1</u>	415	23.6	418	23.3	<u>415</u>	<u>23.5</u>
401.bzip2	654	14.8	647	14.9	<u>650</u>	<u>14.8</u>	<u>606</u>	<u>15.9</u>	608	15.9	603	16.0
403.gcc	655	12.3	<u>654</u>	<u>12.3</u>	653	12.3	437	18.4	<u>437</u>	<u>18.4</u>	436	18.4
429.mcf	465	19.6	<u>465</u>	<u>19.6</u>	459	19.9	429	21.2	429	21.3	<u>429</u>	<u>21.3</u>
445.gobmk	549	19.1	<u>549</u>	<u>19.1</u>	549	19.1	489	21.4	<u>489</u>	<u>21.4</u>	489	21.4
456.hammer	590	15.8	591	15.8	<u>590</u>	<u>15.8</u>	344	27.1	<u>344</u>	<u>27.2</u>	343	27.2
458.sjeng	687	17.6	<u>685</u>	<u>17.7</u>	684	17.7	<u>624</u>	<u>19.4</u>	627	19.3	623	19.4
462.libquantum	158	131	<u>158</u>	<u>131</u>	155	134	<u>137</u>	<u>151</u>	133	156	138	151
464.h264ref	<u>717</u>	<u>30.9</u>	717	30.9	719	30.8	<u>677</u>	<u>32.7</u>	677	32.7	677	32.7
471.omnetpp	464	13.5	463	13.5	<u>464</u>	<u>13.5</u>	430	14.5	431	14.5	<u>430</u>	<u>14.5</u>
473.astar	503	14.0	<u>504</u>	<u>13.9</u>	504	13.9	<u>463</u>	<u>15.2</u>	463	15.1	462	15.2
483.xalancbmk	<u>288</u>	<u>24.0</u>	288	23.9	288	24.0	<u>288</u>	<u>24.0</u>	288	23.9	288	24.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to physical,0
KMP_STACKSIZE set to 8M

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -vec-guard-write -opt-malloc-options=3 -parallel
-par-runtime-control

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Base Optimization Flags (Continued)

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc
401.bzip2: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/bin/icc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/include
456.hmmer: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/bin/icc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/include
C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:
400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

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Peak Optimization Flags (Continued)

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch
-auto-ilp32

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmcr: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive
-auto-ilp32

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.28.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.28.xml>



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Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 14:50:51 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 16 October 2007.