



SPEC[®] CINT2006 Result

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Hewlett-Packard Company

SPECint[®]_rate2006 = 116

ProLiant DL360 G5
(3.0 GHz, Intel Xeon processor X5365)

SPECint_rate_base2006 = 97.5

CPU2006 license: 3

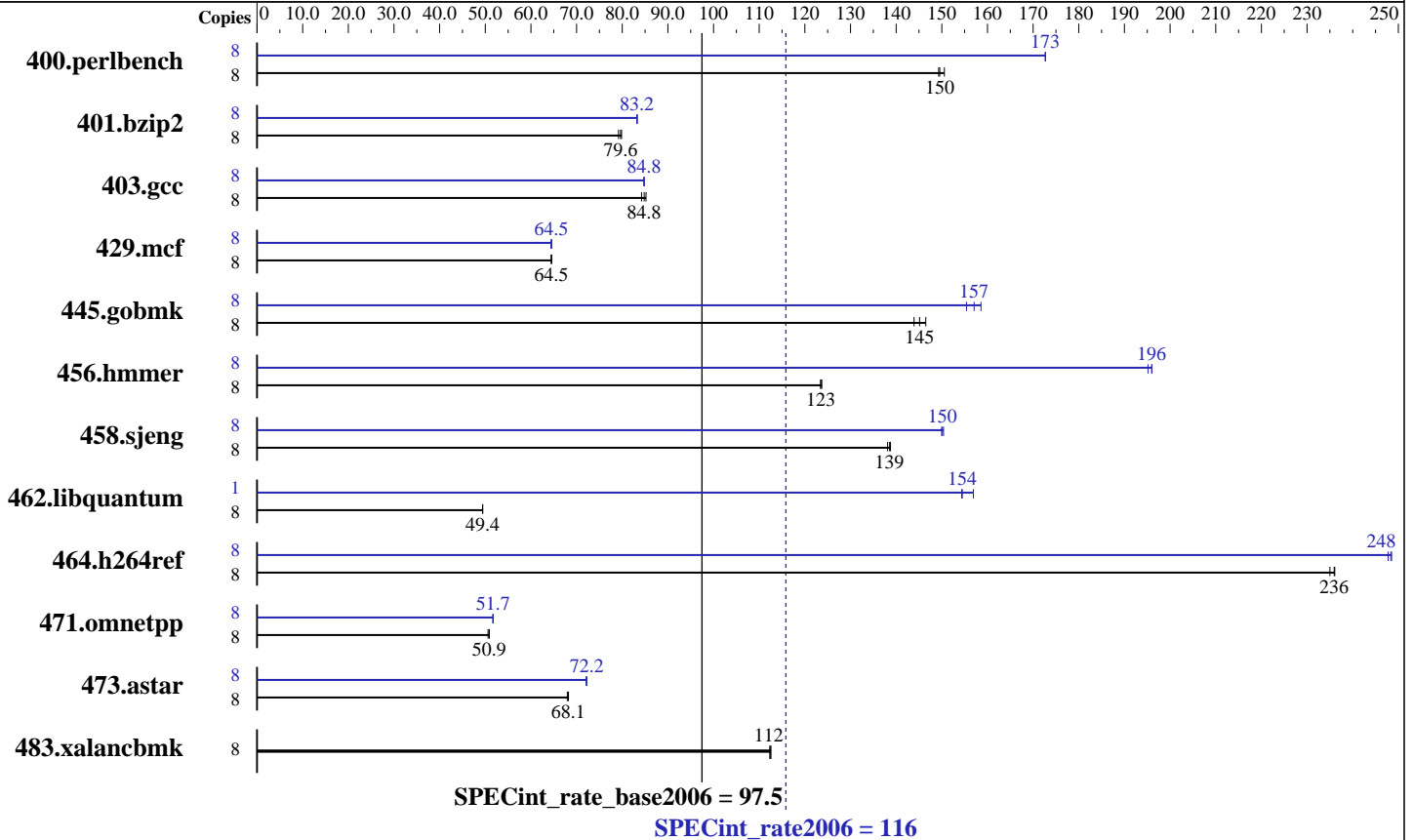
Test date: Aug-2007

Test sponsor: Hewlett-Packard Company

Hardware Availability: Sep-2007

Tested by: Hewlett-Packard Company

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon X5365
 CPU Characteristics: 3.0 GHz, 2x4 MB L2 shared, 1333 MHz system bus
 CPU MHz: 3000
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8x2 GB PC2-5300F CL5)
 Disk Subsystem: 1x72 GB 15 K SAS
 Other Hardware: None

Software

Operating System: SuSE Linux Enterprise Server 10 (x86_64) SP1
 kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler for Linux32 and Linux64
 version 10.1
 Build 20070725
 Auto Parallel: Yes
 File System: ext2
 System State: Multi-user run level 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: MicroQuill SmartHeap Library 8.1
 binutils-2.17.50



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Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	519	151	523	149	<u>523</u>	<u>150</u>	8	453	173	453	173	<u>453</u>	<u>173</u>
401.bzip2	8	967	79.9	<u>969</u>	<u>79.6</u>	975	79.2	8	928	83.2	927	83.3	<u>927</u>	<u>83.2</u>
403.gcc	8	<u>759</u>	<u>84.8</u>	756	85.2	765	84.2	8	760	84.8	759	84.8	<u>760</u>	<u>84.8</u>
429.mcf	8	1130	64.5	<u>1132</u>	<u>64.5</u>	1132	64.4	8	1130	64.6	1133	64.4	<u>1132</u>	<u>64.5</u>
445.gobmk	8	573	146	<u>578</u>	<u>145</u>	583	144	8	<u>534</u>	<u>157</u>	529	159	540	155
456.hammer	8	603	124	605	123	<u>604</u>	<u>123</u>	8	382	195	381	196	<u>381</u>	<u>196</u>
458.sjeng	8	<u>699</u>	<u>139</u>	701	138	698	139	8	<u>645</u>	<u>150</u>	644	150	646	150
462.libquantum	8	3355	49.4	3354	49.4	<u>3354</u>	<u>49.4</u>	1	<u>134</u>	<u>154</u>	134	154	132	157
464.h264ref	8	<u>750</u>	<u>236</u>	750	236	753	235	8	712	249	<u>713</u>	<u>248</u>	715	248
471.omnetpp	8	988	50.6	<u>983</u>	<u>50.9</u>	983	50.9	8	967	51.7	967	51.7	<u>967</u>	<u>51.7</u>
473.astar	8	823	68.2	826	68.0	<u>824</u>	<u>68.1</u>	8	779	72.1	<u>778</u>	<u>72.2</u>	778	72.2
483.xalancbmk	8	490	113	491	112	<u>491</u>	<u>112</u>	8	490	113	491	112	<u>491</u>	<u>112</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to physical,0
KMP_STACKSIZE set to 64M

Platform Notes

BIOS configuration:
Power Regulator set to Static High Performance Mode
Adjacent Sector Prefetch Disabled

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

Continued on next page



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Base Portability Flags (Continued)

462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:

-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include

456.hmmer: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



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Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca



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The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-flags.20090714.01.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-flags.20090714.01.xml>

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