



SPEC® CINT2006 Result

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Lenovo Group Limited

(Test Sponsor: Intel Corporation)

SPECint®_rate2006 = Not Run

Lenovo ThinkPad T61(Intel Core 2 Duo T7700)

SPECint_rate_base2006 = 23.3

CPU2006 license: 13

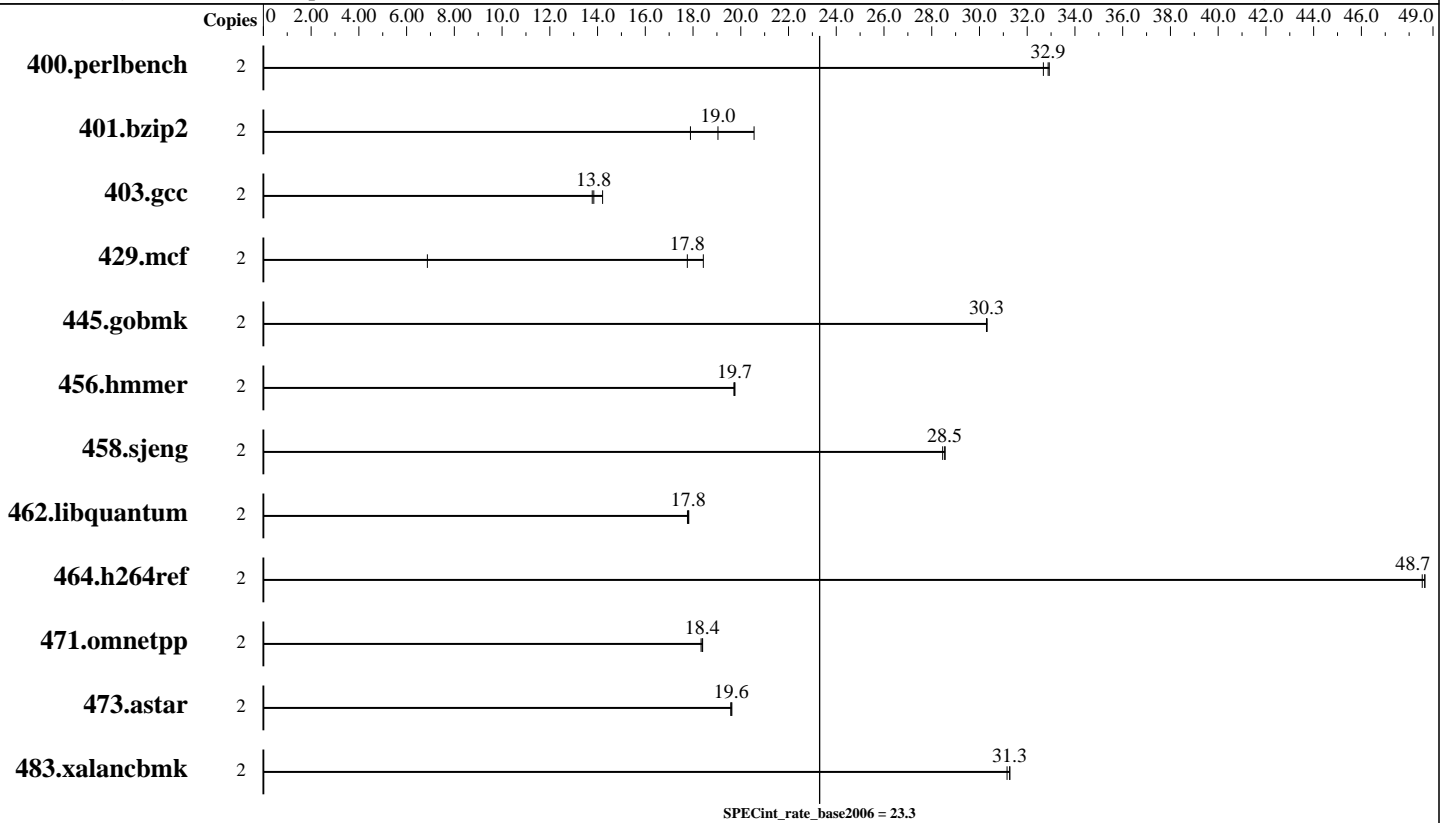
Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Jun-2007

Hardware Availability: Jun-2007

Software Availability: Feb-2007



Hardware

CPU Name: Intel Core 2 Duo T7700
 CPU Characteristics: 2.40 GHz, 4MB L2, 800 MHz Bus
 CPU MHz: 2400
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 4 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 2 GB (2x1GB Qimonda* PC-5300 DDR2-667 5-5-5-12)
 Disk Subsystem: Hitachi* HTS721010G9 SATA 100GB 7200RPM
 Other Hardware: None

Software

Operating System: Windows Vista Business
 Compiler: Intel C++ Compiler for IA32 version 9.1
 Build no 20060816
 Microsoft Visual Studio .Net 2003 (for libraries)
 Auto Parallel: No
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: Not Applicable
 Other Software: SmartHeap library V7.4 from Microquill



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	594	32.9	593	32.9	598	32.7							
401.bzip2	2	939	20.6	1013	19.0	1079	17.9							
403.gcc	2	1133	14.2	1163	13.8	1169	13.8							
429.mcf	2	2655	6.87	990	18.4	1027	17.8							
445.gobmk	2	692	30.3	693	30.3	692	30.3							
456.hmmmer	2	945	19.8	947	19.7	945	19.7							
458.sjeng	2	847	28.6	850	28.5	848	28.5							
462.libquantum	2	2332	17.8	2328	17.8	2325	17.8							
464.h264ref	2	910	48.7	910	48.7	912	48.5							
471.omnetpp	2	679	18.4	682	18.3	680	18.4							
473.astar	2	716	19.6	717	19.6	715	19.6							
483.xalancbmk	2	443	31.2	441	31.3	441	31.3							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Binaries built on Windows XP Professional w/ SP2
Vista Power Profile set to High Performance

Base Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99
C++ benchmarks:
icl -Qvc7.1

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Base Optimization Flags

C benchmarks:
-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

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Base Optimization Flags (Continued)

C++ benchmarks:

```
-fast -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.20090714.00.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.20090714.00.xml>

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For other inquiries, please contact webmaster@spec.org.

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