



SPEC® CFP2006 Result

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Fujitsu Siemens Computers

PRIMERGY TX120, Intel Celeron processor 440, 2.0 GHz

SPECfp®2006 = 11.5

SPECfp_base2006 = 11.4

CPU2006 license: 22

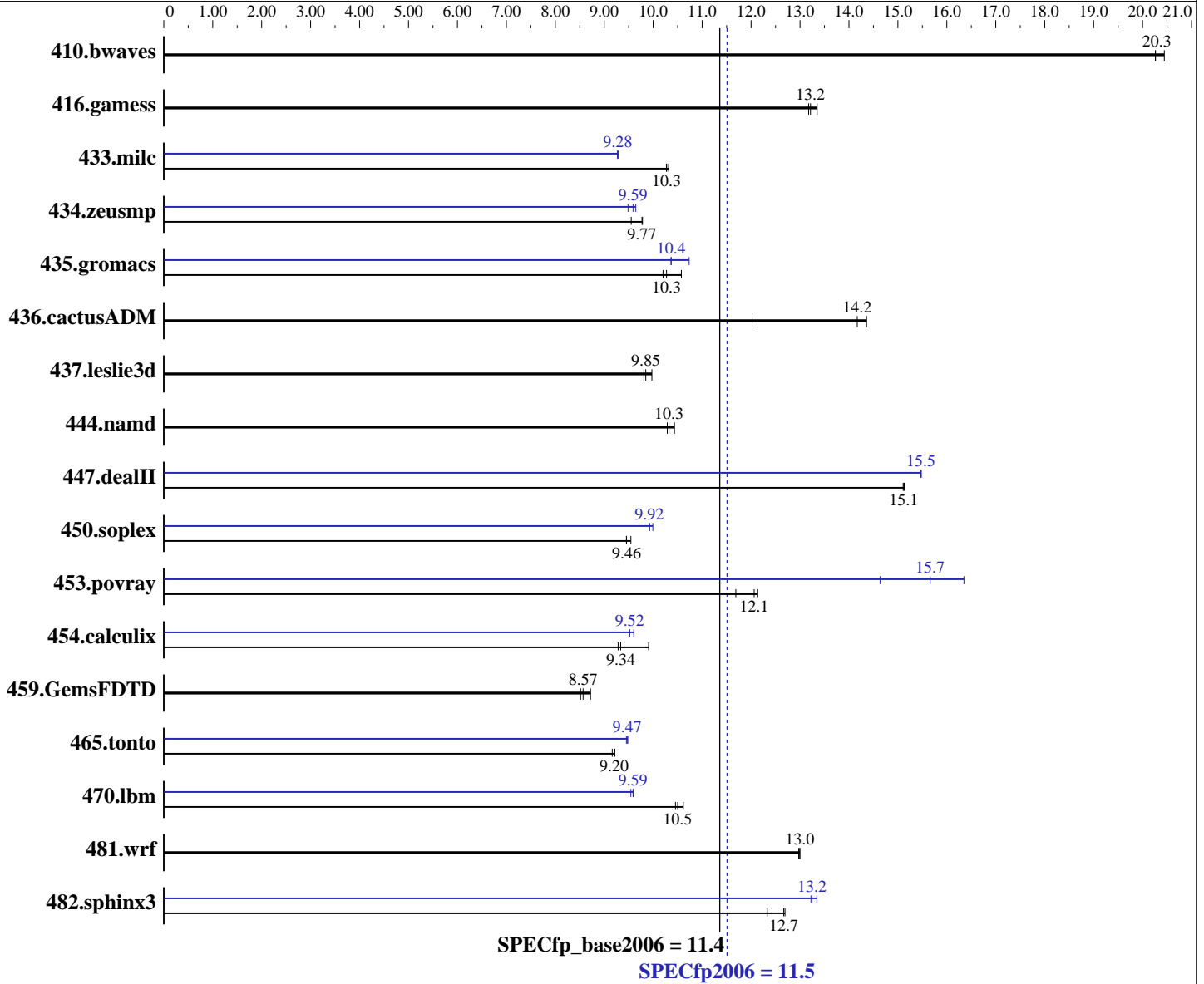
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Jun-2007

Hardware Availability: Jul-2007

Software Availability: Mar-2007



Hardware

CPU Name: Intel Celeron 440
 CPU Characteristics: 800 MHz system bus
 CPU MHz: 2000
 FPU: Integrated
 CPU(s) enabled: 1 core, 1 chip, 1 core/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per chip
 Secondary Cache: 512 KB I+D on chip per chip

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Software

Operating System: 64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp on an x86_64
 Compiler: Intel C++ Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package-ID: l_cc_p_9.1.047
 Intel Fortran Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package ID: l_fc_p_9.1.043
 Auto Parallel: No
 File System: ReiserFS

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L3 Cache: None
Other Cache: None
Memory: 8 GB (4x2 GB DDR2 PC2-4200E, 2 rank, CAS 4-4-4, with ECC)
Disk Subsystem: Seagate ST973401SS (SAS, 73GB 10000rpm)
Other Hardware: None

System State: Multiuser, Runlevel 3
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	665	20.4	670	20.3	671	20.3	665	20.4	670	20.3	671	20.3
416.gamess	1482	13.2	1467	13.3	1486	13.2	1482	13.2	1467	13.3	1486	13.2
433.milc	890	10.3	893	10.3	894	10.3	989	9.28	989	9.28	990	9.27
434.zeusmp	953	9.55	931	9.77	930	9.78	949	9.59	959	9.49	944	9.64
435.gromacs	675	10.6	700	10.2	695	10.3	665	10.7	689	10.4	689	10.4
436.cactusADM	832	14.4	843	14.2	994	12.0	832	14.4	843	14.2	994	12.0
437.leslie3d	942	9.97	954	9.85	958	9.81	942	9.97	954	9.85	958	9.81
444.namd	769	10.4	777	10.3	780	10.3	769	10.4	777	10.3	780	10.3
447.dealII	756	15.1	757	15.1	757	15.1	739	15.5	739	15.5	740	15.5
450.soplex	874	9.54	882	9.46	882	9.45	835	9.99	840	9.92	840	9.92
453.povray	438	12.1	441	12.1	455	11.7	325	16.4	363	14.6	340	15.7
454.calculix	833	9.91	884	9.34	889	9.28	859	9.60	867	9.52	867	9.52
459.GemsFDTD	1217	8.72	1246	8.52	1238	8.57	1217	8.72	1246	8.52	1238	8.57
465.tonto	1070	9.20	1068	9.22	1074	9.16	1038	9.48	1039	9.47	1041	9.45
470.lbm	1294	10.6	1308	10.5	1314	10.5	1440	9.54	1433	9.59	1432	9.59
481.wrf	861	13.0	859	13.0	859	13.0	861	13.0	859	13.0	859	13.0
482.sphinx3	1539	12.7	1535	12.7	1581	12.3	1472	13.2	1461	13.3	1474	13.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

General Notes

The system bus runs at 800 MHz

All binaries were built with 64-bit Intel compiler except:
433.milc, 434.zeusmp, 450.soplex, 470.lbm and 482.sphinx3 in peak were built with 32-bit Intel compiler by changing the path for include and library files.

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Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-fast

C++ benchmarks:

-fast

Fortran benchmarks:

-fast

Benchmarks using both Fortran and C:

-fast



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Peak Compiler Invocation

C benchmarks:

```
/opt/intel/cc/9.1.047/bin/icc -I/opt/intel/cc/9.1.047/include
-L/opt/intel/cc/9.1.047/lib
```

C++ benchmarks (except as noted below):

icpc

```
450.soplex: /opt/intel/cc/9.1.047/bin/icpc
-I/opt/intel/cc/9.1.047/include -L/opt/intel/cc/9.1.047/lib
```

Fortran benchmarks (except as noted below):

ifort

```
434.zeusmp: /opt/intel/fc/9.1.043/bin/ifort
-I/opt/intel/fc/9.1.043/include -L/opt/intel/fc/9.1.043/lib
```

Benchmarks using both Fortran and C:

icc ifort

Peak Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.deallI: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
433.milc: -prof_gen(pass 1) -prof_use(pass 2) -fast
```

```
470.lbm: Same as 433.milc
```

```
482.sphinx3: -fast
```

C++ benchmarks:

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Peak Optimization Flags (Continued)

444.namd: basepeak = yes

447.dealII: -prof_gen(pass 1) -prof_use(pass 2) -fast

450.soplex: Same as 447.dealII

453.povray: Same as 447.dealII

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: basepeak = yes

434.zeusmp: -fast

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -prof_gen(pass 1) -prof_use(pass 2) -fast

Benchmarks using both Fortran and C:

435.gromacs: -prof_gen(pass 1) -prof_use(pass 2) -fast

436.cactusADM: basepeak = yes

454.calculix: Same as 435.gromacs

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.xml

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For other inquiries, please contact webmaster@spec.org.

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