



SPEC® CINT2006 Result

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Fujitsu Siemens Computers

PRIMERGY TX120, Intel Celeron processor 440, 2.0 GHz

SPECint®2006 = 11.7

SPECint_base2006 = 11.1

CPU2006 license: 22

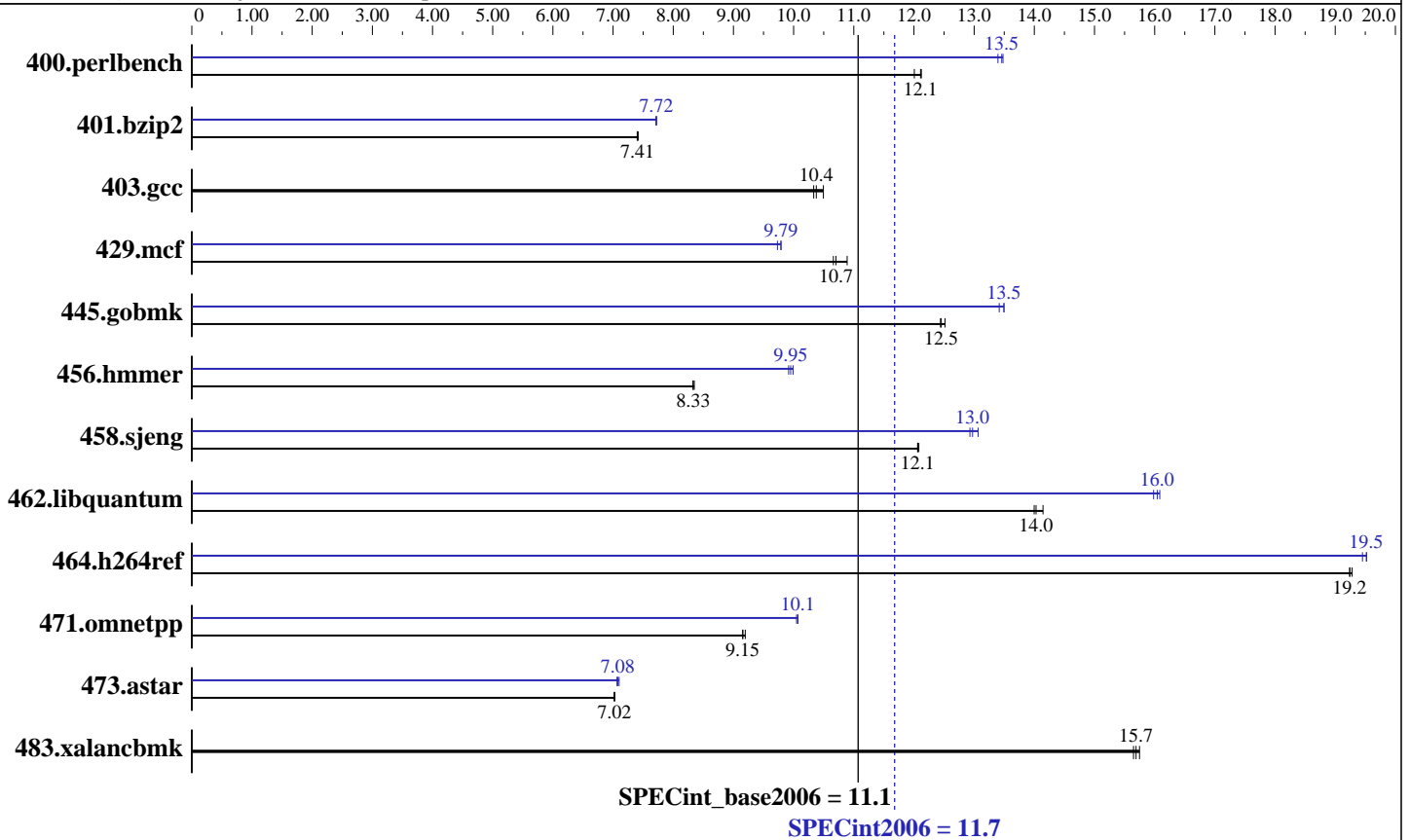
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Jun-2007

Hardware Availability: Jul-2007

Software Availability: Mar-2007



Hardware

CPU Name: Intel Celeron 440
 CPU Characteristics: 800 MHz system bus
 CPU MHz: 2000
 FPU: Integrated
 CPU(s) enabled: 1 core, 1 chip, 1 core/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per chip
 Secondary Cache: 512 KB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 8 GB (4x2 GB DDR2 PC2-4200E, 2 rank, CAS 4-4-4, with ECC)
 Disk Subsystem: Seagate ST973401SS (SAS, 73GB 10000rpm)
 Other Hardware: None

Software

Operating System: 64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp on an x86_64
 Compiler: Intel C++ Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package-ID: l_cc_p_9.1.047
 Auto Parallel: No
 File System: ReiserFS
 System State: Multiuser, Runlevel 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Smart Heap Library, Version 8.1



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	806	12.1	814	12.0	807	12.1	729	13.4	725	13.5	726	13.5
401.bzip2	1301	7.42	1303	7.41	1304	7.40	1251	7.72	1250	7.72	1251	7.71
403.gcc	767	10.5	779	10.3	776	10.4	767	10.5	779	10.3	776	10.4
429.mcf	837	10.9	852	10.7	855	10.7	931	9.79	932	9.79	937	9.73
445.gobmk	838	12.5	843	12.4	842	12.5	777	13.5	782	13.4	777	13.5
456.hmmer	1117	8.35	1120	8.33	1120	8.33	934	9.99	941	9.92	938	9.95
458.sjeng	1003	12.1	1003	12.1	1002	12.1	926	13.1	933	13.0	936	12.9
462.libquantum	1465	14.1	1480	14.0	1477	14.0	1288	16.1	1292	16.0	1296	16.0
464.h264ref	1148	19.3	1150	19.2	1150	19.2	1134	19.5	1133	19.5	1138	19.5
471.omnetpp	680	9.20	683	9.15	683	9.15	621	10.1	622	10.0	621	10.1
473.astar	1000	7.02	999	7.03	999	7.02	989	7.10	992	7.08	994	7.07
483.xalancbmk	438	15.7	441	15.6	440	15.7	438	15.7	441	15.6	440	15.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

General Notes

The system bus runs at 800 MHz

All binaries were built with 32-bit Intel compiler except:
401.bzip2, 456.hmmer and 462.libquantum in peak were built with
64-bit Intel compiler by changing the path for include and library files.

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Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_X64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-fast

C++ benchmarks:

-xP -O3 -ipo -no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/cce/9.1.047/bin/icc
-I/opt/intel/cce/9.1.047/include
-L/opt/intel/cce/9.1.047/lib

456.hmmer: /opt/intel/cce/9.1.047/bin/icc
-I/opt/intel/cce/9.1.047/include
-L/opt/intel/cce/9.1.047/lib

462.libquantum: /opt/intel/cce/9.1.047/bin/icc
-I/opt/intel/cce/9.1.047/include
-L/opt/intel/cce/9.1.047/lib

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



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Peak Optimization Flags

C benchmarks:

400.perlbench: -prof_gen(pass 1) -prof_use(pass 2) -fast

401.bzip2: -fast

403.gcc: basepeak = yes

429.mcf: -prof_gen(pass 1) -prof_use(pass 2) -fast
-L/opt/SmartHeap_8_1/lib -lsmartheap

445.gobmk: Same as 429.mcf

456.hmmer: Same as 400.perlbench

458.sjeng: Same as 429.mcf

462.libquantum: Same as 400.perlbench

464.h264ref: Same as 429.mcf

C++ benchmarks:

471.omnetpp: -prof_gen(pass 1) -prof_use(pass 2) -xP -O3 -ipo
-no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap

473.astar: -prof_gen(pass 1) -prof_use(pass 2) -fast
-L/opt/SmartHeap_8_1/lib -lsmartheap

483.xalancbmk: basepeak = yes

The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.xml

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