



# SPEC® CINT2006 Result

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## Supermicro Motherboard PDSBA+

**SPECint®2006 = 11.2**  
**SPECint\_base2006 = 10.7**

CPU2006 license: 001176

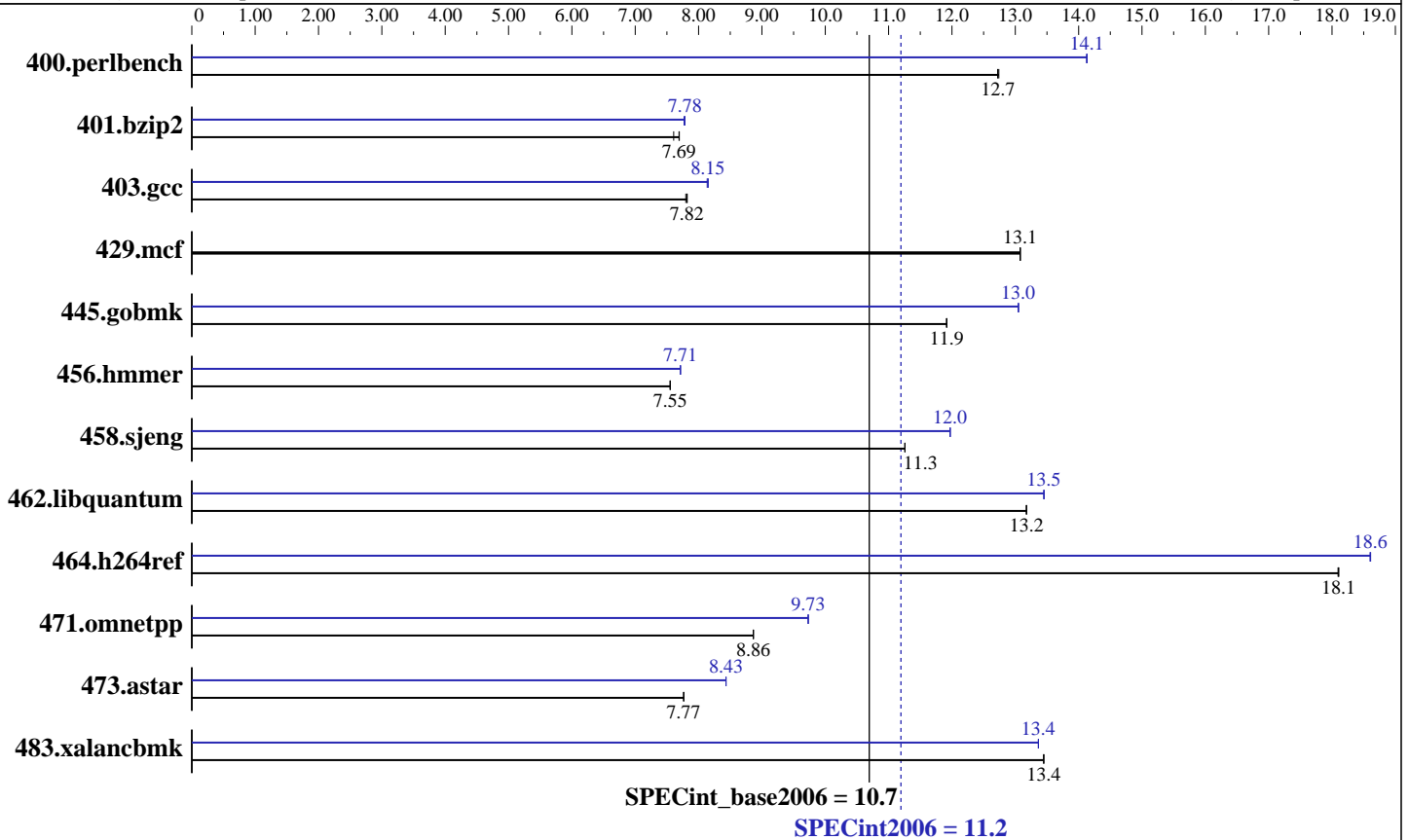
Test sponsor: Supermicro

Tested by: Supermicro

Test date: May-2007

Hardware Availability: Apr-2007

Software Availability: Apr-2007



### Hardware

CPU Name: Intel Core 2 Duo E4300  
 CPU Characteristics: 1.8GHz, 800MHz bus  
 CPU MHz: 1800  
 FPU: Integrated  
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 2 MB I+D on chip per chip  
 L3 Cache: None  
 Other Cache: None  
 Memory: 2 GB (2X 1GB ECC, CL4, 533MHz, UnBuffer)  
 Disk Subsystem: 150GB SATA, 7200RPM  
 Other Hardware: None

### Software

Operating System: Windows XP Professional w/ SP2  
 Compiler: Intel C++ Compiler for IA32 version 9.1  
 Build no 20070322Z  
 Auto Parallel: No  
 File System: NTFS  
 System State: Default  
 Base Pointers: 32-bit  
 Peak Pointers: 32-bit  
 Other Software: SmartHeap Library Version 8.0 from  
<http://www.microquill.com/>



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	<b>768</b>	<b>12.7</b>	767	12.7	768	12.7	692	14.1	<b>691</b>	<b>14.1</b>	691	14.1
401.bzip2	1269	7.61	1254	7.70	<b>1255</b>	<b>7.69</b>	1240	7.78	<b>1240</b>	<b>7.78</b>	1242	7.77
403.gcc	1032	7.80	1029	7.82	<b>1030</b>	<b>7.82</b>	<b>988</b>	<b>8.15</b>	987	8.15	990	8.14
429.mcf	<b>698</b>	<b>13.1</b>	697	13.1	698	13.1	<b>698</b>	<b>13.1</b>	697	13.1	698	13.1
445.gobmk	<b>880</b>	<b>11.9</b>	881	11.9	880	11.9	804	13.1	<b>804</b>	<b>13.0</b>	804	13.0
456.hammer	1235	7.55	<b>1235</b>	<b>7.55</b>	1235	7.55	1210	7.71	<b>1210</b>	<b>7.71</b>	1210	7.71
458.sjeng	1075	11.3	<b>1075</b>	<b>11.3</b>	1075	11.3	1011	12.0	1011	12.0	<b>1011</b>	<b>12.0</b>
462.libquantum	1573	13.2	<b>1572</b>	<b>13.2</b>	1572	13.2	1540	13.5	<b>1540</b>	<b>13.5</b>	1541	13.5
464.h264ref	1222	18.1	1223	18.1	<b>1222</b>	<b>18.1</b>	1189	18.6	1190	18.6	<b>1190</b>	<b>18.6</b>
471.omnetpp	<b>705</b>	<b>8.86</b>	705	8.86	705	8.87	<b>642</b>	<b>9.73</b>	642	9.73	642	9.73
473.astar	904	7.77	904	7.77	<b>904</b>	<b>7.77</b>	832	8.43	832	8.43	<b>832</b>	<b>8.43</b>
483.xalancbmk	<b>513</b>	<b>13.4</b>	513	13.4	513	13.4	516	13.4	<b>516</b>	<b>13.4</b>	516	13.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

Tested systems can be used with SC733T-645 case,  
To ensure system stability, a 450W (minimum) ATX power supply [4-pin +12V AND (20 or 24-pin)] is required.  
Product description located as of <http://www.supermicro.com/products/motherboard/Core2Duo/965/PDSBA+.cfm>  
The system bus runs at 800 MHz

## Base Compiler Invocation

C benchmarks:  
icl -Qvc7.1 -Qc99  
  
C++ benchmarks:  
icl -Qvc7.1

## Base Portability Flags

403.gcc: -DSPEC\_CPU\_WIN32  
464.h264ref: -DSPEC\_CPU\_NO\_INTTYPES -DWIN32

## Base Optimization Flags

C benchmarks:  
-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

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## Base Optimization Flags (Continued)

C++ benchmarks:

```
-fast -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE
```

## Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

## Peak Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:

```
icl -Qvc7.1
```

## Peak Portability Flags

```
403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
```

## Peak Optimization Flags

C benchmarks:

```
400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000
shlw32m.lib -link /FORCE:MULTIPLE
```

```
401.bzip2: Same as 400.perlbench
```

```
403.gcc: Same as 400.perlbench
```

```
429.mcf: basepeak = yes
```

```
445.gobmk: Same as 400.perlbench
```

```
456.hmmmer: Same as 400.perlbench
```

```
458.sjeng: Same as 400.perlbench
```

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## Peak Optimization Flags (Continued)

462.libquantum: Same as 400.perlbench

464.h264ref: Same as 400.perlbench

C++ benchmarks:

471.omnetpp: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qcxx\_features  
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

473.astar: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -QxP -O2 -Qipo  
-Qprec-div- -Qunroll14 -Ob2 -Qsfa16 -Qcxx\_features  
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

483.xalancbmk: Same as 471.omnetpp

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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