



SPEC® CINT2006 Result

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Dell Inc.

PowerEdge 2900 (Intel Xeon processor E5310, 1.60 GHz)

SPECint®_rate2006 = Not Run

SPECint_rate_base2006 = 55.4

CPU2006 license: 55

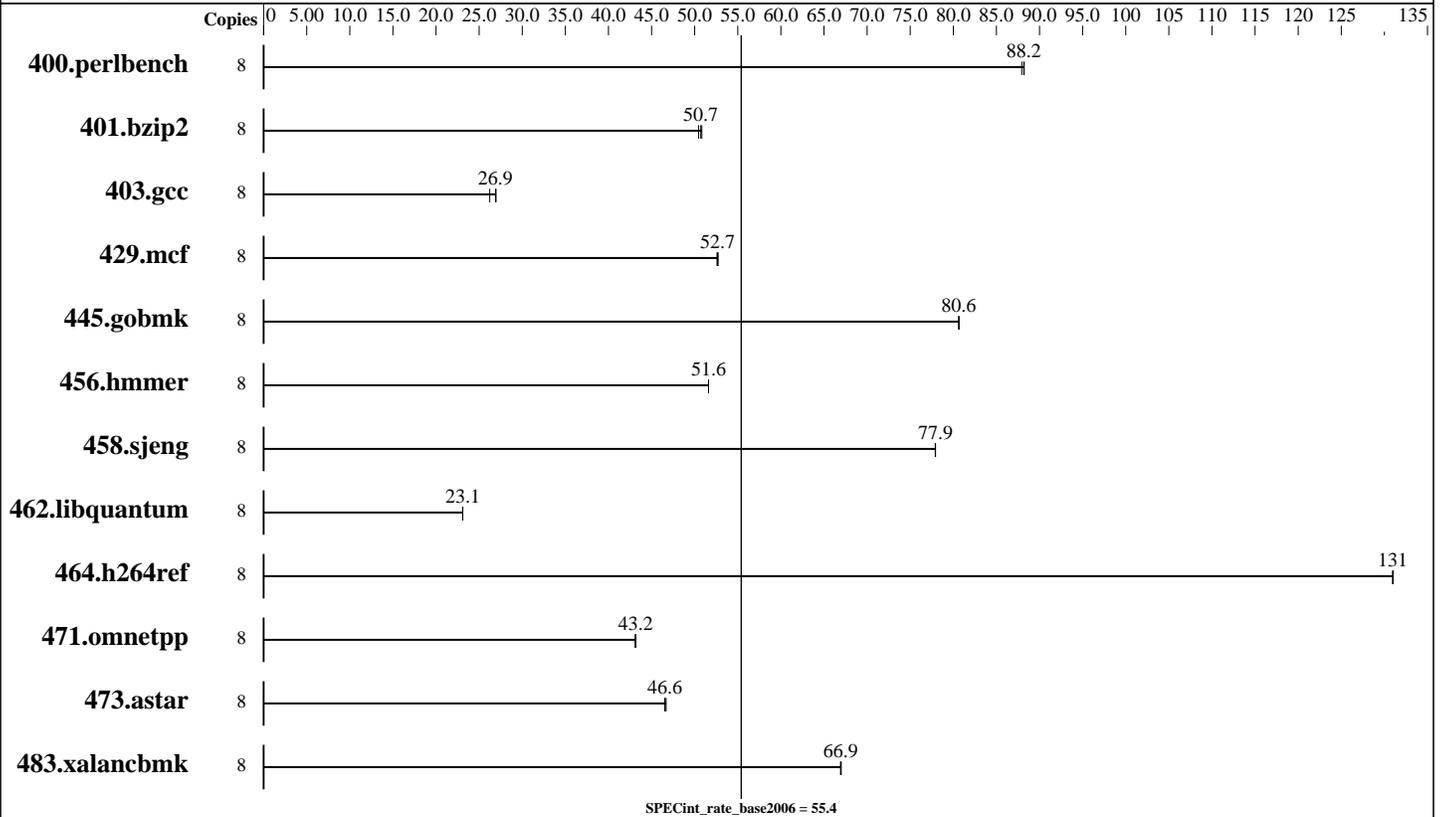
Test sponsor: Dell Inc.

Tested by: Dell Inc.

Test date: Mar-2007

Hardware Availability: Dec-2006

Software Availability: Nov-2006



Hardware

CPU Name: Intel Xeon E5310
 CPU Characteristics: 1066 MHz Bus Speed
 CPU MHz: 1600
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8 x 2 GB 667MHz CL5 DDR2 FB-DIMM SDRAM)
 Disk Subsystem: 1 x 73 GB SAS 15k RPM
 Other Hardware: None

Software

Operating System: Microsoft Windows Server 2003 Enterprise x64 Edition + SP1 (64-bit)
 Compiler: Intel C++ Compiler 9.1 for IA32 (20061103Z) Microsoft Visual Studio 2005
 Auto Parallel: No
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: Not Applicable
 Other Software: MicroQuill SmartHeap Library 8.0



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	889	87.9	886	88.2	886	88.2							
401.bzip2	8	1530	50.5	1519	50.8	1523	50.7							
403.gcc	8	2394	26.9	2389	27.0	2455	26.2							
429.mcf	8	1387	52.6	1384	52.7	1386	52.7							
445.gobmk	8	1041	80.6	1041	80.6	1041	80.7							
456.hammer	8	1446	51.6	1446	51.6	1446	51.6							
458.sjeng	8	1242	77.9	1242	77.9	1243	77.9							
462.libquantum	8	7180	23.1	7175	23.1	7176	23.1							
464.h264ref	8	1352	131	1351	131	1352	131							
471.omnetpp	8	1158	43.2	1158	43.2	1161	43.1							
473.astar	8	1203	46.7	1206	46.6	1207	46.5							
483.xalanbmk	8	825	66.9	824	67.0	825	66.9							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

Hardware Prefetcher disabled in BIOS
Adjacent Cache Line Prefetch disabled in BIOS

Base Compiler Invocation

C benchmarks:
icl -Qc99
C++ benchmarks:
icl

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalanbmk: -Qoption, cpp, --no_wchar_t_keyword

Base Optimization Flags

C benchmarks:
-fast /F512000000 shlw32M.lib -link /FORCE:MULTIPLE

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Base Optimization Flags (Continued)

C++ benchmarks:

```
-fast -Qcxx_features /F512000000 shlW32M.lib
-link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/dell.cpu2006.ic91.flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/dell.cpu2006.ic91.flags.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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