



SPEC® CINT2006 Result

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Hewlett-Packard Company

HP Integrity rx6600
(1.6GHz/24MB Dual-Core Intel Itanium 2)

SPECint_rate2006 = 91.1

SPECint_rate_base2006 = 86.5

CPU2006 license: 03

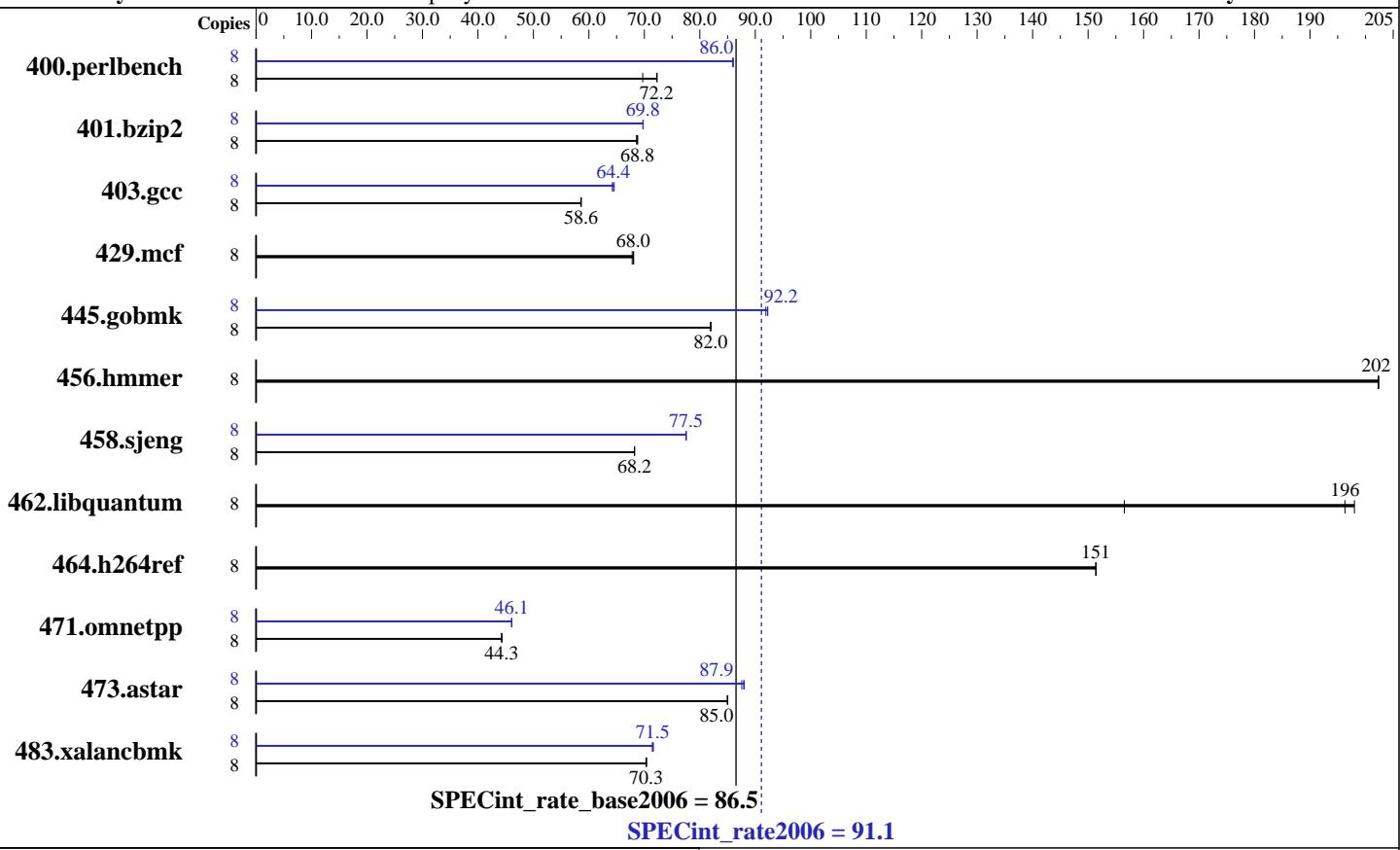
Test sponsor: Hewlett-Packard Company

Tested by: Hewlett-Packard Company

Test date: Oct-2006

Hardware Availability: Sep-2006

Software Availability: Nov-2006



Hardware

CPU Name: Dual-Core Intel Itanium 2 9050
CPU Characteristics: 1.6GHz/24MB, 533MHz FSB
CPU MHz: 1600
FPU: Integrated
CPU(s) enabled: 8 cores, 4 chips, 2 cores/chip
CPU(s) orderable: 1-4 chips
Primary Cache: 16 KB I + 16 KB D on chip per core
Secondary Cache: 1 MB I + 256 KB D on chip per core
L3 Cache: 12 MB I+D on chip per core
Other Cache: None
Memory: 24 GB (24x1GB DIMMs)
Disk Subsystem: 2x73GB 10K RPM SAS (mirrored)
Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux AS release 4 (Update 4)
Compiler: Intel C++ Compiler for Itanium version 9.1 (Build 20060818)
Auto Parallel: No
File System: ext3
System State: Multi-user
Base Pointers: 64-bit
Peak Pointers: 64-bit
Other Software: MicroQuill Smartheap 8.0



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	1121	69.7	1081	72.3	1082	72.2	8	909	86.0	909	86.0	909	86.0
401.bzip2	8	1126	68.6	1122	68.8	1123	68.8	8	1107	69.8	1106	69.8	1107	69.7
403.gcc	8	1100	58.5	1098	58.6	1099	58.6	8	1000	64.4	1003	64.2	997	64.6
429.mcf	8	1073	68.0	1076	67.8	1072	68.1	8	1073	68.0	1076	67.8	1072	68.1
445.gobmk	8	1024	82.0	1024	81.9	1023	82.0	8	913	91.9	910	92.3	910	92.2
456.hammer	8	369	202	369	202	369	202	8	369	202	369	202	369	202
458.sjeng	8	1419	68.2	1419	68.2	1419	68.2	8	1249	77.5	1249	77.5	1248	77.6
462.libquantum	8	837	198	844	196	1059	157	8	837	198	844	196	1059	157
464.h264ref	8	1169	151	1169	151	1169	151	8	1169	151	1169	151	1169	151
471.omnetpp	8	1129	44.3	1129	44.3	1129	44.3	8	1085	46.1	1085	46.1	1085	46.1
473.astar	8	661	85.0	660	85.0	661	85.0	8	639	87.9	638	88.0	641	87.6
483.xalancbmk	8	785	70.3	785	70.3	784	70.4	8	773	71.4	771	71.6	772	71.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

stacksize set to unlimited prior to run

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

```

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_IA64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hammer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64

```

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Base Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-fast -IPF_fp_relaxed -ansi-alias

C++ benchmarks:

-fast -IPF_fp_relaxed -ansi-alias -Wl,-z,muldefs
/opt/SmartHeap_8/lib/libsmartheapC64.a
/opt/SmartHeap_8/lib/libsmartheap64.a

Peak Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof_gen(pass 1) -prof_use(pass 2) -fast -IPF_fp_relaxed
-ansi-alias

401.bzip2: Same as 400.perlbench

403.gcc: Same as 400.perlbench

429.mcf: basepeak = yes

445.gobmk: Same as 400.perlbench

456.hammer: basepeak = yes

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Peak Optimization Flags (Continued)

458.sjeng: Same as 400.perlbench

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

```
471.omnetpp: -prof_gen(pass 1) -prof_use(pass 2) -fast -IPF_fp_relaxed
              -ansi-alias -Wl,-z,muldefs
              /opt/SmartHeap_8/lib/libsmartheapC64.a
              /opt/SmartHeap_8/lib/libsmartheap64.a
```

```
473.astar: -prof_gen(pass 1) -prof_use(pass 2) -fast -IPF_fp_relaxed
              -ansi-alias -inline-factor=150 -Wl,-z,muldefs
              /opt/SmartHeap_8/lib/libsmartheapC64.a
              /opt/SmartHeap_8/lib/libsmartheap64.a
```

483.xalancbmk: Same as 471.omnetpp

The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/IPF_intel91_flags.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/IPF_intel91_flags.xml

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