



# SPEC<sup>®</sup> CFP2006 Result

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## Dell Inc.

PowerEdge R940  
(Intel Xeon Gold 5120, 2.20 GHz)

SPECfp<sup>®</sup>\_rate2006 = 2160

SPECfp\_rate\_base2006 = 2130

CPU2006 license: 55

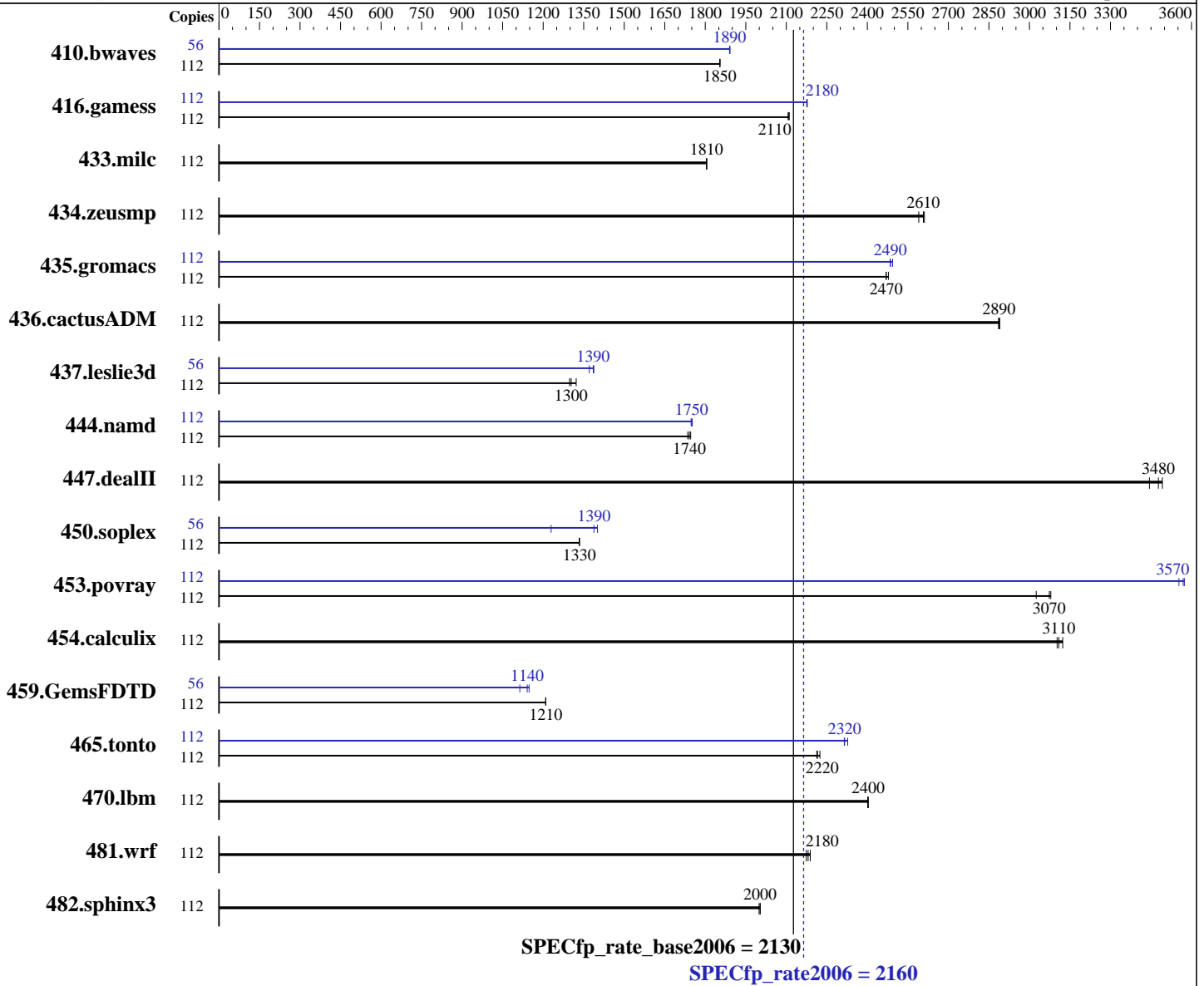
Test sponsor: Dell Inc.

Tested by: Dell Inc.

Test date: Oct-2017

Hardware Availability: Sep-2017

Software Availability: Apr-2017



### Hardware

CPU Name: Intel Xeon Gold 5120  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.20 GHz  
 CPU MHz: 2200  
 FPU: Integrated  
 CPU(s) enabled: 56 cores, 4 chips, 14 cores/chip, 2 threads/core  
 CPU(s) orderable: 2,4 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 1 MB I+D on chip per core

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### Software

Operating System: SUSE Linux Enterprise Server 12 (x86\_64) SP2 4.4.21-69-default  
 Compiler: C/C++: Version 17.0.3.191 of Intel C/C++ Compiler for Linux;  
 Fortran: Version 17.0.3.191 of Intel Fortran Compiler for Linux  
 Auto Parallel: Yes  
 File System: xfs  
 System State: Run level 3 (multi-user)

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L3 Cache: 19.25 MB I+D on chip per chip  
Other Cache: None  
Memory: 768 GB (48 x 16 GB 2Rx8 PC4-2666V-R, running at 2400)  
Disk Subsystem: 1 x 900 GB 15K RPM SAS12  
Other Hardware: None

Base Pointers: 32/64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
410.bwaves	112	<b>821</b>	<b>1850</b>	821	1850	821	1850	56	<b>403</b>	<b>1890</b>	403	1890	402	1890		
416.gamess	112	<b>1039</b>	<b>2110</b>	1041	2110	1039	2110	112	<b>1007</b>	<b>2180</b>	1008	2180	1007	2180		
433.milc	112	570	1810	<b>570</b>	<b>1810</b>	570	1810	112	570	1810	<b>570</b>	<b>1810</b>	570	1810		
434.zeusmp	112	393	2590	<b>391</b>	<b>2610</b>	390	2610	112	393	2590	<b>391</b>	<b>2610</b>	390	2610		
435.gromacs	112	<b>324</b>	<b>2470</b>	323	2480	324	2470	112	<b>322</b>	<b>2490</b>	321	2490	322	2480		
436.cactusADM	112	<b>463</b>	<b>2890</b>	463	2890	464	2890	112	<b>463</b>	<b>2890</b>	463	2890	464	2890		
437.leslie3d	112	811	1300	796	1320	<b>808</b>	<b>1300</b>	56	384	1370	379	1390	<b>380</b>	<b>1390</b>		
444.namd	112	<b>516</b>	<b>1740</b>	518	1740	514	1750	112	513	1750	514	1750	<b>513</b>	<b>1750</b>		
447.dealII	112	367	3490	<b>368</b>	<b>3480</b>	372	3440	112	367	3490	<b>368</b>	<b>3480</b>	372	3440		
450.soplex	112	<b>700</b>	<b>1330</b>	700	1330	700	1330	56	380	1230	<b>336</b>	<b>1390</b>	333	1400		
453.povray	112	197	3030	<b>194</b>	<b>3070</b>	194	3080	112	167	3570	<b>167</b>	<b>3570</b>	168	3550		
454.calculix	112	296	3120	<b>297</b>	<b>3110</b>	298	3100	112	296	3120	<b>297</b>	<b>3110</b>	298	3100		
459.GemsFDTD	112	983	1210	<b>983</b>	<b>1210</b>	982	1210	56	534	1110	<b>521</b>	<b>1140</b>	518	1150		
465.tonto	112	<b>497</b>	<b>2220</b>	498	2210	495	2220	112	474	2330	<b>476</b>	<b>2320</b>	476	2310		
470.lbm	112	641	2400	640	2400	<b>640</b>	<b>2400</b>	112	641	2400	640	2400	<b>640</b>	<b>2400</b>		
481.wrf	112	572	2190	575	2170	<b>574</b>	<b>2180</b>	112	572	2190	575	2170	<b>574</b>	<b>2180</b>		
482.sphinx3	112	1090	2000	1092	2000	<b>1090</b>	<b>2000</b>	112	1090	2000	1092	2000	<b>1090</b>	<b>2000</b>		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Kernel boot parameter: nohz\_full=1-111  
Stack size set to unlimited using "ulimit -s unlimited"



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CPU2006 license: 55  
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Hardware Availability: Sep-2017  
Software Availability: Apr-2017

## Platform Notes

BIOS settings:  
Logical Processor Enabled  
Virtualization Technology Disabled  
Sub NUMA Cluster Enabled  
System Profile set to Custom  
CPU Performance set to Maximum Performance  
C1E Disabled  
C States set to Autonomous  
Uncore Frequency set to Dynamic  
Memory Patrol Scrub Disabled  
Energy Efficiency Policy set to Performance  
CPU Interconnect Bus Link Power Management Disabled  
PCI ASPM L1 Link Power Management Disabled  
Sysinfo program /home/cpu2006/config/sysinfo.rev6993  
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)  
running on linux-ehog Wed Oct 25 05:06:07 2017

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 5120 CPU @ 2.20GHz
 4 "physical id"s (chips)
 112 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores : 14
siblings : 28
physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
physical 2: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
physical 3: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
cache size : 19712 KB
```

```
From /proc/meminfo
MemTotal: 791224272 kB
HugePages_Total: 0
Hugepagesize: 2048 kB
```

```
/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12 SP2
```

```
From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 2
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
```

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## Platform Notes (Continued)

```
os-release:
  NAME="SLES"
  VERSION="12-SP2"
  VERSION_ID="12.2"
  PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
  ID="sles"
  ANSI_COLOR="0;32"
  CPE_NAME="cpe:/o:suse:sles:12:sp2"
```

```
uname -a:
Linux linux-ehog 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016
(9464f67) x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Oct 24 17:33
```

```
SPEC is set to: /home/cpu2006
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda4       xfs   796G  17G  779G   3% /home
```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Dell Inc. 1.1.7 08/10/2017

Memory:  
48x 00AD00B300AD HMA82GR7AFR8N-VK 16 GB 2 rank 2666 MHz, configured at 2400 MHz

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

```
LD_LIBRARY_PATH = "/home/cpu2006/lib/ia32:/home/cpu2006/lib/intel64:/home/cpu2006/sh10.2"
```

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.2

Transparent Huge Pages enabled by default

Filesystem page cache cleared with:

```
shell invocation of 'sync; echo 3 > /proc/sys/vm/drop_caches' prior to run
```

```
runspec command invoked through numactl i.e.:
```

```
numactl --interleave=all runspec <etc>
```

## Base Compiler Invocation

C benchmarks:  
icc -m64

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## Base Compiler Invocation (Continued)

C++ benchmarks:  
icpc -m64

Fortran benchmarks:  
ifort -m64

Benchmarks using both Fortran and C:  
icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32  
-qopt-mem-layout-trans=3

C++ benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32  
-qopt-mem-layout-trans=3

Fortran benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch

Benchmarks using both Fortran and C:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32  
-qopt-mem-layout-trans=3



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## Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32 -L/opt/intel/compilers\_and\_libraries\_2017/linux/lib/ia32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -D\_FILE\_OFFSET\_BITS=64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

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## Peak Optimization Flags (Continued)

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -fno-alias -auto-ilp32  
-qopt-mem-layout-trans=3

447.dealII: basepeak = yes

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -qopt-malloc-options=3  
-qopt-mem-layout-trans=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -unroll4 -qopt-mem-layout-trans=3

### Fortran benchmarks:

410.bwaves: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: Same as 410.bwaves

459.GemsFDTD: Same as 410.bwaves

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -unroll4 -auto -inline-calloc  
-qopt-malloc-options=3

### Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -qopt-prefetch -auto-ilp32  
-qopt-mem-layout-trans=3

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes



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The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Flags-PowerEdge14G-revD.20171221.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Flags-PowerEdge14G-revD.20171221.xml>

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