# SPEC® CFP2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

## Dell Inc.

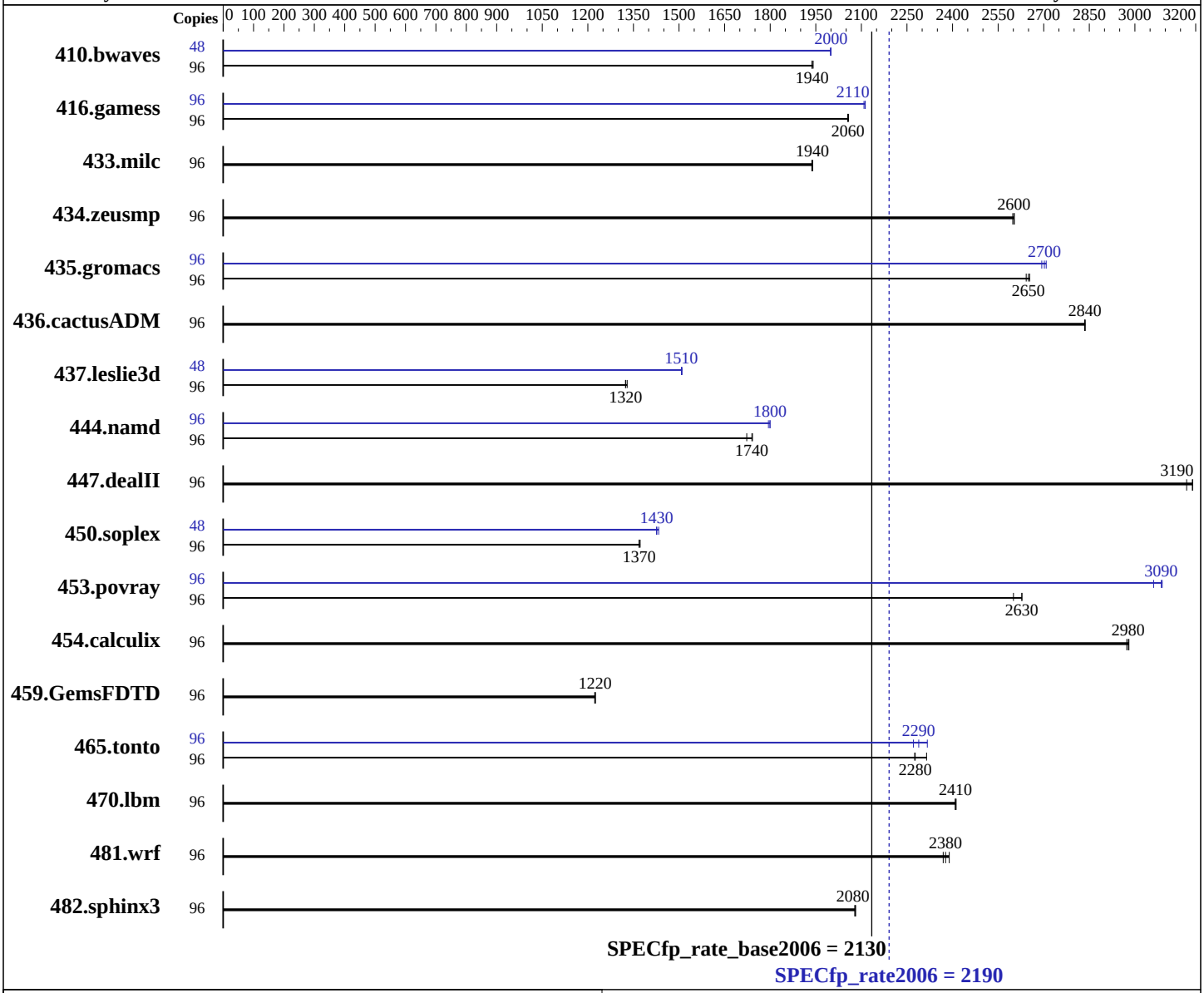
PowerEdge R940 (Intel Xeon Gold 6126, 2.60 GHz)

SPECfp®_rate2006 = 2190

SPECfp_rate_base2006 = 2130

**CPU2006 license:** 55
**Test sponsor:** Dell Inc.
**Tested by:** Dell Inc.

**Test date:** Jun-2017
**Hardware Availability:** Jul-2017
**Software Availability:** Nov-2016




## Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon Gold 6126 |
| CPU Characteristics: | Intel Turbo Boost Technology up to 3.70 GHz |
| CPU MHz: | 2600 |
| FPU: | Integrated |
| CPU(s) enabled: | 48 cores, 4 chips, 12 cores/chip, 2 threads/core |
| CPU(s) orderable: | 2,4 chip |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 1 MB I+D on chip per core |

Continued on next page

## Software

| | |
|---|---|
| Operating System: | SUSE Linux Enterprise Server 12 SP2 4.4.21-69-default |
| Compiler: | C/C++: Version 17.0.3.191 of Intel C/C++ Compiler for Linux; Fortran: Version 17.0.3.191 of Intel Fortran Compiler for Linux |
| Auto Parallel: | Yes |
| File System: | xfs |
| System State: | Run level 3 (multi-user) |

Continued on next page

# SPEC CFP2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

## Dell Inc.

PowerEdge R940 (Intel Xeon Gold 6126, 2.60 GHz)

SPECfp_rate2006 = 2190

SPECfp_rate_base2006 = 2130

**CPU2006 license:** 55
**Test sponsor:** Dell Inc.
**Tested by:** Dell Inc.

**Test date:** Jun-2017
**Hardware Availability:** Jul-2017
**Software Availability:** Nov-2016

L3 Cache: 19.25 MB I+D on chip per chip
Other Cache: None
Memory: 768 GB (48 x 16 GB 2Rx8 PC4-2666V-R)
Disk Subsystem: 1 x 960 GB SATA SSD
Other Hardware: None

Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: None

## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 410.bwaves | 96 | 672 | 1940 | **673** | **1940** | 673 | 1940 | 48 | **326** | **2000** | 326 | 2000 | 326 | 2000 |
| 416.gamess | 96 | 913 | 2060 | 915 | 2050 | **914** | **2060** | 96 | 890 | 2110 | **890** | **2110** | 891 | 2110 |
| 433.milc | 96 | 455 | 1940 | 454 | 1940 | **454** | **1940** | 96 | 455 | 1940 | 454 | 1940 | **454** | **1940** |
| 434.zeusmp | 96 | 336 | 2600 | 336 | 2600 | **336** | **2600** | 96 | 336 | 2600 | 336 | 2600 | **336** | **2600** |
| 435.gromacs | 96 | 258 | 2650 | **259** | **2650** | 259 | 2640 | 96 | 253 | 2710 | **254** | **2700** | 254 | 2690 |
| 436.cactusADM | 96 | 405 | 2830 | **405** | **2840** | 404 | 2840 | 96 | 405 | 2830 | **405** | **2840** | 404 | 2840 |
| 437.leslie3d | 96 | **681** | **1320** | 679 | 1330 | 682 | 1320 | 48 | 299 | 1510 | **299** | **1510** | 299 | 1510 |
| 444.namd | 96 | **443** | **1740** | 447 | 1720 | 442 | 1740 | 96 | 428 | 1800 | 429 | 1790 | **428** | **1800** |
| 447.dealII | 96 | 344 | 3190 | **344** | **3190** | 346 | 3170 | 96 | 344 | 3190 | **344** | **3190** | 346 | 3170 |
| 450.soplex | 96 | **585** | **1370** | 585 | 1370 | 584 | 1370 | 48 | 279 | 1430 | **281** | **1430** | 281 | 1430 |
| 453.povray | 96 | **194** | **2630** | 194 | 2630 | 196 | 2600 | 96 | 167 | 3060 | **165** | **3090** | 165 | 3090 |
| 454.calculix | 96 | 266 | 2980 | **266** | **2980** | 266 | 2970 | 96 | 266 | 2980 | **266** | **2980** | 266 | 2970 |
| 459.GemsFDTD | 96 | 832 | 1220 | 833 | 1220 | **832** | **1220** | 96 | 832 | 1220 | 833 | 1220 | **832** | **1220** |
| 465.tonto | 96 | 408 | 2310 | 415 | 2280 | **415** | **2280** | 96 | 416 | 2270 | 408 | 2320 | **413** | **2290** |
| 470.lbm | 96 | **547** | **2410** | 548 | 2410 | 547 | 2410 | 96 | **547** | **2410** | 548 | 2410 | 547 | 2410 |
| 481.wrf | 96 | **451** | **2380** | 449 | 2390 | 453 | 2370 | 96 | **451** | **2380** | 449 | 2390 | 453 | 2370 |
| 482.sphinx3 | 96 | **900** | **2080** | 899 | 2080 | 900 | 2080 | 96 | **900** | **2080** | 899 | 2080 | 900 | 2080 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

## Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

## Platform Notes

```
BIOS settings:
Sub NUMA Cluster enabled
Virtualization Technology disabled
```

Continued on next page

## Platform Notes (Continued)

```
System Profile set to Custom
CPU Performance set to Maximum Performance
C States set to autonomous
C1E disabled
Uncore Frequency set to Dynamic
Energy Efficiency Policy set to Performance
Memory Patrol Scrub disabled
Logical Processor enabled
CPU Interconnect Bus Link Power Management disabled
PCI ASPM L1 Link Power Management disabled
Sysinfo program /home/cpu2006-1.2_ic17u3/config/sysinfo.rev6993
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)
running on linux-kj6v Fri Jun  2 07:46:18 2017

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) Gold 6126 CPU @ 2.60GHz
      4 "physical id"s (chips)
      96 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 12
      siblings  : 24
      physical 0: cores 0 1 3 4 5 6 8 9 10 11 12 13
      physical 1: cores 0 1 3 4 5 6 8 9 10 11 12 13
      physical 2: cores 0 1 3 4 5 6 8 9 10 11 12 13
      physical 3: cores 0 1 3 4 5 6 8 9 10 11 12 13
  cache size : 19712 KB

From /proc/meminfo
   MemTotal:       791001148 kB
   HugePages_Total:       0
   Hugepagesize:       2048 kB

/usr/bin/lsb_release -d
   SUSE Linux Enterprise Server 12 SP2

From /etc/*release* /etc/*version*
   SuSE-release:
      SUSE Linux Enterprise Server 12 (x86_64)
      VERSION = 12
      PATCHLEVEL = 2
      # This file is deprecated and will be removed in a future service pack or
      release.
      # Please check /etc/os-release for details about this release.
   os-release:
      NAME="SLES"
      VERSION="12-SP2"
```

Continued on next page

## Platform Notes (Continued)

```
        VERSION_ID="12.2"
        PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
        ID="sles"
        ANSI_COLOR="0;32"
        CPE_NAME="cpe:/o:suse:sles:12:sp2"

  uname -a:
     Linux linux-kj6v 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016
     (9464f67) x86_64 x86_64 x86_64 GNU/Linux

  run-level 3 Jun 1 22:06

  SPEC is set to: /home/cpu2006-1.2_ic17u3
     Filesystem     Type  Size  Used Avail Use% Mounted on
     /dev/sda4      xfs   796G  6.6G  789G   1% /home
  Additional information from dmidecode:

     Warning: Use caution when you interpret this section. The 'dmidecode' program
     reads system data which is "intended to allow hardware to be accurately
     determined", but the intent may not be met, as there are frequent changes to
     hardware, firmware, and the "DMTF SMBIOS" standard.

    BIOS Dell Inc. 1.0.0 05/16/2017
    Memory:
     48x 00CE063200CE M393A2K43BB1-CTD 16 GB 2 rank 2666 MHz

  (End of data from sysinfo program)
```

## General Notes

```
 Environment variables set by runspec before the start of the run:
 LD_LIBRARY_PATH = "/home/cpu2006-1.2_ic17u3/lib/ia32:/home/cpu2006-1.2_ic17u3/lib/intel64:/home/cpu2006-1.2_ic17u3/sh10.2"

  Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
  memory using Redhat Enterprise Linux 7.2
  Transparent Huge Pages enabled by default
  Filesystem page cache cleared with:
  shell invocation of 'sync; echo 3 > /proc/sys/vm/drop_caches' prior to run
  runspec command invoked through numactl i.e.:
  numactl --interleave=all runspec <etc>
```

# Base Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Continued on next page

## Base Compiler Invocation (Continued)

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

## Base Optimization Flags

C benchmarks:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3

Fortran benchmarks:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3

## Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks (except as noted below):
icpc -m64

450.soplex: icpc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -D_FILE_OFFSET_BITS=64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

## Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

Continued on next page

## Peak Optimization Flags (Continued)

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2) -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -fno-alias -auto-ilp32 -qopt-mem-layout-trans=3

447.dealII: basepeak = yes

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2) -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -qopt-malloc-options=3 -qopt-mem-layout-trans=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2) -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -unroll4 -qopt-mem-layout-trans=3

Fortran benchmarks:

410.bwaves: -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2) -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: Same as 410.bwaves

459.GemsFDTD: basepeak = yes

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2) -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -unroll4 -auto -inline-calloc -qopt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2) -par-num-threads=1(pass 1) -qopt-prefetch -auto-ilp32 -qopt-mem-layout-trans=3

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html
http://www.spec.org/cpu2006/flags/Dell-Platform-Flags-PowerEdge14G-revB.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml
http://www.spec.org/cpu2006/flags/Dell-Platform-Flags-PowerEdge14G-revB.xml


SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.


For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Wed Jul 12 12:12:25 2017 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 11 July 2017.