SPEC® CFP2006 Result

Cisco Systems

Cisco UCS C220 M4 (Intel Xeon E5-2690 v4, 2.60 GHz)

$SPECfp^{®}2006 =$ 125

$SPECfp\_base2006 =$ 118

CPU2006 license: 9019
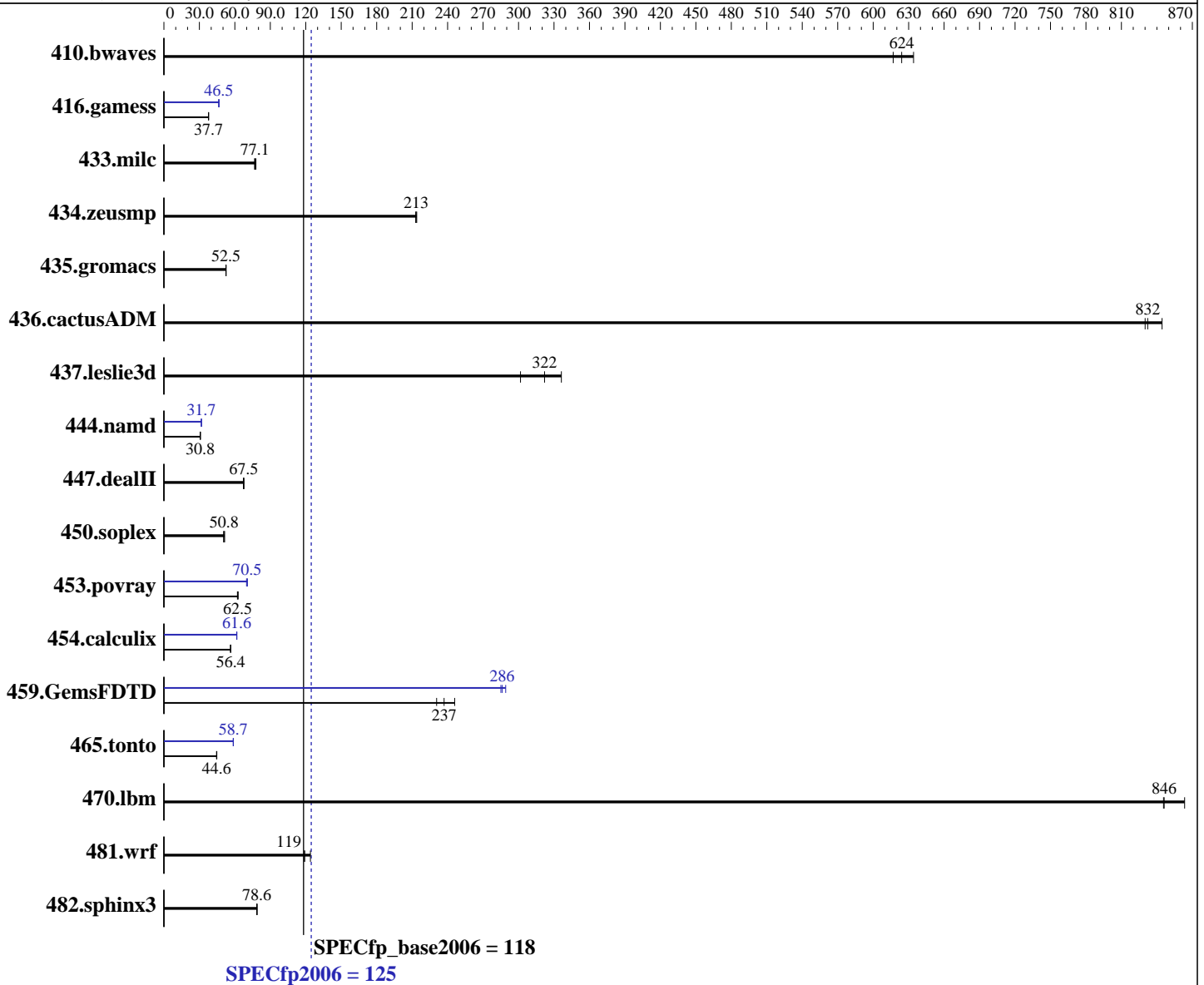**Test sponsor:** Cisco Systems
Tested by: Cisco Systems

Test date: Jul-2016
Hardware Availability: Apr-2016
Software Availability: Dec-2015

410.bwaves 624
416.gamess 46.5 / 37.7
433.milc 77.1
434.zeusmp 213
435.gromacs 52.5
436.cactusADM 832
437.leslie3d 322
444.namd 31.7 / 30.8
447.dealII 67.5
450.soplex 50.8
453.povray 70.5 / 62.5
454.calculix 61.6 / 56.4
459.GemsFDTD 286 / 237
465.tonto 58.7 / 44.6
470.lbm 846
481.wrf 119
482.sphinx3 78.6

SPECfp_base2006 = 118
SPECfp2006 = 125

Hardware

CPU Name: Intel Xeon E5-2690 v4
CPU Characteristics: Intel Turbo Boost Technology up to 3.50 GHz
CPU MHz: 2600
FPU: Integrated
CPU(s) enabled: 28 cores, 2 chips, 14 cores/chip
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: SUSE Linux Enterprise Server 12 SP1 (x86_64) 3.12.49-11-default
Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux; Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
Auto Parallel: Yes
File System: xfs
System State: Run level 3 (multi-user)

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L3 Cache:	35 MB I+D on chip per chip	Base Pointers:	64-bit
Other Cache:	None	Peak Pointers:	32/64-bit
Memory:	256 GB (16 x 16 GB 2Rx4 PC4-2400T-R)	Other Software:	None
Disk Subsystem:	1 x 300 GB SAS HDD, 15K RPM		
Other Hardware:	None		

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	22.0	617	**21.8**	**624**	21.4	634	22.0	617	**21.8**	**624**	21.4	634
416.gamess	517	37.8	520	37.7	**519**	**37.7**	**421**	**46.5**	422	46.4	420	46.6
433.milc	**119**	**77.1**	120	76.7	118	77.8	**119**	**77.1**	120	76.7	118	77.8
434.zeusmp	42.6	214	**42.6**	**213**	42.7	213	42.6	214	**42.6**	**213**	42.7	213
435.gromacs	**136**	**52.5**	136	52.4	136	52.6	**136**	**52.5**	136	52.4	136	52.6
436.cactusADM	**14.4**	**832**	14.4	830	14.2	844	**14.4**	**832**	14.4	830	14.2	844
437.leslie3d	28.0	336	**29.2**	**322**	31.2	302	28.0	336	**29.2**	**322**	31.2	302
444.namd	261	30.8	**261**	**30.8**	260	30.8	253	31.7	253	31.7	**253**	**31.7**
447.dealII	170	67.2	169	67.7	**170**	**67.5**	170	67.2	169	67.7	**170**	**67.5**
450.soplex	165	50.5	162	51.4	**164**	**50.8**	165	50.5	162	51.4	**164**	**50.8**
453.povray	85.6	62.2	**85.1**	**62.5**	84.7	62.8	**75.5**	**70.5**	75.4	70.5	76.1	69.9
454.calculix	**146**	**56.4**	146	56.4	146	56.5	**134**	**61.6**	134	61.6	134	61.7
459.GemsFDTD	**44.8**	**237**	43.1	246	46.0	231	36.7	289	**37.1**	**286**	37.2	285
465.tonto	220	44.8	**221**	**44.6**	221	44.6	168	58.6	**168**	**58.7**	168	58.7
470.lbm	**16.2**	**846**	16.2	846	15.9	863	**16.2**	**846**	16.2	846	15.9	863
481.wrf	90.1	124	94.0	119	**93.8**	**119**	90.1	124	94.0	119	**93.8**	**119**
482.sphinx3	**248**	**78.6**	248	78.5	247	78.9	**248**	**78.6**	248	78.5	247	78.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
BIOS Settings:
Intel Hyper-Threading Technology option set to Disabled
CPU performance set to Enterprise
Power Technology set to Energy Efficient
Energy Performance BIAS setting set to Balanced Performance
Memory RAS configuration set to Maximum Performance
Memory Power Saving Mode set to Disabled
QPI Snoop Mode set to Home Directory Snoop with OSB
 Sysinfo program /home/CISCO_Benchmarks/cpu2006/config/sysinfo.rev6914
 $Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
```

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Test date: Jul-2016

Hardware Availability: Apr-2016

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Platform Notes (Continued)

```
running on linux-f3gd Thu Jul 14 06:30:34 2016

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-2690 v4@ 2.60GHz
      2 "physical id"s (chips)
      28 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 14
      siblings  : 14
      physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
      physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
   cache size : 35840 KB

From /proc/meminfo
   MemTotal:       264368484 kB
   HugePages_Total:        0
   Hugepagesize:        2048 kB

/usr/bin/lsb_release -d
   SUSE Linux Enterprise Server 12 SP1

From /etc/*release* /etc/*version*
   SuSE-release:
      SUSE Linux Enterprise Server 12 (x86_64)
      VERSION = 12
      PATCHLEVEL = 1
      # This file is deprecated and will be removed in a future service pack or
      release.
      # Please check /etc/os-release for details about this release.
   os-release:
      NAME="SLES"
      VERSION="12-SP1"
      VERSION_ID="12.1"
      PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
      ID="sles"
      ANSI_COLOR="0;32"
      CPE_NAME="cpe:/o:suse:sles:12:sp1"

uname -a:
   Linux linux-f3gd 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
   (8d714a0) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Jul 13 11:48

SPEC is set to: /home/CISCO_Benchmarks/cpu2006
   Filesystem     Type   Size   Used Avail Use% Mounted on
```

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Platform Notes (Continued)

```
    /dev/sdb1      xfs   238G   44G  194G  19% /
 Additional information from dmidecode:

    Warning: Use caution when you interpret this section. The 'dmidecode' program
    reads system data which is "intended to allow hardware to be accurately
    determined", but the intent may not be met, as there are frequent changes to
    hardware, firmware, and the "DMTF SMBIOS" standard.

  BIOS Cisco Systems, Inc. C220M4.2.0.9.42.021920161702 02/19/2016
  Memory:
  16x 0xCE00 M393A2G40EB1-CRC 16 GB 2 rank 2400 MHz
  8x NO DIMM NO DIMM

 (End of data from sysinfo program)
```

General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/home/CISCO_Benchmarks/cpu2006/libs/32:/home/CISCO_Benchmarks/cpu2006/libs/64:/home/CISCO_Benchmarks/cpu2006/sh"
OMP_NUM_THREADS = "28"

 Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB
 memory using RedHat EL 7.1
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

Base Compiler Invocation

C benchmarks:
```
    icc   -m64
```

C++ benchmarks:
```
    icpc  -m64
```

Fortran benchmarks:
```
    ifort -m64
```

Benchmarks using both Fortran and C:
```
    icc   -m64 ifort -m64
```

Base Portability Flags

```
    410.bwaves: -DSPEC_CPU_LP64
    416.gamess: -DSPEC_CPU_LP64
      433.milc: -DSPEC_CPU_LP64
```

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Base Portability Flags (Continued)

```
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias
```

C++ benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias
```

Fortran benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
```

Benchmarks using both Fortran and C:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias
```

Peak Compiler Invocation

C benchmarks:
```
icc    -m64
```

C++ benchmarks:
```
icpc   -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc    -m64 ifort -m64
```

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Peak Portability Flags

```
Same as Base Portability Flags
```

Peak Optimization Flags

C benchmarks:

    433.milc: basepeak = yes

    470.lbm: basepeak = yes

    482.sphinx3: basepeak = yes

C++ benchmarks:

    444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
        -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
        -par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias
        -auto-ilp32

    447.dealII: basepeak = yes

    450.soplex: basepeak = yes

    453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
        -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
        -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4
        -ansi-alias

Fortran benchmarks:

    410.bwaves: basepeak = yes

    416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
        -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
        -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
        -inline-level=0 -scalar-rep-

    434.zeusmp: basepeak = yes

    437.leslie3d: basepeak = yes

    459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
        -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
        -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
        -inline-level=0 -opt-prefetch -parallel

    465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
        -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
        -par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc

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Peak Optimization Flags (Continued)

465.tonto (continued):
```
-opt-malloc-options=3 -auto -unroll4
```

Benchmarks using both Fortran and C:

435.gromacs: `basepeak = yes`

436.cactusADM: `basepeak = yes`

454.calculix: `-xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias`

481.wrf: `basepeak = yes`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html
http://www.spec.org/cpu2006/flags/Cisco-Platform-Settings-V1.2-revE.html

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You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Cisco-Platform-Settings-V1.2-revE.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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