



# SPEC® CINT2006 Result

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## ACTION S.A.

ACTINA SOLAR 222 S6 (Intel Xeon E5-2670 v3, 2.30 GHz)

**SPECint®2006 = 60.0**

**SPECint\_base2006 = 57.4**

CPU2006 license: 9008

Test sponsor: ACTION S.A.

Tested by: ACTION S.A.

Test date: Oct-2015

Hardware Availability: Sep-2014

Software Availability: Aug-2015



### Hardware

CPU Name: Intel Xeon E5-2670 v3  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.10 GHz  
 CPU MHz: 2300  
 FPU: Integrated  
 CPU(s) enabled: 24 cores, 2 chips, 12 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 30 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2400P-R, running at 2133 MHz)  
 Disk Subsystem: 1 x 240 GB SATA II SSD  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 7.1 (Maipo)  
 3.10.0-229.11.1.el7.x86\_64  
 Compiler: C/C++: Version 16.0.0.047 of Intel C++ Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.0



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	<b>270</b>	<b>36.2</b>	268	36.4	270	36.2	248	39.5	<b>249</b>	<b>39.2</b>	249	39.2
401.bzip2	437	22.1	<b>438</b>	<b>22.0</b>	439	22.0	<b>430</b>	<b>22.5</b>	430	22.5	430	22.5
403.gcc	240	33.5	<b>241</b>	<b>33.4</b>	242	33.3	239	33.6	238	33.8	<b>239</b>	<b>33.7</b>
429.mcf	<b>168</b>	<b>54.2</b>	169	53.9	167	54.5	168	54.3	170	53.8	<b>169</b>	<b>54.1</b>
445.gobmk	<b>397</b>	<b>26.4</b>	398	26.4	397	26.4	405	25.9	405	25.9	<b>405</b>	<b>25.9</b>
456.hammer	<b>151</b>	<b>61.9</b>	151	61.9	151	61.8	155	60.2	<b>155</b>	<b>60.1</b>	156	60.0
458.sjeng	406	29.8	407	29.8	<b>406</b>	<b>29.8</b>	<b>399</b>	<b>30.4</b>	399	30.4	399	30.3
462.libquantum	3.47	5970	3.67	5640	<b>3.67</b>	<b>5640</b>	3.47	5970	3.67	5640	<b>3.67</b>	<b>5640</b>
464.h264ref	<b>446</b>	<b>49.6</b>	443	49.9	446	49.6	<b>446</b>	<b>49.6</b>	443	49.9	446	49.6
471.omnetpp	184	34.0	182	34.3	<b>183</b>	<b>34.2</b>	128	48.9	<b>128</b>	<b>49.0</b>	127	49.3
473.astar	231	30.4	229	30.7	<b>229</b>	<b>30.7</b>	230	30.5	228	30.7	<b>229</b>	<b>30.6</b>
483.xalancbmk	111	62.3	<b>111</b>	<b>62.3</b>	111	62.2	<b>101</b>	<b>68.4</b>	101	68.3	101	68.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

### Bios Settings

Hyper-Threading (All) = Disable  
Power Technology = Energy Efficient  
Enforce POR = Disabled  
Memory Frequency = 2133  
COD Enable = Disable

### BMC Setting

Fan Mode = Full Speed

Sysinfo program /cpu2006.1.2/config/sysinfo.rev6818

\$Rev: 6818 \$ \$Date:: 2012-07-17 # \$ e86d102572650a6e4d596a3cee98f191  
running on SUT Tue Oct 27 09:47:53 2015

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

Continued on next page



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### Platform Notes (Continued)

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) CPU E5-2670 v3 @ 2.30GHz

2 "physical id"s (chips)

24 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

cpu cores : 12

siblings : 12

physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13

physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13

cache size : 30720 KB

From /proc/meminfo

MemTotal: 263868004 kB

HugePages\_Total: 1

Hugepagesize: 2048 kB

From /etc/\*release\* /etc/\*version\*

os-release:

NAME="Red Hat Enterprise Linux Server"

VERSION="7.0 (Maipo)"

ID="rhel"

ID\_LIKE="fedora"

VERSION\_ID="7.0"

PRETTY\_NAME="Red Hat Enterprise Linux"

ANSI\_COLOR="0;31"

CPE\_NAME="cpe:/o:redhat:enterprise\_linux:7.0:GA:server"

os-release.rpmnew:

NAME="Red Hat Enterprise Linux Server"

VERSION="7.1 (Maipo)"

ID="rhel"

ID\_LIKE="fedora"

VERSION\_ID="7.1"

PRETTY\_NAME="Red Hat Enterprise Linux Server 7.1 (Maipo)"

ANSI\_COLOR="0;31"

CPE\_NAME="cpe:/o:redhat:enterprise\_linux:7.1:GA:server"

redhat-release: Red Hat Enterprise Linux Server release 7.1 (Maipo)

system-release: Red Hat Enterprise Linux Server release 7.1 (Maipo)

system-release-cpe: cpe:/o:redhat:enterprise\_linux:7.1:ga:server

uname -a:

Linux SUT 3.10.0-229.11.1.el7.x86\_64 #5 SMP Mon Sep 14 17:11:19 CEST 2015

x86\_64 x86\_64 x86\_64 GNU/Linux

run-level 3 Oct 27 09:45

SPEC is set to: /cpu2006.1.2

Filesystem Type Size Used Avail Use% Mounted on

/dev/sda1 ext4 212G 37G 165G 19% /

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## Platform Notes (Continued)

Additional information from dmidecode:

BIOS American Megatrends Inc. 1.0c 01/07/2015

Memory:

16x 16 GB

16x Hynix Semiconductor (date:15/28) HMA42GR7AFR4N-UH 16 GB 2133 MHz 2 rank

(End of data from sysinfo program)

dmidecode does not properly detect memory modules

16 modules of 16 GB were used to run the test (256 GB total)

## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/cpu2006.1.2/libs/32:/cpu2006.1.2/libs/64:/cpu2006.1.2/sh"

OMP\_NUM\_THREADS = "24"

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

Binaries compiled on a system with 2x Xeon E5-2650 v3 chips + 256 GB memory using RedHat EL 7.1

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64

401.bzip2: -DSPEC\_CPU\_LP64

403.gcc: -DSPEC\_CPU\_LP64

429.mcf: -DSPEC\_CPU\_LP64

445.gobmk: -DSPEC\_CPU\_LP64

456.hmmmer: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

464.h264ref: -DSPEC\_CPU\_LP64

471.omnetpp: -DSPEC\_CPU\_LP64

473.astar: -DSPEC\_CPU\_LP64

483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX



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## Base Optimization Flags

C benchmarks:

`-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32`

C++ benchmarks:

`-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-Wl,-z,muldefs -L/cpu2006.1.2/sh -lsmartheap64`

## Base Other Flags

C benchmarks:

`403.gcc: -Dalloca=_alloca`

## Peak Compiler Invocation

C benchmarks (except as noted below):

`icc -m64`

`400.perlbench: icc -m32 -L/opt/intel/lib/ia32`

`445.gobmk: icc -m32 -L/opt/intel/lib/ia32`

C++ benchmarks (except as noted below):

`icpc -m32 -L/opt/intel/lib/ia32`

`473.astar: icpc -m64`

## Peak Portability Flags

`400.perlbench: -DSPEC_CPU_LINUX_IA32`

`401.bzip2: -DSPEC_CPU_LP64`

`403.gcc: -DSPEC_CPU_LP64`

`429.mcf: -DSPEC_CPU_LP64`

`456.hmmer: -DSPEC_CPU_LP64`

`458.sjeng: -DSPEC_CPU_LP64`

`462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`

`464.h264ref: -DSPEC_CPU_LP64`

`473.astar: -DSPEC_CPU_LP64`

`483.xalancbmk: -DSPEC_CPU_LINUX`



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## Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -opt-prefetch -ansi-alias

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32  
 -opt-prefetch -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc  
 -opt-malloc-options=3 -auto-ilp32

429.mcf: -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel  
 -opt-prefetch -auto-p32

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
 -ansi-alias

456.hmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32  
 -ansi-alias

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -unroll4

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -opt-ra-region-strategy=block -ansi-alias  
 -Wl,-z,muldefs -L/cpu2006.1.2/sh -lsmarheap

473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
 -auto-p32 -Wl,-z,muldefs -L/cpu2006.1.2/sh -lsmarheap64

483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
 -ansi-alias -Wl,-z,muldefs -L/cpu2006.1.2/sh -lsmarheap

## Peak Other Flags

C benchmarks:

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## Peak Other Flags (Continued)

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/ACTION.SA-Platform-Flags-RevD-aug-2015-For-Supermicro-Platform.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/ACTION.SA-Platform-Flags-RevD-aug-2015-For-Supermicro-Platform.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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