



# SPEC® CINT2006 Result

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## Lenovo Group Limited

Lenovo ThinkServer TD350 (Intel Xeon E5-2683 v3, 2.00 GHz)

**SPECint®2006 = 55.9**

**SPECint\_base2006 = 53.3**

**CPU2006 license:** 9017

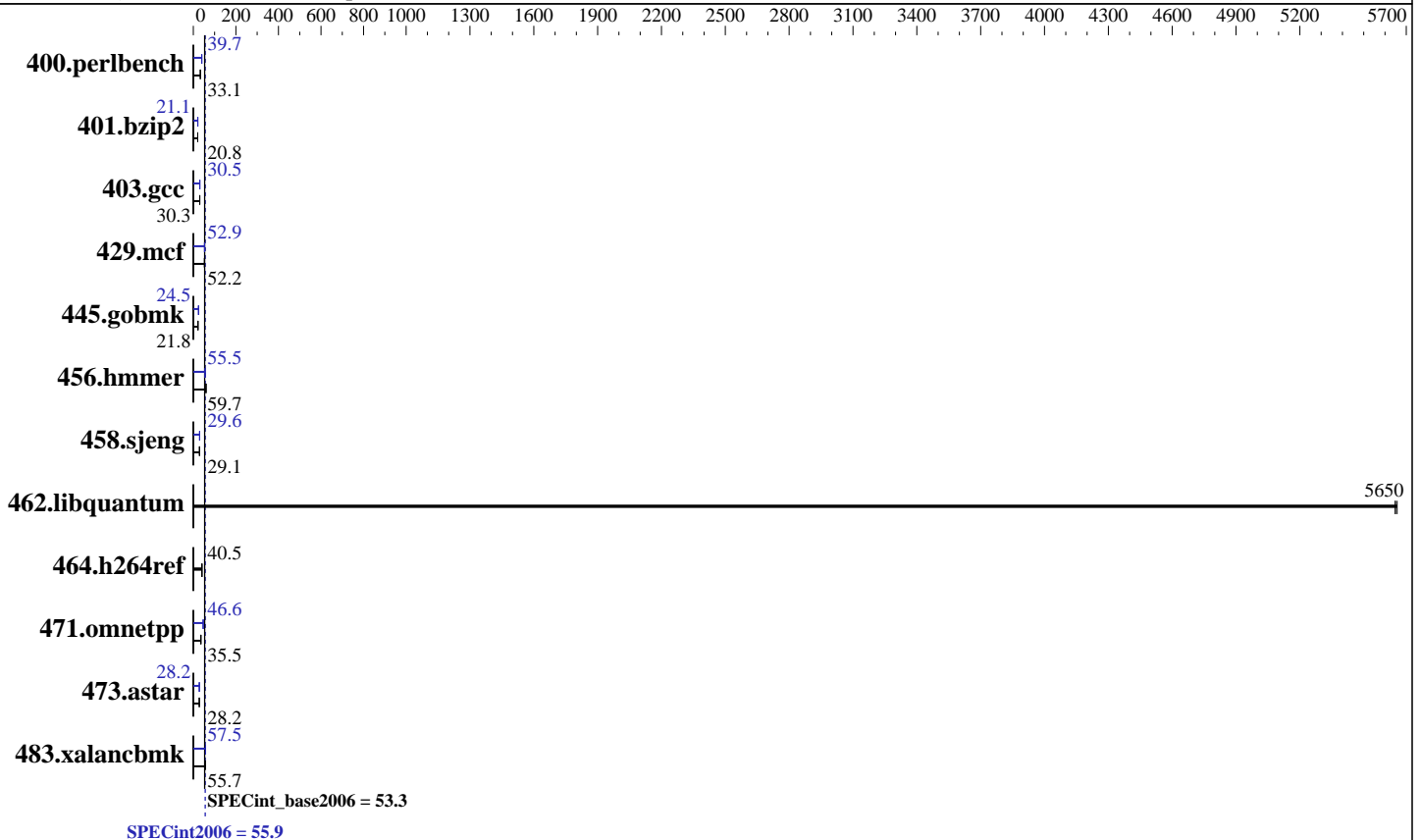
**Test sponsor:** Lenovo Group Limited

**Tested by:** Lenovo Group Limited

**Test date:** Nov-2014

**Hardware Availability:** Sep-2014

**Software Availability:** Jan-2014



### Hardware

**CPU Name:** Intel Xeon E5-2683 v3  
**CPU Characteristics:** Intel Turbo Boost Technology up to 3.00 GHz  
**CPU MHz:** 2000  
**FPU:** Integrated  
**CPU(s) enabled:** 28 cores, 2 chips, 14 cores/chip  
**CPU(s) orderable:** 1,2 chip  
**Primary Cache:** 32 KB I + 32 KB D on chip per core  
**Secondary Cache:** 256 KB I+D on chip per core  
**L3 Cache:** 35 MB I+D on chip per chip  
**Other Cache:** None  
**Memory:** 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)  
**Disk Subsystem:** 1 x 800 GB SATA SSD  
**Other Hardware:** None

### Software

**Operating System:** Red Hat Enterprise Linux Server release 6.5 (Santiago)  
 2.6.32-431.el6.x86\_64  
**Compiler:** C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux  
**Auto Parallel:** Yes  
**File System:** ext4  
**System State:** Run level 3 (multi-user)  
**Base Pointers:** 32/64-bit  
**Peak Pointers:** 32/64-bit  
**Other Software:** Microquill SmartHeap V10.0



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	294	33.2	<b><u>295</u></b>	<b><u>33.1</u></b>	295	33.1	246	39.7	<b><u>246</u></b>	<b><u>39.7</u></b>	246	39.7
401.bzip2	464	20.8	<b><u>465</u></b>	<b><u>20.8</u></b>	466	20.7	458	21.1	<b><u>458</u></b>	<b><u>21.1</u></b>	458	21.1
403.gcc	<b><u>266</u></b>	<b><u>30.3</u></b>	266	30.3	267	30.2	264	30.5	<b><u>264</u></b>	<b><u>30.5</u></b>	264	30.5
429.mcf	172	53.0	<b><u>175</u></b>	<b><u>52.2</u></b>	175	52.2	172	53.1	<b><u>173</u></b>	<b><u>52.9</u></b>	174	52.4
445.gobmk	<b><u>480</u></b>	<b><u>21.8</u></b>	481	21.8	479	21.9	428	24.5	<b><u>428</u></b>	<b><u>24.5</u></b>	429	24.5
456.hammer	158	59.2	156	59.8	<b><u>156</u></b>	<b><u>59.7</u></b>	<b><u>168</u></b>	<b><u>55.5</u></b>	168	55.5	168	55.6
458.sjeng	418	28.9	<b><u>415</u></b>	<b><u>29.1</u></b>	415	29.2	<b><u>409</u></b>	<b><u>29.6</u></b>	409	29.6	409	29.5
462.libquantum	3.66	5660	3.67	5650	<b><u>3.67</u></b>	<b><u>5650</u></b>	3.66	5660	3.67	5650	<b><u>3.67</u></b>	<b><u>5650</u></b>
464.h264ref	547	40.4	<b><u>547</u></b>	<b><u>40.5</u></b>	547	40.5	547	40.4	<b><u>547</u></b>	<b><u>40.5</u></b>	547	40.5
471.omnetpp	<b><u>176</u></b>	<b><u>35.5</u></b>	175	35.7	176	35.5	136	46.0	134	46.7	<b><u>134</u></b>	<b><u>46.6</u></b>
473.astar	249	28.2	251	28.0	<b><u>249</u></b>	<b><u>28.2</u></b>	249	28.2	247	28.5	<b><u>249</u></b>	<b><u>28.2</u></b>
483.xalancbmk	124	55.8	<b><u>124</u></b>	<b><u>55.7</u></b>	124	55.5	120	57.5	120	57.5	<b><u>120</u></b>	<b><u>57.5</u></b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:

Hyper-Threading set to Disabled

Performance Profile set to Custom

C1E Support set to Disabled

Core C3 set to Disabled

Core C6 set to Disabled

Thermal Profile set to Max Performance

Memory Power Savings set to Disabled

Sysinfo program /usr/cpu2006/config/sysinfo.rev6818

\$Rev: 6818 \$ \$Date:: 2012-07-17 #\$ e86d102572650a6e4d596a3cee98f191

running on TD350 Tue Nov 4 17:28:32 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) CPU E5-2683 v3 @ 2.00GHz

2 "physical id"s (chips)

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info@spec.org

<http://www.spec.org/>



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### Platform Notes (Continued)

28 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

```
cpu cores : 14
siblings  : 14
physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
cache size : 35840 KB
```

From /proc/meminfo

```
MemTotal:      264414152 kB
HugePages_Total: 0
Hugepagesize:  2048 kB
```

/usr/bin/lsb\_release -d

Red Hat Enterprise Linux Server release 6.5 (Santiago)

From /etc/\*release\* /etc/\*version\*

```
redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

uname -a:

```
Linux TD350 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54 EST 2013 x86_64
x86_64 x86_64 GNU/Linux
```

run-level 3 Nov 4 17:28

SPEC is set to: /usr/cpu2006

```
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2        ext4  730G   56G  638G   9% /
```

Additional information from dmidecode:

BIOS LENOVO TB5TS110 10/06/2014

Memory:

```
16x 16 GB
2x Hynix Semiconductor HMA42GR7MFR4N-TF 16 GB 2133 MHz 2 rank
14x Hynix Semiconductor HMA42GR7MFR4N-TFTD 16 GB 2133 MHz 2 rank
```

(End of data from sysinfo program)

TD350 support 4 channels and 8 DIMMs per processor, total 8 channels and 16 DIMMs. All 16 DIMM slots installed with 16 GB DIMM for this run.

### General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,scatter"

LD\_LIBRARY\_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64:/usr/cpu2006/sh"

OMP\_NUM\_THREADS = "28"

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## General Notes (Continued)

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled  
runspec command invoked through numactl i.e.:  
numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
403.gcc: -DSPEC\_CPU\_LP64  
429.mcf: -DSPEC\_CPU\_LP64  
445.gobmk: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
464.h264ref: -DSPEC\_CPU\_LP64  
471.omnetpp: -DSPEC\_CPU\_LP64  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-Wl,-z,muldefs -L/sh -lsmarthheap64

## Base Other Flags

C benchmarks:

Continued on next page



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## Base Other Flags (Continued)

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32

445.gobmk: icc -m32

C++ benchmarks (except as noted below):

icpc -m32

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
403.gcc: -DSPEC\_CPU\_LP64  
429.mcf: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
464.h264ref: -DSPEC\_CPU\_LP64  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -ansi-alias

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32  
-opt-prefetch -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc  
-opt-malloc-options=3 -auto-ilp32

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## Peak Optimization Flags (Continued)

429.mcf: -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel  
-opt-prefetch -auto-p32

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias

456.hmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32  
-ansi-alias

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll4

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-ra-region-strategy=block -ansi-alias  
-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-auto-p32 -Wl,-z,muldefs -L/sh -lsmartheap64

483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-ansi-alias -Wl,-z,muldefs -L/sh -lsmartheap

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Settings-V1.2-TD350-revA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Settings-V1.2-TD350-revA.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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