



# SPEC<sup>®</sup> CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Lenovo Group Limited

Lenovo ThinkServer RD340 (Intel Xeon E5-2470 v2, 2.40 GHz)

SPECint<sup>®</sup>2006 = 54.8

SPECint\_base2006 = 50.9

CPU2006 license: 9017

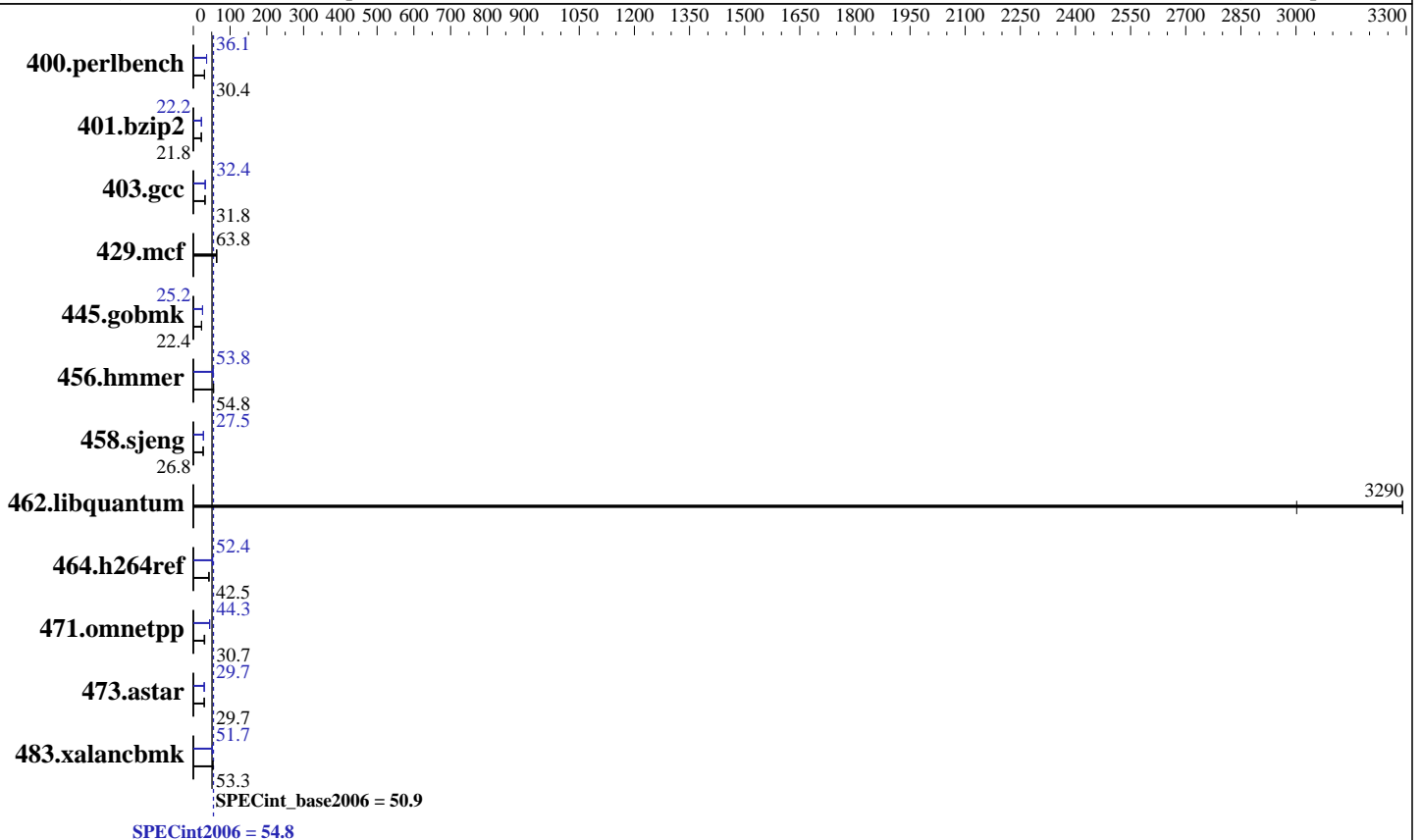
Test sponsor: Lenovo Group Limited

Tested by: Lenovo Group Limited

Test date: Dec-2013

Hardware Availability: Jan-2014

Software Availability: Sep-2013



### Hardware

CPU Name: Intel Xeon E5-2470 v2  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.20 GHz  
 CPU MHz: 2400  
 FPU: Integrated  
 CPU(s) enabled: 20 cores, 2 chips, 10 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 25 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 96 GB (12 x 8 GB 2Rx8 PC3L-12800R-11, ECC)  
 Disk Subsystem: 1 x 400 GB SATA SSD  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 6.4 (Santiago)  
 2.6.32-358.el6.x86\_64  
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4  
 System State: Run level 3 (Full multiuser with network)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.0



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Lenovo Group Limited

Lenovo ThinkServer RD340 (Intel Xeon E5-2470 v2, 2.40 GHz)

SPECint2006 = **54.8**

SPECint\_base2006 = **50.9**

CPU2006 license: 9017

Test sponsor: Lenovo Group Limited

Tested by: Lenovo Group Limited

Test date: Dec-2013

Hardware Availability: Jan-2014

Software Availability: Sep-2013

## Results Table

| Benchmark      | Base              |                    |                    |                    |                   |                    | Peak              |                    |                    |                    |                   |                    |
|----------------|-------------------|--------------------|--------------------|--------------------|-------------------|--------------------|-------------------|--------------------|--------------------|--------------------|-------------------|--------------------|
|                | Seconds           | Ratio              | Seconds            | Ratio              | Seconds           | Ratio              | Seconds           | Ratio              | Seconds            | Ratio              | Seconds           | Ratio              |
| 400.perlbench  | 321               | 30.4               | 322                | 30.4               | <b><u>322</u></b> | <b><u>30.4</u></b> | 270               | 36.2               | 271                | 36.1               | <b><u>270</u></b> | <b><u>36.1</u></b> |
| 401.bzip2      | <b><u>442</u></b> | <b><u>21.8</u></b> | 442                | 21.8               | 443               | 21.8               | 435               | 22.2               | <b><u>436</u></b>  | <b><u>22.2</u></b> | 436               | 22.1               |
| 403.gcc        | 253               | 31.8               | 254                | 31.7               | <b><u>253</u></b> | <b><u>31.8</u></b> | <b><u>248</u></b> | <b><u>32.4</u></b> | 248                | 32.4               | 248               | 32.5               |
| 429.mcf        | 146               | 62.7               | <b><u>143</u></b>  | <b><u>63.8</u></b> | 143               | 63.8               | 146               | 62.7               | <b><u>143</u></b>  | <b><u>63.8</u></b> | 143               | 63.8               |
| 445.gobmk      | 468               | 22.4               | <b><u>468</u></b>  | <b><u>22.4</u></b> | 468               | 22.4               | 416               | 25.2               | 417                | 25.2               | <b><u>417</u></b> | <b><u>25.2</u></b> |
| 456.hammer     | 168               | 55.5               | 171                | 54.7               | <b><u>170</u></b> | <b><u>54.8</u></b> | <b><u>174</u></b> | <b><u>53.8</u></b> | 176                | 53.1               | 173               | 53.8               |
| 458.sjeng      | 452               | 26.8               | 452                | 26.8               | <b><u>452</u></b> | <b><u>26.8</u></b> | <b><u>439</u></b> | <b><u>27.5</u></b> | 440                | 27.5               | 439               | 27.5               |
| 462.libquantum | 6.30              | 3290               | <b><u>6.30</u></b> | <b><u>3290</u></b> | 6.90              | 3000               | 6.30              | 3290               | <b><u>6.30</u></b> | <b><u>3290</u></b> | 6.90              | 3000               |
| 464.h264ref    | 520               | 42.5               | 522                | 42.4               | <b><u>521</u></b> | <b><u>42.5</u></b> | 426               | 52.0               | <b><u>422</u></b>  | <b><u>52.4</u></b> | 422               | 52.4               |
| 471.omnetpp    | 201               | 31.1               | <b><u>203</u></b>  | <b><u>30.7</u></b> | 213               | 29.3               | 139               | 44.8               | <b><u>141</u></b>  | <b><u>44.3</u></b> | 141               | 44.2               |
| 473.astar      | <b><u>237</u></b> | <b><u>29.7</u></b> | 237                | 29.7               | 235               | 29.9               | 237               | 29.6               | <b><u>236</u></b>  | <b><u>29.7</u></b> | 235               | 29.8               |
| 483.xalancbmk  | 129               | 53.4               | 131                | 52.8               | <b><u>130</u></b> | <b><u>53.3</u></b> | 133               | 51.7               | <b><u>133</u></b>  | <b><u>51.7</u></b> | 133               | 51.8               |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

Sysinfo program /usr/cpu2006/config/sysinfo.rev6818  
\$Rev: 6818 \$ \$Date:: 2012-07-17 #\$ e86d102572650a6e4d596a3cee98f191  
running on RD340 Sun Dec 1 21:26:29 2013

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2470 v2 @ 2.40GHz
 2 "physical id"s (chips)
 40 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores : 10
siblings : 20
physical 0: cores 0 1 2 3 4 8 9 10 11 12
physical 1: cores 0 1 2 3 4 8 9 10 11 12
```

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Lenovo Group Limited

**SPECint2006 = 54.8**

Lenovo ThinkServer RD340 (Intel Xeon E5-2470 v2, 2.40 GHz)

**SPECint\_base2006 = 50.9**

**CPU2006 license:** 9017

**Test date:** Dec-2013

**Test sponsor:** Lenovo Group Limited

**Hardware Availability:** Jan-2014

**Tested by:** Lenovo Group Limited

**Software Availability:** Sep-2013

### Platform Notes (Continued)

cache size : 25600 KB

From /proc/meminfo

```
MemTotal:      99027476 kB
HugePages_Total:    0
Hugepagesize:    2048 kB
```

/usr/bin/lsb\_release -d

Red Hat Enterprise Linux Server release 6.4 (Santiago)

From /etc/\*release\* /etc/\*version\*

```
redhat-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

uname -a:

```
Linux RD340 2.6.32-358.el6.x86_64 #1 SMP Tue Jan 29 11:47:41 EST 2013 x86_64
x86_64 x86_64 GNU/Linux
```

run-level 3 Nov 27 23:44

SPEC is set to: /usr/cpu2006

```
Filesystem      Type      Size  Used Avail Use% Mounted on
/dev/sda2        ext4      363G   89G  256G  26% /
```

Additional information from dmidecode:

BIOS LENOVO A0TS10A 08/26/2013

Memory:

```
12x      8 GB
12x Samsung M393B1G73QH0-YK0 8 GB 1600 MHz 2 rank
```

(End of data from sysinfo program)

RD340 support 3 channels and 6 DIMMS per CPU, total 6 channels and 12 DIMMS. All 12 DIMM slots installed with 8 GB DIMM for this run.

### General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,scatter"

LD\_LIBRARY\_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64:/usr/cpu2006/sh"

OMP\_NUM\_THREADS = "20"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

**Lenovo Group Limited**

**SPECint2006 = 54.8**

Lenovo ThinkServer RD340 (Intel Xeon E5-2470 v2, 2.40 GHz)

**SPECint\_base2006 = 50.9**

**CPU2006 license:** 9017

**Test date:** Dec-2013

**Test sponsor:** Lenovo Group Limited

**Hardware Availability:** Jan-2014

**Tested by:** Lenovo Group Limited

**Software Availability:** Sep-2013

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
 401.bzip2: -DSPEC\_CPU\_LP64  
 403.gcc: -DSPEC\_CPU\_LP64  
 429.mcf: -DSPEC\_CPU\_LP64  
 445.gobmk: -DSPEC\_CPU\_LP64  
 456.hmmer: -DSPEC\_CPU\_LP64  
 458.sjeng: -DSPEC\_CPU\_LP64  
 462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
 464.h264ref: -DSPEC\_CPU\_LP64  
 471.omnetpp: -DSPEC\_CPU\_LP64  
 473.astar: -DSPEC\_CPU\_LP64  
 483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -Wl,-z,muldefs  
-L/sh -lsmartheap64

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

**Lenovo Group Limited**

Lenovo ThinkServer RD340 (Intel Xeon E5-2470 v2, 2.40 GHz)

**SPECint2006 = 54.8**

**SPECint\_base2006 = 50.9**

**CPU2006 license:** 9017

**Test sponsor:** Lenovo Group Limited

**Tested by:** Lenovo Group Limited

**Test date:** Dec-2013

**Hardware Availability:** Jan-2014

**Software Availability:** Sep-2013

## Peak Compiler Invocation (Continued)

400.perlbench: `icc -m32`

445.gobmk: `icc -m32`

464.h264ref: `icc -m32`

C++ benchmarks (except as noted below):

`icpc -m32`

473.astar: `icpc -m64`

## Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`

401.bzip2: `-DSPEC_CPU_LP64`

403.gcc: `-DSPEC_CPU_LP64`

429.mcf: `-DSPEC_CPU_LP64`

456.hmmer: `-DSPEC_CPU_LP64`

458.sjeng: `-DSPEC_CPU_LP64`

462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`

473.astar: `-DSPEC_CPU_LP64`

483.xalancbmk: `-DSPEC_CPU_LINUX`

## Peak Optimization Flags

C benchmarks:

400.perlbench: `-xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch -ansi-alias`

401.bzip2: `-xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32 -opt-prefetch -ansi-alias`

403.gcc: `-xAVX -ipo -O3 -no-prec-div -inline-calloc -opt-malloc-options=3 -auto-ilp32`

429.mcf: `basepeak = yes`

445.gobmk: `-xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -ansi-alias`

456.hmmer: `-xAVX -ipo -O3 -no-prec-div -unroll2 -auto-ilp32 -ansi-alias`

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Lenovo Group Limited

Lenovo ThinkServer RD340 (Intel Xeon E5-2470 v2, 2.40 GHz)

**SPECint2006 = 54.8**

**SPECint\_base2006 = 50.9**

**CPU2006 license:** 9017

**Test sponsor:** Lenovo Group Limited

**Tested by:** Lenovo Group Limited

**Test date:** Dec-2013

**Hardware Availability:** Jan-2014

**Software Availability:** Sep-2013

## Peak Optimization Flags (Continued)

458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4

462.libquantum: basepeak = yes

464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-ansi-alias

C++ benchmarks:

471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2)  
-opt-ra-region-strategy=block -ansi-alias  
-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-Wl,-z,muldefs -L/sh -lsmartheap64

483.xalancbmk: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias  
-Wl,-z,muldefs -L/sh -lsmartheap

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Settings-V1.2-revA.20140423.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Settings-V1.2-revA.20140423.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.2.  
Report generated on Thu Jul 24 23:09:27 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 22 April 2014.