



SPEC[®] CFP2006 Result

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Huawei

SPECfp[®]_rate2006 = 1770

Huawei RH5885H v3 (Intel Xeon E7-4890 v2)

SPECfp_rate_base2006 = 1730

CPU2006 license: 3175

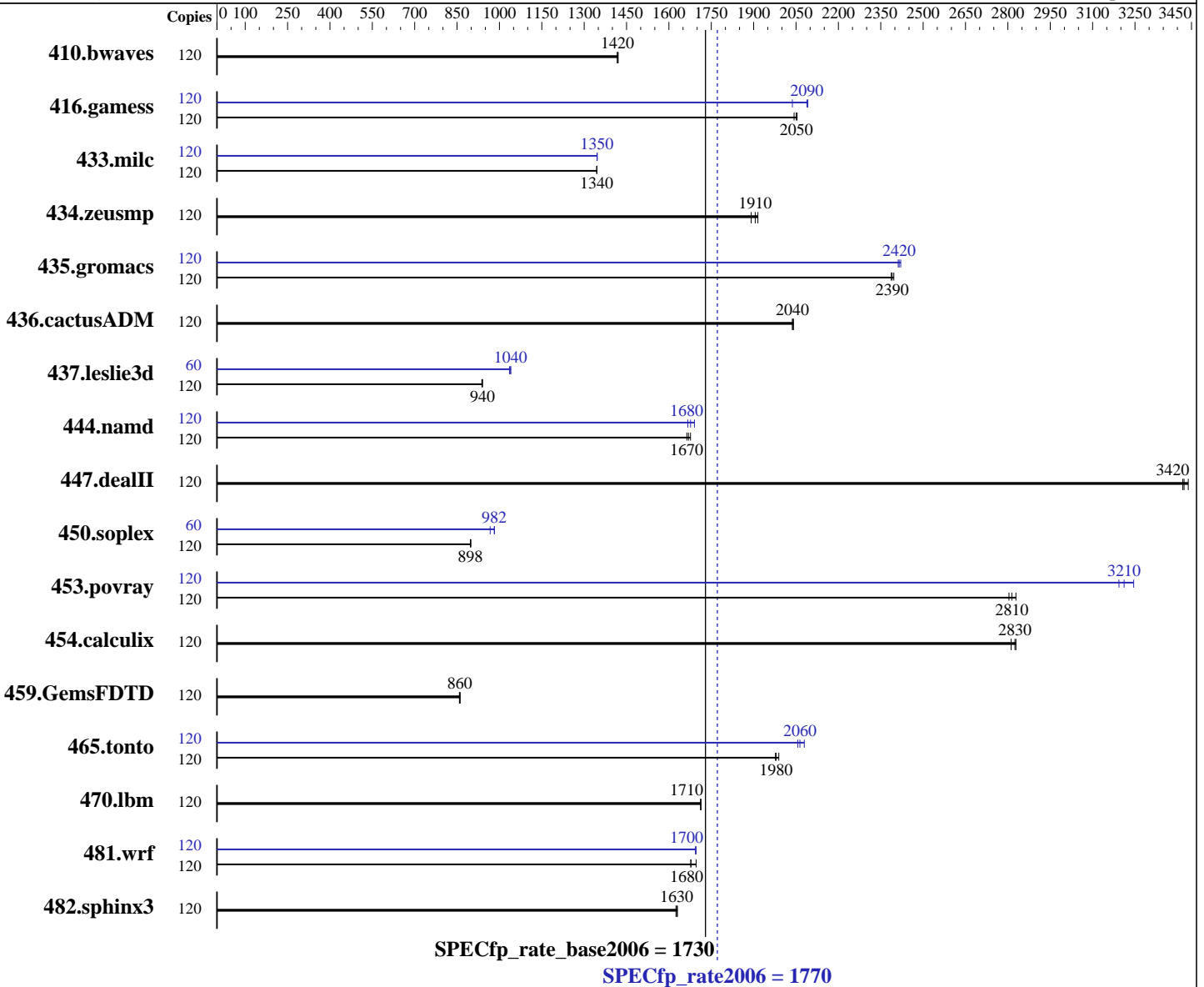
Test date: Mar-2014

Test sponsor: Huawei

Hardware Availability: Feb-2014

Tested by: Huawei

Software Availability: Sep-2013



Hardware

CPU Name: Intel Xeon E7-4890 v2
 CPU Characteristics: Intel Turbo Boost Technology up to 3.40 GHz
 CPU MHz: 2800
 FPU: Integrated
 CPU(s) enabled: 60 cores, 4 chips, 15 cores/chip, 2 threads/core
 CPU(s) orderable: 2,4 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)
 2.6.32-431.el6.x86_64
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;
 Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux
 Auto Parallel: No
 File System: ext4

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L3 Cache: 37.5 MB I+D on chip per chip
Other Cache: None
Memory: 512 GB (64 x 8 GB 2Rx4 PC3-10600R-9, ECC)
Disk Subsystem: 2 x 300 GB SAS, 10K RPM
Other Hardware: None

System State: Run level 3 (multi-user)
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	120	1151	1420	<u>1150</u>	<u>1420</u>	1148	1420	120	1151	1420	<u>1150</u>	<u>1420</u>	1148	1420
416.gamess	120	1150	2040	1144	2050	<u>1146</u>	<u>2050</u>	120	1123	2090	1153	2040	<u>1125</u>	<u>2090</u>
433.milc	120	819	1350	819	1340	<u>819</u>	<u>1340</u>	120	<u>818</u>	<u>1350</u>	818	1350	819	1350
434.zeusmp	120	577	1890	570	1920	<u>573</u>	<u>1910</u>	120	577	1890	570	1920	<u>573</u>	<u>1910</u>
435.gromacs	120	359	2390	<u>358</u>	<u>2390</u>	358	2400	120	<u>355</u>	<u>2420</u>	354	2420	355	2410
436.cactusADM	120	703	2040	704	2040	<u>704</u>	<u>2040</u>	120	703	2040	704	2040	<u>704</u>	<u>2040</u>
437.leslie3d	120	1203	938	<u>1200</u>	<u>940</u>	1200	940	60	542	1040	<u>543</u>	<u>1040</u>	545	1040
444.namd	120	574	1680	<u>577</u>	<u>1670</u>	579	1660	120	577	1670	569	1690	<u>574</u>	<u>1680</u>
447.dealII	120	399	3440	<u>401</u>	<u>3420</u>	401	3420	120	399	3440	<u>401</u>	<u>3420</u>	401	3420
450.soplex	120	1113	899	<u>1115</u>	<u>898</u>	1115	897	60	<u>510</u>	<u>982</u>	509	983	517	967
453.povray	120	228	2800	<u>227</u>	<u>2810</u>	226	2830	120	200	3190	<u>199</u>	<u>3210</u>	197	3250
454.calculix	120	352	2810	<u>350</u>	<u>2830</u>	350	2830	120	352	2810	<u>350</u>	<u>2830</u>	350	2830
459.GemsFDTD	120	<u>1481</u>	<u>860</u>	1484	858	1479	861	120	<u>1481</u>	<u>860</u>	1484	858	1479	861
465.tonto	120	<u>597</u>	<u>1980</u>	597	1980	594	1990	120	574	2060	<u>572</u>	<u>2060</u>	568	2080
470.lbm	120	963	1710	962	1710	<u>963</u>	<u>1710</u>	120	963	1710	962	1710	<u>963</u>	<u>1710</u>
481.wrf	120	<u>799</u>	<u>1680</u>	799	1680	790	1700	120	791	1690	790	1700	<u>791</u>	<u>1700</u>
482.sphinx3	120	1439	1620	<u>1437</u>	<u>1630</u>	1435	1630	120	1439	1620	<u>1437</u>	<u>1630</u>	1435	1630

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:
Set Power Efficiency Mode to Performance
Set Lock_step to disabled

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Platform Notes (Continued)

Sysinfo program /spec/config/sysinfo.rev6818
\$Rev: 6818 \$ \$Date:: 2012-07-17 # \$ e86d102572650a6e4d596a3cee98f191
running on speccpu Sat Mar 22 13:50:38 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name : Intel(R) Xeon(R) CPU E7-4890 v2 @ 2.80GHz
 4 "physical id"s (chips)
 120 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores : 15
  siblings  : 30
  physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14
  physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14
  physical 2: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14
  physical 3: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14
cache size : 38400 KB
```

From /proc/meminfo

```
MemTotal:      529098888 kB
HugePages_Total: 0
Hugepagesize:  2048 kB
```

/usr/bin/lsc_release -d

Red Hat Enterprise Linux Server release 6.5 (Santiago)

From /etc/*release* /etc/*version*

```
redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

uname -a:

```
Linux speccpu 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54 EST 2013
x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Mar 21 23:19

SPEC is set to: /spec

```
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2       ext4  259G  114G  132G  47% /
```

Additional information from dmidecode:

BIOS American Megatrends Inc. BLISV099 02/09/2014

Memory:

```
64x 8 GB
32x NO DIMM NO DIMM
27x Samsung M393B1K70CH0-CH9 8 GB 1333 MHz 2 rank
```

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Platform Notes (Continued)

37x Samsung M393B1K70DH0-CH9 8 GB 1333 MHz 2 rank

(End of data from sysinfo program)

Regarding the sysinfo display about the memory installed, the correct amount of memory is 512 GB and the dmidecode description should have two lines reading as:

27x Samsung M393B1K70CH0-CH9 8 GB 1333 MHz 2 rank

37x Samsung M393B1K70CH0-CH9 8 GB 1333 MHz 2 rank

General Notes

Environment variables set by runspec before the start of the run:

LD_LIBRARY_PATH = "/spec/libs/32:/spec/libs/64:/spec/sh"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled

Filesystem page cache cleared with:

echo 1> /proc/sys/vm/drop_caches

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64

416.gamess: -DSPEC_CPU_LP64

433.milc: -DSPEC_CPU_LP64

434.zeusmp: -DSPEC_CPU_LP64

435.gromacs: -DSPEC_CPU_LP64 -nofor_main

436.cactusADM: -DSPEC_CPU_LP64 -nofor_main

437.leslie3d: -DSPEC_CPU_LP64

444.namd: -DSPEC_CPU_LP64

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Base Portability Flags (Continued)

```

447.deallI: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Base Optimization Flags

C benchmarks:

```

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3

```

C++ benchmarks:

```

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3

```

Fortran benchmarks:

```

-xAVX -ipo -O3 -no-prec-div -opt-prefetch

```

Benchmarks using both Fortran and C:

```

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3

```

Peak Compiler Invocation

C benchmarks:

```

icc -m64

```

C++ benchmarks (except as noted below):

```

icpc -m64

```

```

450.soplex: icpc -m32

```

Fortran benchmarks:

```

ifort -m64

```

Benchmarks using both Fortran and C:

```

icc -m64 ifort -m64

```



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Tested by: Huawei

Software Availability: Sep-2013

Peak Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Peak Optimization Flags

C benchmarks:

```

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -auto-ilp32

```

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

```

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -fno-alias -auto-ilp32

```

447.dealII: basepeak = yes

```

450.soplex: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -opt-malloc-options=3

```

```

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -unroll14 -ansi-alias

```

Fortran benchmarks:

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Peak Optimization Flags (Continued)

410.bwaves: basepeak = yes

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xAVX -ipo -O3 -no-prec-div -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xAVX -ipo -O3 -no-prec-div -auto-ilp32

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.xml>

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