



SPEC[®] CFP2006 Result

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Hewlett-Packard Company

SPECfp[®]_rate2006 = 318

ProLiant BL465c Gen8
(3.20 GHz AMD Opteron 6328)

SPECfp_rate_base2006 = 290

CPU2006 license: 3

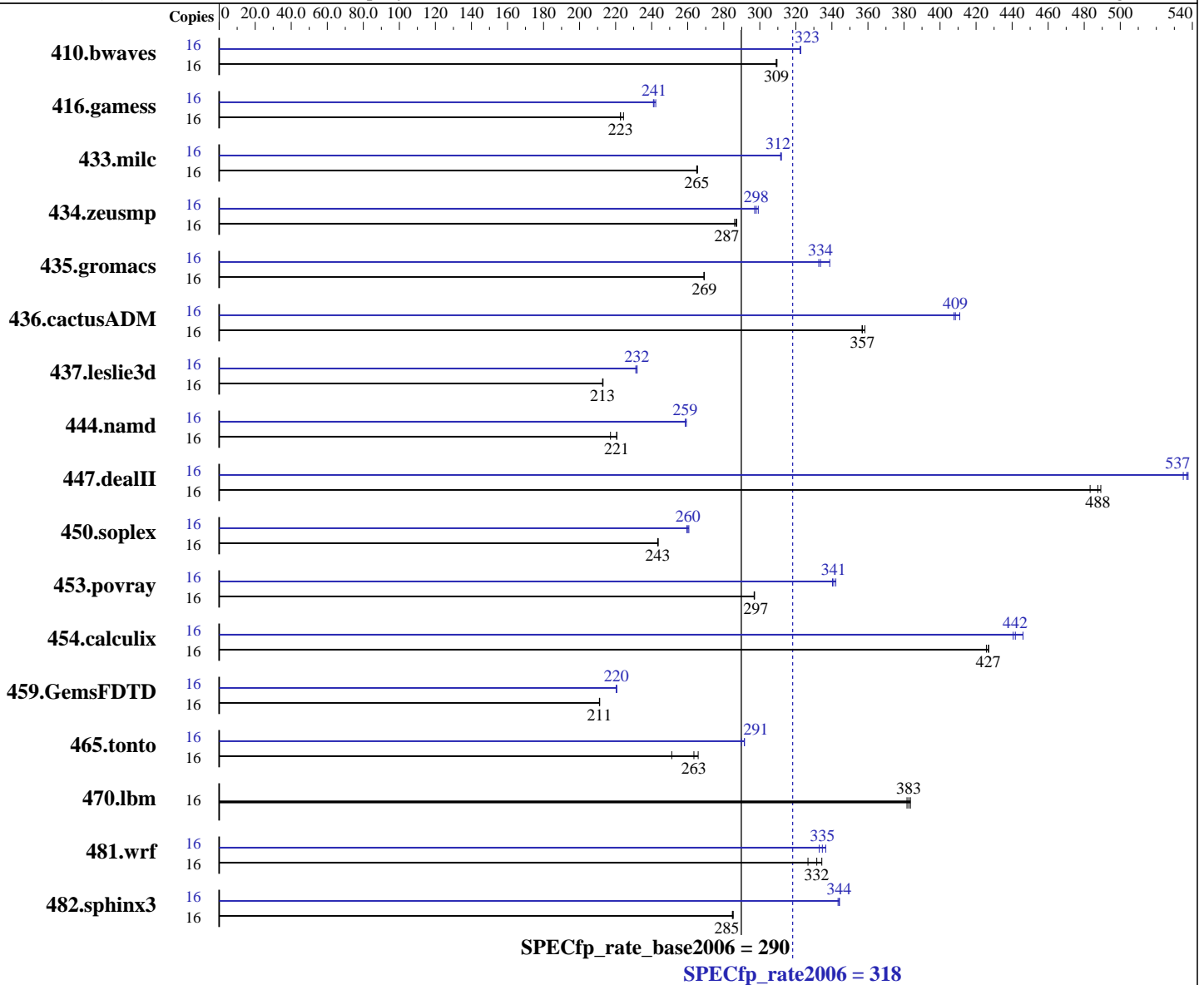
Test date: Dec-2012

Test sponsor: Hewlett-Packard Company

Hardware Availability: Nov-2012

Tested by: Hewlett-Packard Company

Software Availability: Aug-2012



Hardware

CPU Name: AMD Opteron 6328
 CPU Characteristics: AMD Turbo CORE technology up to 3.80 GHz
 CPU MHz: 3200
 FPU: Integrated
 CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip
 CPU(s) orderable: 1,2 chips

Software

Operating System: Red Hat Enterprise Linux Server release 6.3, Kernel 2.6.32-279.el6.x86_64
 Compiler: C/C++/Fortran: Version 4.5.2 of x86 Open64 Compiler Suite (from AMD)
 Auto Parallel: No
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 32/64-bit

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Primary Cache: 256 KB I on chip per chip,
64 KB I shared / 2 cores;
16 KB D on chip per core

Secondary Cache: 8 MB I+D on chip per chip, 2 MB shared / 2 cores

L3 Cache: 16 MB I+D on chip per chip, 8 MB shared / 4 cores

Other Cache: None

Memory: 128 GB (16 x 8 GB 2Rx4 PC3-12800R-11, ECC)

Disk Subsystem: 2 x 200 GB SSD SAS, RAID 1

Other Hardware: None

Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	16	703	309	<u>703</u>	<u>309</u>	702	310	16	<u>674</u>	<u>323</u>	674	323	674	322
416.gamess	16	1396	224	1407	223	<u>1407</u>	<u>223</u>	16	1300	241	<u>1298</u>	<u>241</u>	1293	242
433.milc	16	554	265	<u>554</u>	<u>265</u>	553	266	16	471	312	471	312	<u>471</u>	<u>312</u>
434.zeusmp	16	507	287	<u>508</u>	<u>287</u>	509	286	16	490	297	487	299	<u>489</u>	<u>298</u>
435.gromacs	16	<u>424</u>	<u>269</u>	424	269	425	269	16	343	333	<u>342</u>	<u>334</u>	337	339
436.cactusADM	16	534	358	536	357	<u>536</u>	<u>357</u>	16	<u>468</u>	<u>409</u>	465	411	469	408
437.leslie3d	16	707	213	706	213	<u>707</u>	<u>213</u>	16	<u>649</u>	<u>232</u>	649	232	651	231
444.namd	16	581	221	<u>582</u>	<u>221</u>	591	217	16	496	259	<u>496</u>	<u>259</u>	495	259
447.dealII	16	<u>375</u>	<u>488</u>	374	489	379	483	16	<u>341</u>	<u>537</u>	342	535	340	538
450.soplex	16	<u>548</u>	<u>243</u>	548	244	548	243	16	514	259	<u>513</u>	<u>260</u>	512	261
453.povray	16	287	297	287	297	<u>287</u>	<u>297</u>	16	250	340	249	342	<u>250</u>	<u>341</u>
454.calculix	16	310	426	<u>309</u>	<u>427</u>	309	427	16	<u>299</u>	<u>442</u>	300	441	296	446
459.GemsFDTD	16	<u>804</u>	<u>211</u>	804	211	805	211	16	769	221	<u>770</u>	<u>220</u>	770	220
465.tonto	16	<u>598</u>	<u>263</u>	627	251	592	266	16	540	292	<u>540</u>	<u>291</u>	543	290
470.lbm	16	573	384	576	382	<u>575</u>	<u>383</u>	16	573	384	576	382	<u>575</u>	<u>383</u>
481.wrf	16	<u>539</u>	<u>332</u>	547	327	534	334	16	<u>534</u>	<u>335</u>	537	333	531	337
482.sphinx3	16	1095	285	1093	285	<u>1094</u>	<u>285</u>	16	<u>906</u>	<u>344</u>	906	344	908	343

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size
'ulimit -l 2097152' was used to set environment locked pages in memory limit

Set transparent_hugepage=never as a boot parameter in /boot/grub/menu.lst
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Operating System Notes (Continued)

```
Set vm/nr_hugepages=14336 in /etc/sysctl.conf
mount -t hugetlbfs nodev /mnt/hugepages
```

Platform Notes

BIOS configuration:

```
HP Power Profile set to Maximum Performace
Thermal Configuration set to Maximum Cooling
Collaborative Power Control set to Disable
Minimum Processor Idle Power Cle State to Enabled
Processor Power and Utilization Monitoring set to Disable
```

General Notes

Environment variables set by runspec before the start of the run:

```
HUGETLB_LIMIT = "896"
```

```
LD_LIBRARY_PATH = "/cpu2006/amd1206-rate-libs-revA/32:/cpu2006/amd1206-rate-libs-revA/64"
```

The x86 Open64 Compiler Suite is only available from (and supported by) AMD at <http://developer.amd.com/cpu/open64>

Binaries were compiled on a system with 2x AMD Opteron 6386SE chips + 128GB Memory using RHEL 6.3

Base Compiler Invocation

C benchmarks:
openc

C++ benchmarks:
openCC

Fortran benchmarks:
openf95

Benchmarks using both Fortran and C:
openc openf95

Base Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64
```

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Base Portability Flags (Continued)

```

436.cactusADM: -DSPEC_CPU_LP64 -fno-second-underscore
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LINUX -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LP64
        -fno-second-underscore
482.sphinx3: -DSPEC_CPU_LP64

```

Base Optimization Flags

C benchmarks:

```

-Ofast -OPT:malloc_alg=1 -HP:bd=2m:heap=2m -IPA:plimit=8000
-IPA:small_pu=100 -mso -march=bdver1

```

C++ benchmarks:

```

-Ofast -static -CG:load_exe=0 -OPT:malloc_alg=1 -INLINE:aggressive=on
-HP:bd=2m:heap=2m -D__OPEN64_FAST_SET -march=bdver1

```

Fortran benchmarks:

```

-Ofast -LNO:blocking=off -LNO:simd_peel_align=on -OPT:rsqrt=2
-OPT:unroll_size=256 -HP:bd=2m:heap=2m -mso -march=bdver1

```

Benchmarks using both Fortran and C:

```

-Ofast -OPT:malloc_alg=1 -HP:bd=2m:heap=2m -IPA:plimit=8000
-IPA:small_pu=100 -mso -march=bdver1 -LNO:blocking=off
-LNO:simd_peel_align=on -OPT:rsqrt=2 -OPT:unroll_size=256

```

Peak Compiler Invocation

C benchmarks:

opencc

C++ benchmarks:

openCC

Fortran benchmarks:

openf95

Benchmarks using both Fortran and C:

opencc openf95



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Peak Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64
436.cactusADM: -DSPEC_CPU_LP64 -fno-second-underscore
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LINUX -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LP64
-fno-second-underscore

```

Peak Optimization Flags

C benchmarks:

```

433.milc: -Ofast -CG:movnti=1 -CG:locs_best=on -HP:bdt=2m:heap=2m
-IPA:plimit=7000 -IPA:callee_limit=1200
-OPT:struct_array_copy=2 -OPT:alias=field_sensitive -mso
-march=bdver1

```

470.lbm: basepeak = yes

```

482.sphinx3: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -Ofast
-m32 -IPA:plimit=1000 -OPT:malloc_alg=2 -CG:cmp_peep=on
-CG:p2align=0 -CG:load_exe=1 -CG:dsched=on
-INLINE:aggressive=on -LNO:prefetch=2 -LNO:prefetch_ahead=4
-mso -march=bdver2

```

C++ benchmarks:

```

444.namd: -Ofast -IPA:plimit=3000 -LNO:ignore_feedback=off
-CG:local_sched_alg=0 -CG:load_exe=0 -OPT:unroll_size=256
-fno-exceptions -HP:bdt=2m:heap=2m -LNO:if_select_conv=1
-OPT:alias=disjoint -LNO:psimd_iso_unroll=ON -march=bdver1

```

```

447.dealIII: -Ofast -D__OPEN64_FAST_SET -static -INLINE:aggressive=on
-LNO:opt=1 -LNO:simd=2 -fno-emit-exceptions -m32
-OPT:unroll_times_max=8 -OPT:unroll_size=256
-OPT:unroll_level=2 -HP:bdt=2m:heap=2m -GRA:unspill=on
-CG:cmp_peep=on -CG:movext_icmp=off -TENV:frame_pointer=off
-march=bdver1

```

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Peak Optimization Flags (Continued)

450.soplex: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -O3
-LNO:ignore_feedback=off -INLINE:aggressive=on -OPT:RO=1
-OPT:IEEE_arith=3 -OPT:IEEE_NaN_Inf=off
-OPT:fold_unsigned_relops=on -fno-exceptions -CG:p2align=0
-m32 -mno-fma4 -HP:bdt=2m:heap=2m -WOPT:sib=on
-march=bdver1

453.povray: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -Ofast
-CG:pre_local_sched=off -CG:p2align=0 -CG:p2align_split=on
-CG:dsched=on -INLINE:aggressive=on -HP:bd=2m:heap=2m
-OPT:transform=2 -OPT:alias=disjoint -WOPT:aggcm=0
-march=bdver2

Fortran benchmarks:

410.bwaves: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -Ofast
-OPT:Ofast -OPT:treeheight=on -LNO:blocking=off
-LNO:ignore_feedback=off -LNO:fu=4 -LNO:loop_model_simd=on
-LNO:simd_rm_unity_remainder=on -WOPT:aggstr=0
-HP:bdt=2m:heap=2m -CG:cmp_peep=on -march=bdver1

416.gamess: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -Ofast
-LNO:fu=6 -LNO:blocking=0 -LNO:simd=2 -OPT:ro=3
-OPT:recip=on -CG:local_sched_alg=1 -HP:bdt=2m:heap=2m
-WOPT:sib=on -march=bdver1

434.zeusmp: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -Ofast
-LNO:blocking=off -LNO:interchange=off -IPA:plimit=1500
-HP:bdt=2m:heap=2m -march=bdver1

437.leslie3d: -Ofast -CG:pre_minreg_level=2 -LNO:simd=0 -LNO:fusion=2
-HP:bdt=2m:heap=2m -mso -march=bdver1

459.GemsFDTD: -Ofast -IPA:plimit=1500 -OPT:unroll_size=1024
-OPT:unroll_times_max=16 -LNO:fission=2
-CG:local_sched_alg=2 -HP -march=bdver1

465.tonto: -Ofast -OPT:alias=no_f90_pointer_alias -LNO:blocking=off
-CG:load_exe=1 -CG:local_sched_alg=3 -IPA:plimit=525
-HP:bdt=2m:heap=2m -march=bdver1

Benchmarks using both Fortran and C:

435.gromacs: -Ofast -OPT:rsqrt=2 -HP:bdt=2m:heap=2m
-CG:local_sched_alg=2 -CG:load_exe=3 -GRA:unspill=on
-march=bdver1 -LNO:simd=3

436.cactusADM: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -Ofast
-LNO:blocking=off -LNO:prefetch=2 -LNO:pf2=0
-LNO:prefetch_ahead=4 -HP -CG:locs_shallow_depth=1
-CG:load_exe=0 -CG:dsched=on -WOPT:sib=on -march=bdver1

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Peak Optimization Flags (Continued)

454.calculix: -Ofast -OPT:unroll_size=256 -OPT:alias=disjoint
-GRA:optimize_boundary=on -CG:dsched=on -HP:bdtd=2m:heap=2m
-march=bdver1

481.wrf: -Ofast -LNO:blocking=off -LANG:copyinout=off
-IPA:callee_limit=5000 -GRA:prioritize_by_density=on -HP
-WOPT:sib=on -march=bdver1

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/x86-open64-452-flags-rate-revA-II.html>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-AMD-V1.2-A.20121205.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/x86-open64-452-flags-rate-revA-II.xml>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-AMD-V1.2-A.20121205.xml>

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For other inquiries, please contact webmaster@spec.org.

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