



SPEC® CFP2006 Result

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Dell Inc.

SPECfp®_rate2006 = 866

PowerEdge M820 (Intel Xeon E5-4650L, 2.60 GHz)

SPECfp_rate_base2006 = 841

CPU2006 license: 55

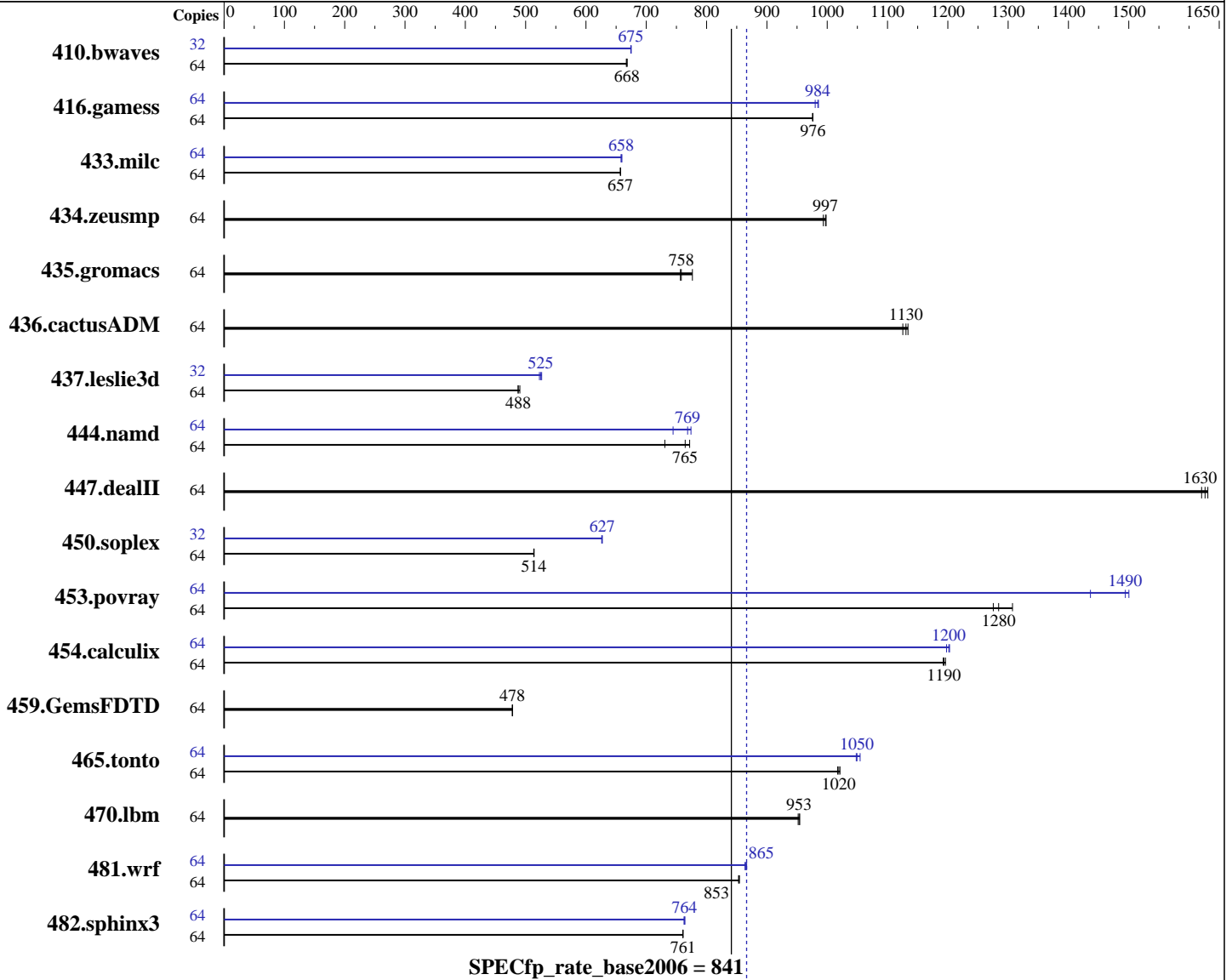
Test date: Apr-2012

Test sponsor: Dell Inc.

Hardware Availability: Aug-2012

Tested by: Dell Inc.

Software Availability: Feb-2012



Hardware

CPU Name: Intel Xeon E5-4650L
 CPU Characteristics: Intel Turbo Boost Technology up to 3.10 GHz
 CPU MHz: 2600
 FPU: Integrated
 CPU(s) enabled: 32 cores, 4 chips, 8 cores/chip, 2 threads/core
 CPU(s) orderable: 2,4 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: SUSE Linux Enterprise Server 11 SP2 (x86_64) 3.0.13-0.27-default
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux;
 Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux
 Auto Parallel: No
 File System: ext3
 System State: Run level 3 (multi-user)

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L3 Cache: 20 MB I+D on chip per chip
 Other Cache: None
 Memory: 256 GB (32 x 8 GB 2Rx4 PC3-12800R-11, ECC)
 Disk Subsystem: 1 x 900 GB 10000 RPM SAS
 Other Hardware: None

Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	64	<u>1302</u>	<u>668</u>	1304	667	1301	668	32	644	675	645	674	<u>645</u>	<u>675</u>
416.gamess	64	1283	976	1284	976	<u>1284</u>	<u>976</u>	64	1272	986	<u>1273</u>	<u>984</u>	1279	980
433.milc	64	894	658	<u>894</u>	<u>657</u>	894	657	64	891	660	<u>892</u>	<u>658</u>	892	658
434.zeusmp	64	<u>584</u>	<u>997</u>	583	998	586	994	64	<u>584</u>	<u>997</u>	583	998	586	994
435.gromacs	64	588	776	604	757	<u>603</u>	<u>758</u>	64	588	776	604	757	<u>603</u>	<u>758</u>
436.cactusADM	64	<u>676</u>	<u>1130</u>	680	1130	674	1130	64	<u>676</u>	<u>1130</u>	680	1130	674	1130
437.leslie3d	64	1234	488	1227	490	<u>1234</u>	<u>488</u>	32	571	527	575	523	<u>573</u>	<u>525</u>
444.namd	64	665	772	<u>671</u>	<u>765</u>	702	731	64	663	774	689	744	<u>668</u>	<u>769</u>
447.dealII	64	449	1630	452	1620	<u>450</u>	<u>1630</u>	64	449	1630	452	1620	<u>450</u>	<u>1630</u>
450.soplex	64	<u>1039</u>	<u>514</u>	1038	514	1040	513	32	<u>426</u>	<u>627</u>	426	626	426	627
453.povray	64	<u>265</u>	<u>1280</u>	260	1310	267	1280	64	227	1500	237	1440	<u>228</u>	<u>1490</u>
454.calculix	64	<u>442</u>	<u>1190</u>	441	1200	443	1190	64	<u>439</u>	<u>1200</u>	441	1200	439	1200
459.GemsFDTD	64	<u>1420</u>	<u>478</u>	1421	478	1420	478	64	<u>1420</u>	<u>478</u>	1421	478	1420	478
465.tonto	64	<u>618</u>	<u>1020</u>	617	1020	619	1020	64	597	1050	601	1050	<u>600</u>	<u>1050</u>
470.lbm	64	924	952	<u>922</u>	<u>953</u>	921	955	64	924	952	<u>922</u>	<u>953</u>	921	955
481.wrf	64	838	853	837	855	<u>838</u>	<u>853</u>	64	828	864	<u>826</u>	<u>865</u>	825	867
482.sphinx3	64	<u>1639</u>	<u>761</u>	1640	760	1639	761	64	1633	764	<u>1634</u>	<u>764</u>	1636	763

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

CPU Power Management set to Maximum Performance
Memory Frequency set to Maximum Performance
Turbo Boost set to Enabled

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Platform Notes (Continued)

C States/C1E set to Enabled
 Sysinfo program /root/CPU2006-1.2/config/sysinfo.rev6800
 \$Rev: 6800 \$ \$Date:: 2011-10-11 # \$ 6f2ebdff5032aaa42e583f96b07f99d3
 running on linux-0j3r Wed Apr 25 20:00:10 2012

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
  model name : Intel(R) Xeon(R) CPU E5-4650L 0 @ 2.60GHz
    4 "physical id"s (chips)
    64 "processors"
  cores, siblings (Caution: counting these is hw and system dependent. The
  following excerpts from /proc/cpuinfo might not be reliable. Use with
  caution.)
    cpu cores : 8
    siblings  : 16
    physical 0: cores 0 1 2 3 4 5 6 7
    physical 1: cores 0 1 2 3 4 5 6 7
    physical 2: cores 0 1 2 3 4 5 6 7
    physical 3: cores 0 1 2 3 4 5 6 7
  cache size : 20480 KB
```

```
From /proc/meminfo
  MemTotal:      264501520 kB
  HugePages_Total:      0
  Hugepagesize:    2048 kB
```

```
/usr/bin/lsb_release -d
  SUSE Linux Enterprise Server 11 (x86_64)
```

```
From /etc/*release* /etc/*version*
  SuSE-release:
    SUSE Linux Enterprise Server 11 (x86_64)
    VERSION = 11
    PATCHLEVEL = 2
```

```
uname -a:
  Linux linux-0j3r 3.0.13-0.27-default #1 SMP Wed Feb 15 13:33:49 UTC 2012
  (d73692b) x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Apr 25 07:02 last=5

```
SPEC is set to: /root/CPU2006-1.2
  Filesystem      Type      Size      Used Avail Use% Mounted on
  /dev/sdal       ext3     886G     145G   696G   18% /
```

Additional information from dmidecode:

(End of data from sysinfo program)



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General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/root/CPU2006-1.2/libs/32:/root/CPU2006-1.2/libs/64"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1> /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.deallI: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64



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Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.deallI: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32
 -opt-mem-layout-trans=3

470.lbm: basepeak = yes

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
 -auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -prof-use(pass 2) -static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
 -inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: -xAVX -ipo -O3 -no-prec-div -static -opt-prefetch

459.GemsFDTD: basepeak = yes

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Peak Optimization Flags (Continued)

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -static -auto-ilp32
-opt-mem-layout-trans=3

481.wrf: Same as 454.calculix

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revA.20120410.00.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revA.20120410.00.xml>

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Tested with SPEC CPU2006 v1.2.
Report generated on Thu Jul 24 11:18:16 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 15 August 2012.