



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Acer Incorporated

Gateway GW2000h-GW170h F1 (Intel Xeon X5670, 2.93GHz)

SPECint®2006 = 39.4

SPECint_base2006 = 36.6

CPU2006 license: 97

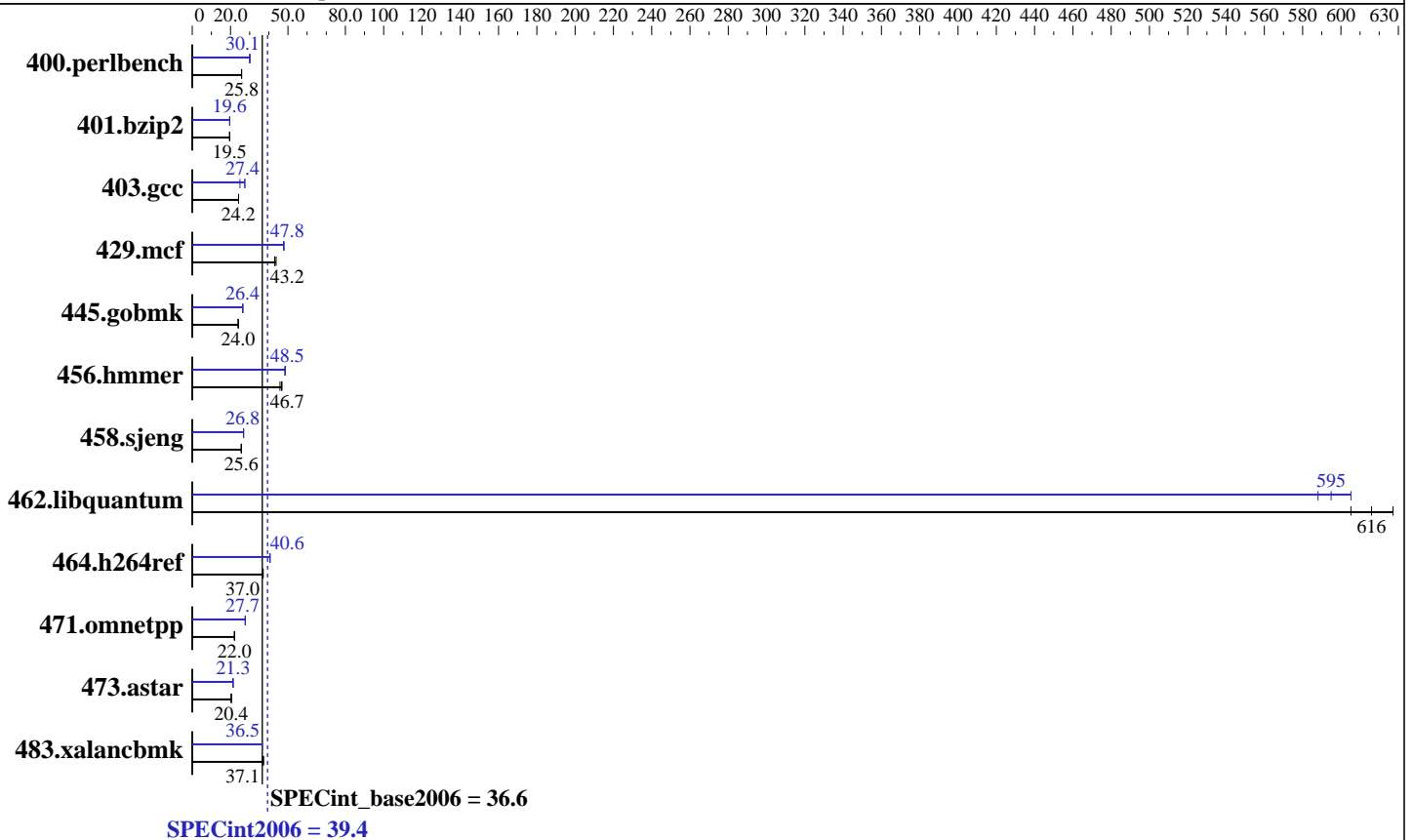
Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Jul-2010

Hardware Availability: Aug-2010

Software Availability: Jan-2010



Hardware

CPU Name: Intel Xeon X5670
 CPU Characteristics: Intel Turbo Boost Technology up to 3.33 GHz
 CPU MHz: 2933
 FPU: Integrated
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 12 MB I+D on chip per chip
 Other Cache: None
 Memory: 48 GB (12 x 4 GB 2Rx8 PC3-10600R-9, ECC)
 Disk Subsystem: 1 x 1000 GB SATA II, 7200 RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64)
 Kernel 2.6.27.19-5-default
 Compiler: Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1
 Build 20091130 Package ID: l_cproc_p_11.1.064
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Acer Incorporated

Gateway GW2000h-GW170h F1 (Intel Xeon X5670, 2.93GHz)

SPECint2006 = 39.4

SPECint_base2006 = 36.6

CPU2006 license: 97
Test sponsor: Acer Incorporated
Tested by: Acer Incorporated

Test date: Jul-2010
Hardware Availability: Aug-2010
Software Availability: Jan-2010

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	380	25.7	<u>379</u>	<u>25.8</u>	378	25.8	325	30.1	325	30.1	<u>325</u>	<u>30.1</u>
401.bzip2	494	19.5	493	19.6	<u>494</u>	<u>19.5</u>	493	19.6	<u>493</u>	<u>19.6</u>	493	19.6
403.gcc	333	24.2	<u>333</u>	<u>24.2</u>	333	24.2	293	27.5	323	24.9	<u>293</u>	<u>27.4</u>
429.mcf	<u>211</u>	<u>43.2</u>	212	43.1	208	43.8	<u>191</u>	<u>47.8</u>	190	47.9	191	47.8
445.gobmk	436	24.0	437	24.0	<u>437</u>	<u>24.0</u>	<u>397</u>	<u>26.4</u>	397	26.4	397	26.4
456.hammer	<u>200</u>	<u>46.7</u>	204	45.8	199	46.9	192	48.5	192	48.5	<u>192</u>	<u>48.5</u>
458.sjeng	471	25.7	473	25.6	<u>472</u>	<u>25.6</u>	<u>452</u>	<u>26.8</u>	450	26.9	452	26.8
462.libquantum	34.2	605	<u>33.6</u>	<u>616</u>	33.0	627	<u>34.8</u>	<u>595</u>	35.2	588	34.2	605
464.h264ref	597	37.1	599	36.9	<u>598</u>	<u>37.0</u>	<u>545</u>	<u>40.6</u>	544	40.7	546	40.5
471.omnetpp	283	22.1	<u>283</u>	<u>22.0</u>	284	22.0	226	27.7	<u>226</u>	<u>27.7</u>	225	27.7
473.astar	345	20.3	<u>344</u>	<u>20.4</u>	343	20.5	<u>329</u>	<u>21.3</u>	329	21.3	329	21.3
483.xalancbmk	185	37.3	188	36.8	<u>186</u>	<u>37.1</u>	<u>189</u>	<u>36.5</u>	188	36.6	189	36.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stack size to unlimited prior to run
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter

General Notes

Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502

The Acer AW2000h-AW170h F1, Gateway GW2000h-GW170h F1, Acer AW2000ht-AW170ht F1 and Gateway GW2000ht-GW170ht F1 are electronically equivalent.
This result was measured on GW2000ht-GW170ht F1.

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Acer Incorporated

SPECint2006 = 39.4

Gateway GW2000h-GW170h F1 (Intel Xeon X5670, 2.93GHz)

SPECint_base2006 = 36.6

CPU2006 license: 97

Test date: Jul-2010

Test sponsor: Acer Incorporated

Hardware Availability: Aug-2010

Tested by: Acer Incorporated

Software Availability: Jan-2010

Base Portability Flags

```

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

```

Base Optimization Flags

C benchmarks:

`-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch`

C++ benchmarks:

`-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64`

Base Other Flags

C benchmarks:

`403.gcc: -Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):

`icc -m64`

`400.perlbench: icc -m32`

`429.mcf: icc -m32`

`445.gobmk: icc -m32`

`464.h264ref: icc -m32`

C++ benchmarks (except as noted below):

`icpc -m32`

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Acer Incorporated

Gateway GW2000h-GW170h F1 (Intel Xeon X5670, 2.93GHz)

SPECint2006 = 39.4

SPECint_base2006 = 36.6

CPU2006 license: 97

Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Jul-2010

Hardware Availability: Aug-2010

Software Availability: Jan-2010

Peak Compiler Invocation (Continued)

473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
 401.bzip2: -DSPEC_CPU_LP64
 403.gcc: -DSPEC_CPU_LP64
 456.hmmer: -DSPEC_CPU_LP64
 458.sjeng: -DSPEC_CPU_LP64
 462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
 473.astar: -DSPEC_CPU_LP64
 483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div -static(pass 2) -prof-use(pass 2)
 -auto-ilp32 -opt-prefetch -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
 -opt-malloc-options=3 -auto-ilp32

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
 -ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
 -ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -unroll4

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -parallel
 -opt-prefetch -par-schedule-static=32768 -ansi-alias

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -unroll2 -ansi-alias

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Acer Incorporated

SPECint2006 = 39.4

Gateway GW2000h-GW170h F1 (Intel Xeon X5670, 2.93GHz)

SPECint_base2006 = 36.6

CPU2006 license: 97

Test date: Jul-2010

Test sponsor: Acer Incorporated

Hardware Availability: Aug-2010

Tested by: Acer Incorporated

Software Availability: Jan-2010

Peak Optimization Flags (Continued)

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64

483.xalancbmk: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch
-Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revG.20101027.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revG.20101027.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 14:58:15 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 3 December 2010.