



# SPEC® CINT2006 Result

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## Acer Incorporated

**SPECint®2006 = 36.1**

## Acer AR160 F1 (Intel Xeon X5570)

**SPECint\_base2006 = 32.0**

CPU2006 license: 97

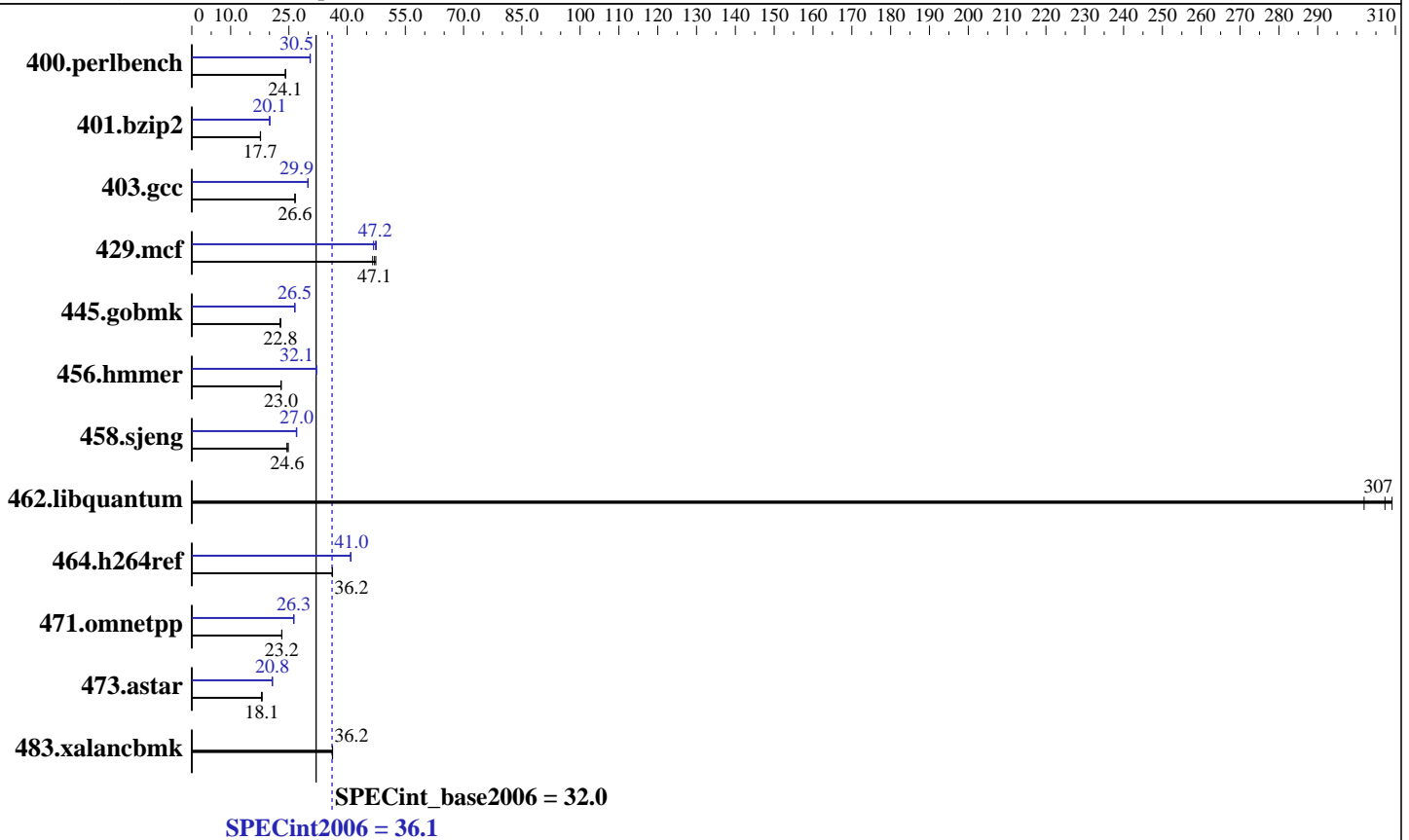
Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Dec-2009

Hardware Availability: Jan-2010

Software Availability: Feb-2009



### Hardware

CPU Name: Intel Xeon X5570  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.33 GHz  
 CPU MHz: 2933  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1, 2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 8 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 24 GB (6 x 4 GB DDR3-1333 RDIMM)  
 Disk Subsystem: 160 GB SATA II, 5400 RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64)  
 Kernel 2.6.27.19-5  
 Compiler: Intel C++ Compiler Professional 11.0 for Linux  
 Build 20090131 Package ID: l\_cproc\_p\_11.0.080  
 Auto Parallel: Yes  
 File System: ReiserFS  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1  
 Binutils 2.18.50.0.7.20080502



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	405	24.1	<b>405</b>	<b>24.1</b>	404	24.2	<b>321</b>	<b>30.5</b>	321	30.4	320	30.5
401.bzip2	545	17.7	<b>546</b>	<b>17.7</b>	547	17.7	481	20.1	<b>481</b>	<b>20.1</b>	481	20.1
403.gcc	303	26.6	<b>303</b>	<b>26.6</b>	303	26.6	<b>269</b>	<b>29.9</b>	269	29.9	269	29.9
429.mcf	196	46.6	192	47.4	<b>194</b>	<b>47.1</b>	192	47.5	195	46.8	<b>193</b>	<b>47.2</b>
445.gobmk	460	22.8	<b>460</b>	<b>22.8</b>	459	22.8	395	26.5	396	26.5	<b>395</b>	<b>26.5</b>
456.hammer	405	23.0	<b>406</b>	<b>23.0</b>	406	23.0	291	32.1	<b>291</b>	<b>32.1</b>	291	32.1
458.sjeng	487	24.9	493	24.5	<b>493</b>	<b>24.6</b>	448	27.0	449	26.9	<b>448</b>	<b>27.0</b>
462.libquantum	68.6	302	67.0	309	<b>67.4</b>	<b>307</b>	68.6	302	67.0	309	<b>67.4</b>	<b>307</b>
464.h264ref	611	36.2	611	36.2	<b>611</b>	<b>36.2</b>	542	40.9	<b>540</b>	<b>41.0</b>	540	41.0
471.omnetpp	<b>270</b>	<b>23.2</b>	270	23.2	269	23.2	238	26.2	238	26.3	<b>238</b>	<b>26.3</b>
473.astar	390	18.0	388	18.1	<b>389</b>	<b>18.1</b>	338	20.8	339	20.7	<b>338</b>	<b>20.8</b>
483.xalanbmk	<b>190</b>	<b>36.2</b>	190	36.3	191	36.2	<b>190</b>	<b>36.2</b>	190	36.3	191	36.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

OMP\_NUM\_THREADS set to number of cores  
KMP\_AFFINITY set to granularity=fine,scatter

This result was measured on the Gateway GR160 F1.  
The Acer AR160 F1 and Gateway GR160 F1 are electronically equivalent.

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalanbmk: -DSPEC\_CPU\_LINUX



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## Base Optimization Flags

C benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel  
-par-runtime-control -opt-prefetch
```

C++ benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/spec/cpu2006.1.1/lib -lsmartheap
```

## Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

```
401.bzip2: /opt/intel/Compiler/11.0/080/bin/intel64/icc
```

```
456.hmmer: /opt/intel/Compiler/11.0/080/bin/intel64/icc
```

```
458.sjeng: /opt/intel/Compiler/11.0/080/bin/intel64/icc
```

C++ benchmarks (except as noted below):

icpc

```
473.astar: /opt/intel/Compiler/11.0/080/bin/intel64/icpc
```

## Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
```

```
401.bzip2: -DSPEC_CPU_LP64
```

```
456.hmmer: -DSPEC_CPU_LP64
```

```
458.sjeng: -DSPEC_CPU_LP64
```

```
462.libquantum: -DSPEC_CPU_LINUX
```

```
473.astar: -DSPEC_CPU_LP64
```

```
483.xalancbmk: -DSPEC_CPU_LINUX
```



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## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
 -prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
 -prof-use(pass 2) -auto-ilp32 -opt-prefetch -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc  
 -opt-malloc-options=3

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2  
 -ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2  
 -ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
 -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
 -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
 -L/spec/cpu2006.1.1/lib -lsmartheap

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -ansi-alias -opt-ra-region-strategy=routine -auto-ilp32  
 -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap64

483.xalancbmk: basepeak = yes



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## Peak Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at  
<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revE.20100216.html>

You can also download the XML flags source by saving the following link:  
<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revE.20100216.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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