



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Bull SAS

SPECint®2006 = 26.4

NovaScale T810 F2 (Intel Xeon X3430, 2.40 GHz)

SPECint_base2006 = 23.3

CPU2006 license: 20

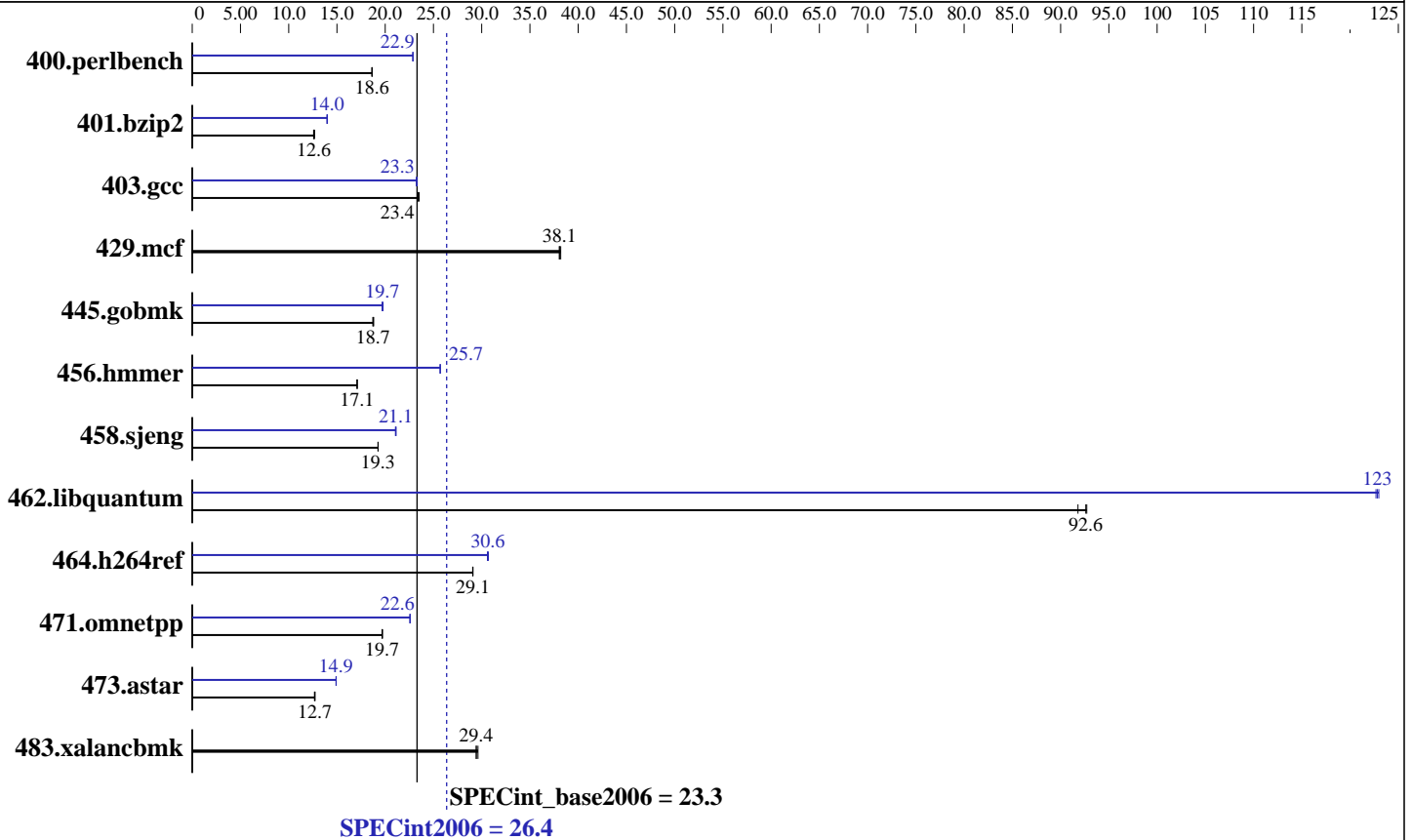
Test date: Oct-2009

Test sponsor: Bull SAS

Hardware Availability: Dec-2009

Tested by: Dell Inc.

Software Availability: Jul-2009



Hardware

CPU Name: Intel Xeon X3430
 CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz
 CPU MHz: 2400
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 8 GB (4 x 2 GB DDR3-1333 DR UDIMM)
 Disk Subsystem: 1 x 160 GB 7200 RPM SATA
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 5.3, Kernel 2.6.18-128.el5
 Compiler: Intel C++ Compiler Professional Edition 11.1 for Linux
 Build 20090511 Package ID: l_cproc_p_11.1.040
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1
 Binutils 2.18.50.0.7.20080502



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Bull SAS

SPECint2006 = **26.4**

NovaScale T810 F2 (Intel Xeon X3430, 2.40 GHz)

SPECint_base2006 = **23.3**

CPU2006 license: 20
Test sponsor: Bull SAS
Tested by: Dell Inc.

Test date: Oct-2009
Hardware Availability: Dec-2009
Software Availability: Jul-2009

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	524	18.6	524	18.6	525	18.6	427	22.9	427	22.9	427	22.9
401.bzip2	767	12.6	766	12.6	761	12.7	689	14.0	690	14.0	691	14.0
403.gcc	343	23.4	344	23.4	342	23.5	346	23.3	346	23.3	346	23.2
429.mcf	240	38.1	240	38.0	239	38.2	240	38.1	240	38.0	239	38.2
445.gobmk	560	18.7	560	18.7	558	18.8	530	19.8	533	19.7	532	19.7
456.hammer	546	17.1	546	17.1	546	17.1	363	25.7	363	25.7	363	25.7
458.sjeng	628	19.3	629	19.3	628	19.3	573	21.1	573	21.1	575	21.1
462.libquantum	226	91.8	224	92.6	223	92.7	168	123	169	123	169	123
464.h264ref	761	29.1	761	29.1	761	29.1	721	30.7	723	30.6	723	30.6
471.omnetpp	317	19.7	318	19.7	317	19.7	277	22.6	277	22.6	277	22.6
473.astar	554	12.7	552	12.7	553	12.7	470	14.9	471	14.9	471	14.9
483.xalancbmk	234	29.4	233	29.6	234	29.4	234	29.4	233	29.6	234	29.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

BIOS Settings:
Power Management = Maximum Performance (Default = Active Power Controller)

General Notes

OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter
The Dell PowerEdge T110 (Intel Xeon X3430, 2.40 GHz) and
the Bull NovaScale T810 F2 (Intel Xeon X3430, 2.40 GHz) models are electronically equivalent.
The results have been measured on a Dell PowerEdge T110 (Intel Xeon X3430, 2.40 GHz) model.

Base Compiler Invocation

C benchmarks:
icc -m32

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Bull SAS

SPECint2006 = 26.4

NovaScale T810 F2 (Intel Xeon X3430, 2.40 GHz)

SPECint_base2006 = 23.3

CPU2006 license: 20

Test sponsor: Bull SAS

Tested by: Dell Inc.

Test date: Oct-2009

Hardware Availability: Dec-2009

Software Availability: Jul-2009

Base Compiler Invocation (Continued)

C++ benchmarks:
icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -inline-calloc
-opt-malloc-options=3

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks (except as noted below):
icpc -m32

473.astar: icpc -m64



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Bull SAS

SPECint2006 = 26.4

NovaScale T810 F2 (Intel Xeon X3430, 2.40 GHz)

SPECint_base2006 = 23.3

CPU2006 license: 20
Test sponsor: Bull SAS
Tested by: Dell Inc.

Test date: Oct-2009
Hardware Availability: Dec-2009
Software Availability: Jul-2009

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -ansi-alias -opt-prefetch
401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -auto-ilp32 -opt-prefetch
403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3
429.mcf: basepeak = yes
445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
-ipo -no-prec-div -ansi-alias
456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias -auto-ilp32
458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll4 -auto-ilp32
462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -parallel
-par-runtime-control -opt-prefetch -inline-calloc
-opt-malloc-options=3
464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Bull SAS

SPECint2006 = 26.4

NovaScale T810 F2 (Intel Xeon X3430, 2.40 GHz)

SPECint_base2006 = 23.3

CPU2006 license: 20

Test sponsor: Bull SAS

Tested by: Dell Inc.

Test date: Oct-2009

Hardware Availability: Dec-2009

Software Availability: Jul-2009

Peak Optimization Flags (Continued)

```
473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
          -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
          -ansi-alias -opt-ra-region-strategy=routine -auto-ilp32
          -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap64
```

```
483.xalancbmk: basepeak = yes
```

Peak Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-int-linux64-revA.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-int-linux64-revA.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 04:02:56 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 22 December 2009.