



SPEC[®] CFP2006 Result

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Fujitsu

SPECfp[®]_rate2006 = 101

PRIMERGY TX200 S5, Intel Xeon X5560, 2.80 GHz

SPECfp_rate_base2006 = 97.7

CPU2006 license: 19

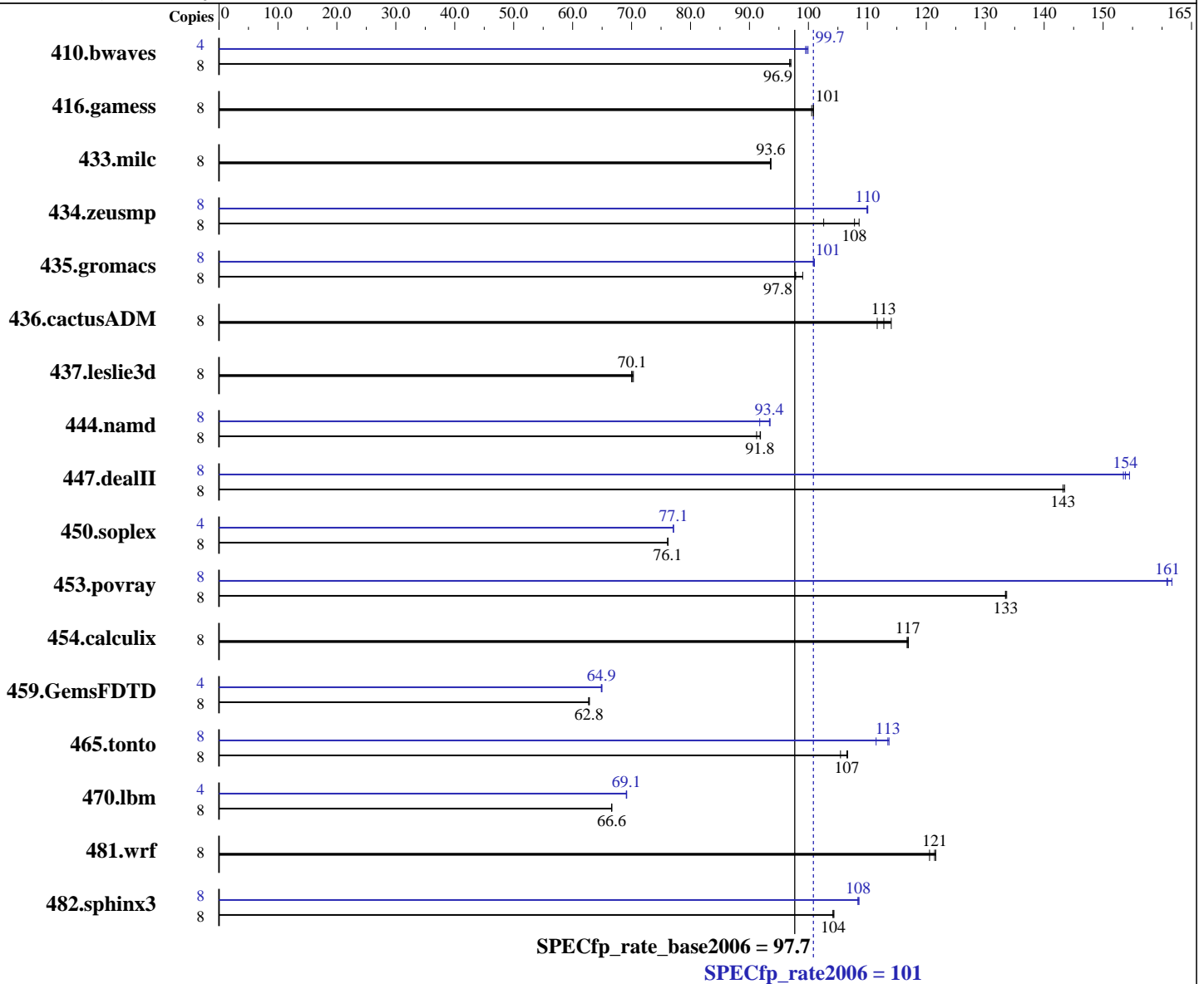
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Sep-2009

Hardware Availability: Jun-2009

Software Availability: Feb-2009



Hardware

CPU Name: Intel Xeon X5560
 CPU Characteristics: Intel Turbo Boost Technology up to 3.20 GHz
 CPU MHz: 2800
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16.60-0.21-smpp
 Compiler: Intel C++ and Fortran Compiler 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.080, l_cprof_p_11.0.080
 Auto Parallel: No
 File System: ext3
 System State: Multi-User Run Level 3
 Base Pointers: 64-bit

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L3 Cache: 8 MB I+D on chip per chip
Other Cache: None
Memory: 24 GB (3x8 GB PC3-10600R, 2 rank, CL9-9-9, ECC)
Disk Subsystem: 1 x SATA, 250 GB, 7200 RPM
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: Binutils 2.18.50.0.7.20080502

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	1123	96.8	1120	97.0	<u>1122</u>	<u>96.9</u>	4	546	99.5	544	99.9	<u>545</u>	<u>99.7</u>
416.gamess	8	1558	101	<u>1554</u>	<u>101</u>	1553	101	8	1558	101	<u>1554</u>	<u>101</u>	1553	101
433.milc	8	785	93.5	<u>784</u>	<u>93.6</u>	784	93.6	8	785	93.5	<u>784</u>	<u>93.6</u>	784	93.6
434.zeusmp	8	710	103	670	109	<u>675</u>	<u>108</u>	8	661	110	<u>662</u>	<u>110</u>	662	110
435.gromacs	8	584	97.8	577	99.0	<u>584</u>	<u>97.8</u>	8	<u>566</u>	<u>101</u>	566	101	565	101
436.cactusADM	8	856	112	<u>848</u>	<u>113</u>	838	114	8	856	112	<u>848</u>	<u>113</u>	838	114
437.leslie3d	8	1074	70.0	<u>1073</u>	<u>70.1</u>	1070	70.3	8	1074	70.0	<u>1073</u>	<u>70.1</u>	1070	70.3
444.namd	8	698	91.9	<u>699</u>	<u>91.8</u>	703	91.2	8	<u>687</u>	<u>93.4</u>	686	93.5	699	91.7
447.dealII	8	638	143	<u>639</u>	<u>143</u>	639	143	8	<u>595</u>	<u>154</u>	592	154	596	153
450.soplex	8	876	76.2	876	76.1	<u>876</u>	<u>76.1</u>	4	<u>433</u>	<u>77.1</u>	433	77.1	433	77.1
453.povray	8	319	133	318	134	<u>319</u>	<u>133</u>	8	263	162	265	161	<u>264</u>	<u>161</u>
454.calculix	8	<u>565</u>	<u>117</u>	565	117	564	117	8	<u>565</u>	<u>117</u>	565	117	564	117
459.GemsFDTD	8	1353	62.7	<u>1353</u>	<u>62.8</u>	1350	62.9	4	653	65.0	654	64.9	<u>654</u>	<u>64.9</u>
465.tonto	8	<u>739</u>	<u>107</u>	747	105	738	107	8	<u>694</u>	<u>113</u>	692	114	706	111
470.lbm	8	1649	66.6	1651	66.6	<u>1650</u>	<u>66.6</u>	4	795	69.1	<u>795</u>	<u>69.1</u>	794	69.2
481.wrf	8	735	122	<u>736</u>	<u>121</u>	741	121	8	735	122	<u>736</u>	<u>121</u>	741	121
482.sphinx3	8	1494	104	<u>1496</u>	<u>104</u>	1497	104	8	1436	109	1438	108	<u>1438</u>	<u>108</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind processes to cores and its local memory.
Details may be found in the config file.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

General Notes

For information about Fujitsu please visit: <http://www.fujitsu.com>



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Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static



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Peak Compiler Invocation

C benchmarks (except as noted below):

icc

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc

450.soplex: icpc -m32

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
-auto-ilp32

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2

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Peak Optimization Flags (Continued)

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-fno-alias -auto-ilp32

447.dealIII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias -scalar-rep-

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

416.gamess: basepeak = yes

434.zeusmp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0 -opt-prefetch

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -auto

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes



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The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090901.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090901.xml>

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