SPEC® CFP2006 Result

Fujitsu

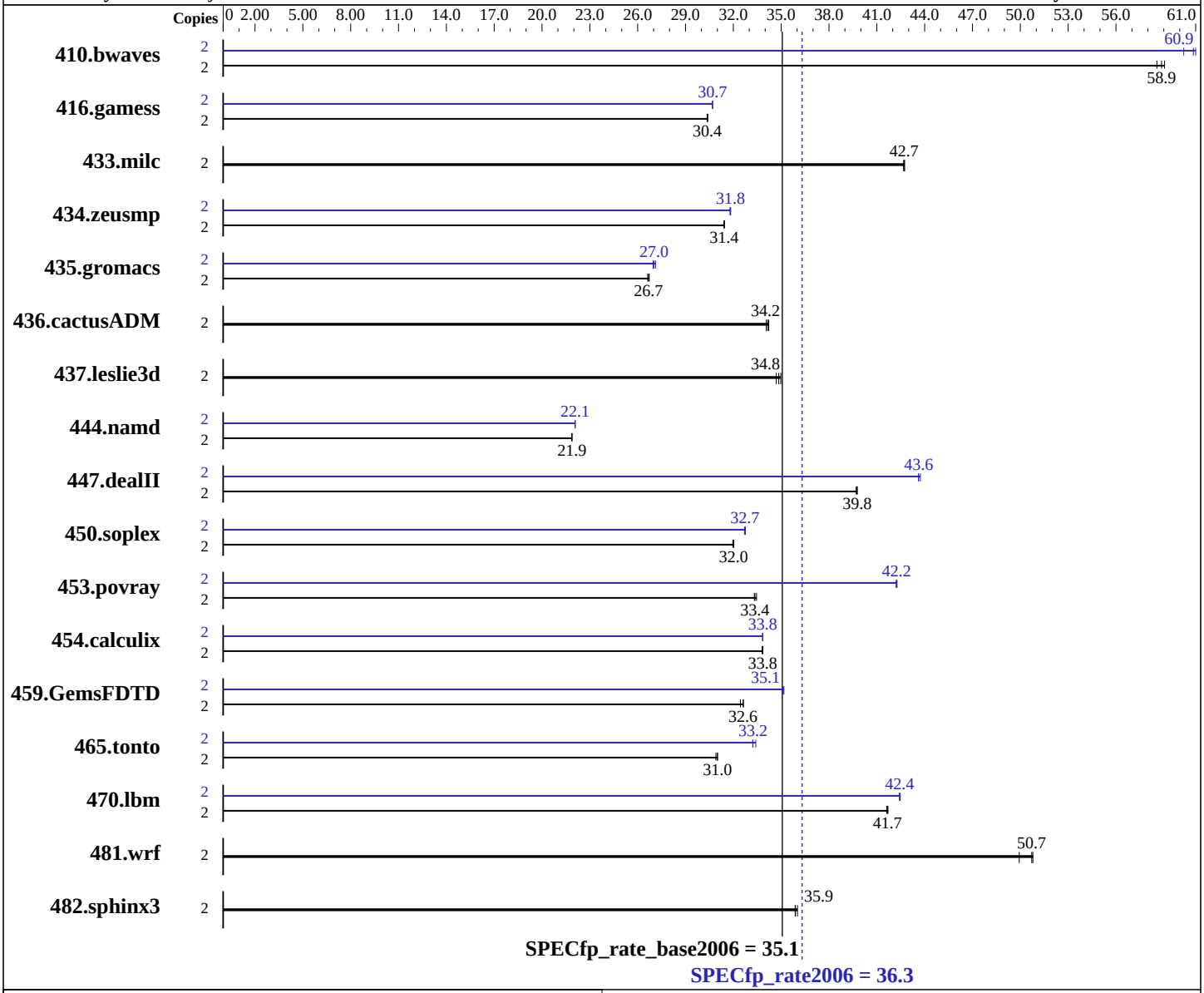
PRIMERGY TX200 S5, Intel Xeon E5502, 1.87 GHz

SPECfp®_rate2006 = 36.3

SPECfp_rate_base2006 = 35.1

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Aug-2009
Hardware Availability: Apr-2009
Software Availability: Feb-2009



Hardware

CPU Name:	Intel Xeon E5502
CPU Characteristics:	
CPU MHz:	1867
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

Continued on next page

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16.60-0.21-smp
Compiler:	Intel C++ and Fortran Compiler 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.080, l_cprof_p_11.0.080
Auto Parallel:	No
File System:	ext3
System State:	Multi-User Run Level 3
Base Pointers:	64-bit

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L3 Cache: 4 MB I+D on chip per chip
Other Cache: None
Memory: 24 GB (6x4 GB PC3-10600R, 2 rank, CL9-9-9, ECC, see add'l detail in notes)
Disk Subsystem: 1 x SATA, 250 GB, 7200 RPM
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: Binutils 2.18.50.0.7.20080502

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	2	**462**	**58.9**	460	59.0	464	58.6	2	446	61.0	451	60.3	**447**	**60.9**
416.gamess	2	1288	30.4	**1289**	**30.4**	1290	30.4	2	1276	30.7	**1276**	**30.7**	1276	30.7
433.milc	2	430	42.7	**430**	**42.7**	430	42.7	2	430	42.7	**430**	**42.7**	430	42.7
434.zeusmp	2	579	31.4	579	31.4	**579**	**31.4**	2	572	31.8	**572**	**31.8**	572	31.8
435.gromacs	2	536	26.6	**535**	**26.7**	534	26.7	2	527	27.1	**528**	**27.0**	530	27.0
436.cactusADM	2	**700**	**34.2**	698	34.2	702	34.1	2	**700**	**34.2**	698	34.2	702	34.1
437.leslie3d	2	542	34.7	537	35.0	**540**	**34.8**	2	542	34.7	537	35.0	**540**	**34.8**
444.namd	2	**733**	**21.9**	733	21.9	733	21.9	2	**727**	**22.1**	727	22.1	726	22.1
447.dealII	2	576	39.7	**575**	**39.8**	575	39.8	2	525	43.6	523	43.7	**524**	**43.6**
450.soplex	2	521	32.0	522	32.0	**521**	**32.0**	2	509	32.8	510	32.7	**510**	**32.7**
453.povray	2	318	33.5	**319**	**33.4**	319	33.3	2	**252**	**42.2**	252	42.2	252	42.3
454.calculix	2	**488**	**33.8**	488	33.8	487	33.9	2	**488**	**33.8**	488	33.8	487	33.9
459.GemsFDTD	2	654	32.4	650	32.6	**651**	**32.6**	2	604	35.1	**604**	**35.1**	603	35.2
465.tonto	2	635	31.0	**635**	**31.0**	637	30.9	2	589	33.4	592	33.2	**592**	**33.2**
470.lbm	2	660	41.6	**660**	**41.7**	659	41.7	2	**648**	**42.4**	647	42.5	648	42.4
481.wrf	2	440	50.8	**440**	**50.7**	447	49.9	2	440	50.8	**440**	**50.7**	447	49.9
482.sphinx3	2	**1086**	**35.9**	1082	36.0	1086	35.9	2	**1086**	**35.9**	1082	36.0	1086	35.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

The system automatically configures the memory to run at 800 MHz.

General Notes

For information about Fujitsu please visit: http://www.fujitsu.com

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static

Fortran benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static

Benchmarks using both Fortran and C:
-xSSE4.2 -ipo -O3 -no-prec-div -static

Peak Compiler Invocation

C benchmarks:
icc

C++ benchmarks (except as noted below):
icpc

450.soplex: icpc -m32

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
-auto-ilp32

482.sphinx3: basepeak = yes

C++ benchmarks:

Continued on next page

Peak Optimization Flags (Continued)

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll2 -ansi-alias -scalar-rep-

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -opt-malloc-options=3

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll2 -Ob0 -opt-prefetch

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll4 -auto

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090901.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090901.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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