



SPEC® CINT2006 Result

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Fujitsu

SPECint®2006 = 20.0

PRIMERGY RX200 S5, Intel Xeon E5502, 1.87 GHz

SPECint_base2006 = 18.0

CPU2006 license: 19

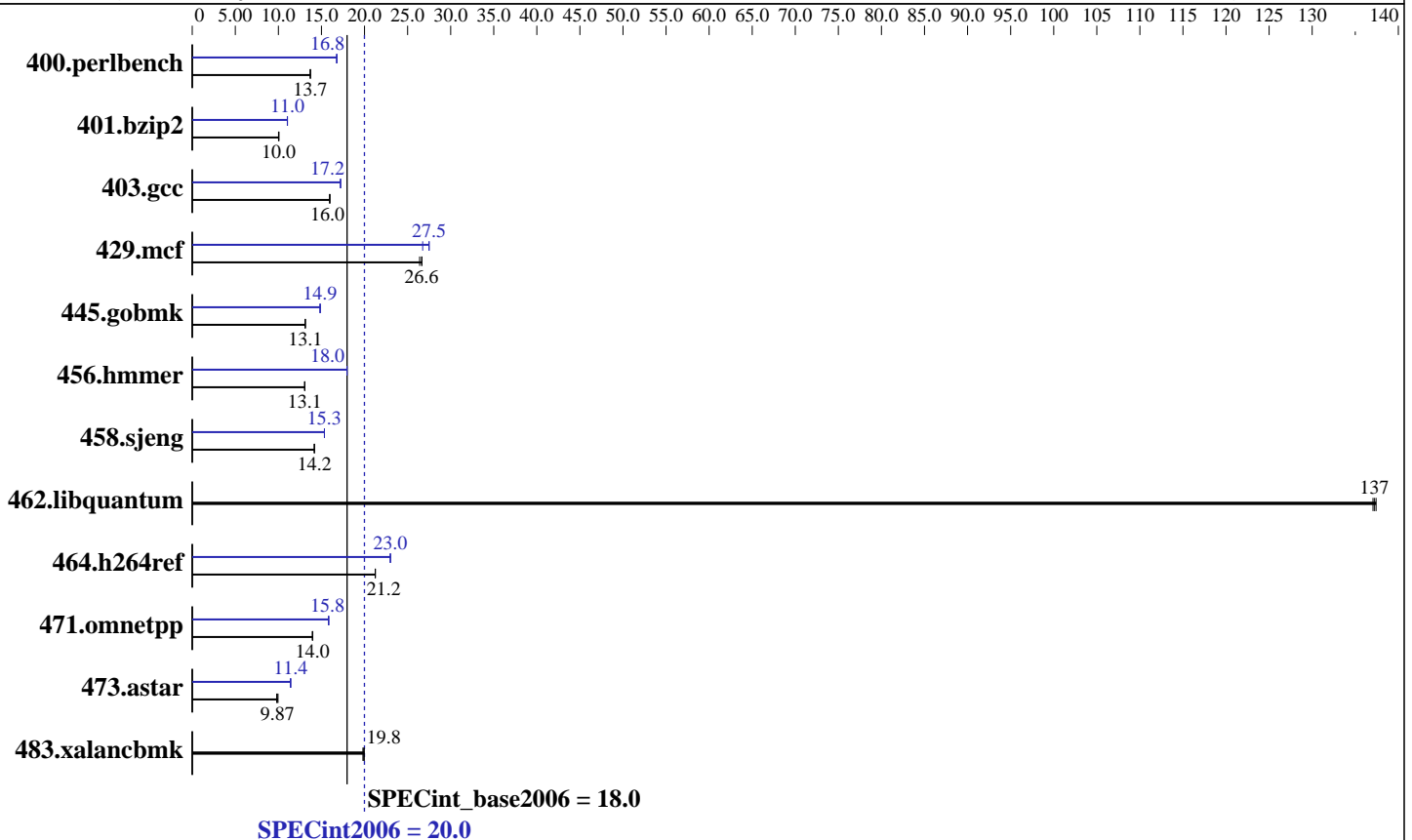
Test date: Jul-2009

Test sponsor: Fujitsu

Hardware Availability: Apr-2009

Tested by: Fujitsu

Software Availability: Feb-2009



Hardware

CPU Name: Intel Xeon E5502
 CPU Characteristics:
 CPU MHz: 1867
 FPU: Integrated
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 4 MB I+D on chip per chip
 Other Cache: None
 Memory: 48 GB (12x4 GB PC3-8500R, 2 rank, CL7-7-7, ECC, see add'l detail in notes)
 Disk Subsystem: 1 x SATA, 250 GB, 7200 RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16.60-0.21-smp
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.080
 Auto Parallel: Yes
 File System: ext3
 System State: Multi-User Run Level 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	712	13.7	<u>713</u>	<u>13.7</u>	713	13.7	<u>582</u>	<u>16.8</u>	582	16.8	583	16.8
401.bzip2	<u>960</u>	<u>10.0</u>	960	10.1	963	10.0	873	11.1	874	11.0	<u>874</u>	<u>11.0</u>
403.gcc	505	15.9	504	16.0	<u>504</u>	<u>16.0</u>	<u>468</u>	<u>17.2</u>	468	17.2	468	17.2
429.mcf	346	26.4	342	26.7	<u>343</u>	<u>26.6</u>	<u>332</u>	<u>27.5</u>	341	26.8	332	27.5
445.gobmk	799	13.1	800	13.1	<u>799</u>	<u>13.1</u>	<u>706</u>	<u>14.9</u>	706	14.9	708	14.8
456.hammer	715	13.1	714	13.1	<u>714</u>	<u>13.1</u>	519	18.0	520	18.0	<u>519</u>	<u>18.0</u>
458.sjeng	<u>853</u>	<u>14.2</u>	854	14.2	853	14.2	789	15.3	<u>788</u>	<u>15.3</u>	788	15.3
462.libquantum	151	137	<u>151</u>	<u>137</u>	151	137	151	137	<u>151</u>	<u>137</u>	151	137
464.h264ref	<u>1042</u>	<u>21.2</u>	1041	21.3	1042	21.2	963	23.0	<u>962</u>	<u>23.0</u>	962	23.0
471.omnetpp	<u>448</u>	<u>14.0</u>	448	13.9	447	14.0	<u>395</u>	<u>15.8</u>	395	15.8	395	15.8
473.astar	716	9.80	<u>711</u>	<u>9.87</u>	705	9.96	617	11.4	<u>614</u>	<u>11.4</u>	614	11.4
483.xalancbmk	345	20.0	348	19.8	<u>348</u>	<u>19.8</u>	345	20.0	348	19.8	<u>348</u>	<u>19.8</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

The system automatically configures the memory to run at 800 MHz.

General Notes

OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter
For information about Fujitsu please visit: <http://www.fujitsu.com>

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel
-par-runtime-control -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/Compiler/11.0/080/bin/intel64/icc

456.hmmer: /opt/intel/Compiler/11.0/080/bin/intel64/icc

458.sjeng: /opt/intel/Compiler/11.0/080/bin/intel64/icc

C++ benchmarks (except as noted below):

icpc

473.astar: /opt/intel/Compiler/11.0/080/bin/intel64/icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -ansi-alias -opt-prefetch
401.bzp2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -auto-ilp32 -opt-prefetch -ansi-alias
403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3
429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
-ipo -no-prec-div -ansi-alias
456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias -auto-ilp32
458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll4 -auto-ilp32
462.libquantum: basepeak = yes
464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap
473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=routine -auto-ilp32
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap64

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Peak Optimization Flags (Continued)

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20091013.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20091013.xml>

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For other inquiries, please contact webmaster@spec.org.

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