SPEC® CINT2006 Result

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Lenovo Group Limited

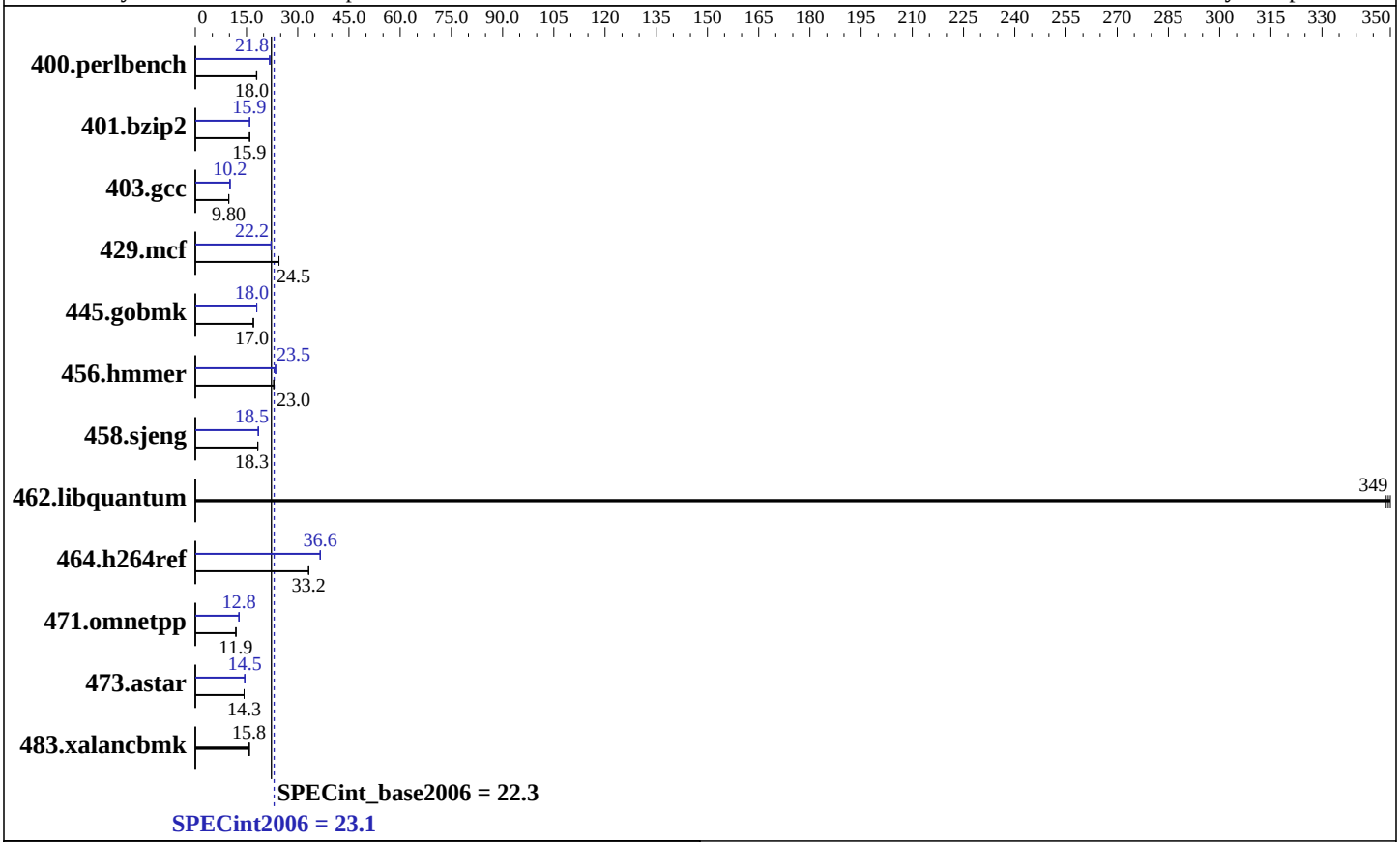
Lenovo ThinkServer TD100x(Intel Xeon E5430)

SPECint®2006 = 23.1

SPECint_base2006 = 22.3

CPU2006 license: 9017
Test sponsor: Lenovo Group Limited
Tested by: Lenovo Group Limited

Test date: Apr-2009
Hardware Availability: Apr-2009
Software Availability: Apr-2009



Hardware

CPU Name:	Intel Xeon E5430
CPU Characteristics:	1333MHz system bus
CPU MHz:	2666
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8 x 2GB Hynix 2Rx8 PC2 5300F)
Disk Subsystem:	1 x 160 GB, SATA 7200 RPM
Other Hardware:	None

Software

Operating System:	Windows Server 2003 R2, Standard x64 Edition
Compiler:	Intel C++ Compiler Professional 11.0 for Intel64 Build 20080930 Package ID: w_cproc_p_11.0.072 Microsoft Visual Studio 2008 (for libraries)
Auto Parallel:	Yes
File System:	NTFS
System State:	Default
Base Pointers:	32/64-bit
Peak Pointers:	32/64-bit
Other Software:	SmartHeap Library Version 9.0 from http://www.microquill.com/

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	**544**	**18.0**	544	18.0	544	17.9	449	21.8	**449**	**21.8**	449	21.8
401.bzip2	606	15.9	**606**	**15.9**	606	15.9	606	15.9	606	15.9	**606**	**15.9**
403.gcc	822	9.80	823	9.80	**822**	**9.80**	794	10.1	**790**	**10.2**	790	10.2
429.mcf	373	24.5	**373**	**24.5**	373	24.5	410	22.2	**410**	**22.2**	410	22.2
445.gobmk	615	17.0	616	17.0	**616**	**17.0**	584	18.0	**584**	**18.0**	584	18.0
456.hmmer	405	23.0	405	23.0	**405**	**23.0**	397	23.5	**397**	**23.5**	397	23.5
458.sjeng	660	18.3	**660**	**18.3**	660	18.3	655	18.5	656	18.4	**655**	**18.5**
462.libquantum	59.2	350	59.4	349	**59.3**	**349**	59.2	350	59.4	349	**59.3**	**349**
464.h264ref	667	33.2	667	33.2	**667**	**33.2**	604	36.6	**604**	**36.6**	604	36.6
471.omnetpp	527	11.9	**527**	**11.9**	527	11.9	490	12.8	**490**	**12.8**	490	12.8
473.astar	491	14.3	**491**	**14.3**	491	14.3	483	14.5	**483**	**14.5**	483	14.5
483.xalancbmk	437	15.8	**437**	**15.8**	437	15.8	437	15.8	**437**	**15.8**	437	15.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:

```
icl -Qvc9 -Qstd=c99
```

C++ benchmarks:

```
icl -Qvc9
```

Base Portability Flags

400.perlbench: `-DSPEC_CPU_P64 -DSPEC_CPU_WIN64_X64 -DSPEC_CPU_NO_NEED_VA_COPY`
401.bzip2: `-DSPEC_CPU_P64`
403.gcc: `-DSPEC_CPU_P64 -DSPEC_CPU_WIN64`
429.mcf: `-DSPEC_CPU_P64`
445.gobmk: `-DSPEC_CPU_P64`
456.hmmer: `-DSPEC_CPU_P64`
458.sjeng: `-DSPEC_CPU_P64`
462.libquantum: `-DSPEC_CPU_P64`
464.h264ref: `-DSPEC_CPU_P64 -DWIN32 -DSPEC_CPU_NO_INTTYPES`
471.omnetpp: `-DSPEC_CPU_P64 -DSPEC_CPU_WIN64`
473.astar: `-DSPEC_CPU_P64`
483.xalancbmk: `-DSPEC_CPU_P64 -Qoption,cpp,--no_wchar_t_keyword`

Base Optimization Flags

C benchmarks:
```
-QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qparallel
-Qpar-runtime-control -Qvec-guard-write -Qauto-ilp32 /F512000000
```

C++ benchmarks:
```
-QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx-features
-Qauto-ilp32 /F512000000 shlW64Mt.lib            -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks:
```
icl -Qvc9 -Qstd=c99
```

C++ benchmarks:
```
icl -Qvc9
```

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

400.perlbench: `-QxSSE4.1`(pass 2) `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-prefetch /F512000000`

401.bzip2: `-QxSSE4.1`(pass 2) `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-Qipo -O3 -Qprec-div- -Qopt-prefetch -Qansi-alias /F512000000`

403.gcc: `-QxSSE4.1`(pass 2) `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-Qipo -O3 -Qprec-div- /F512000000`

Continued on next page

Peak Optimization Flags (Continued)

429.mcf: -QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000

445.gobmk: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O2 -Qprec-div- -Qansi-alias /F512000000

456.hmmer: -QxSSE4.1 -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias /F512000000

458.sjeng: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll4 /F512000000

462.libquantum: basepeak = yes

464.h264ref: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias /F512000000

C++ benchmarks:

471.omnetpp: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-ra-region-strategy=block /F512000000 shlW64Mt.lib -link /FORCE:MULTIPLE

473.astar: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-ra-region-strategy=routine /F512000000 shlW64Mt.lib -link /FORCE:MULTIPLE

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.0-winx64-revA.20090721.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.0-winx64-revA.20090721.xml

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