SPEC® CINT2006 Result

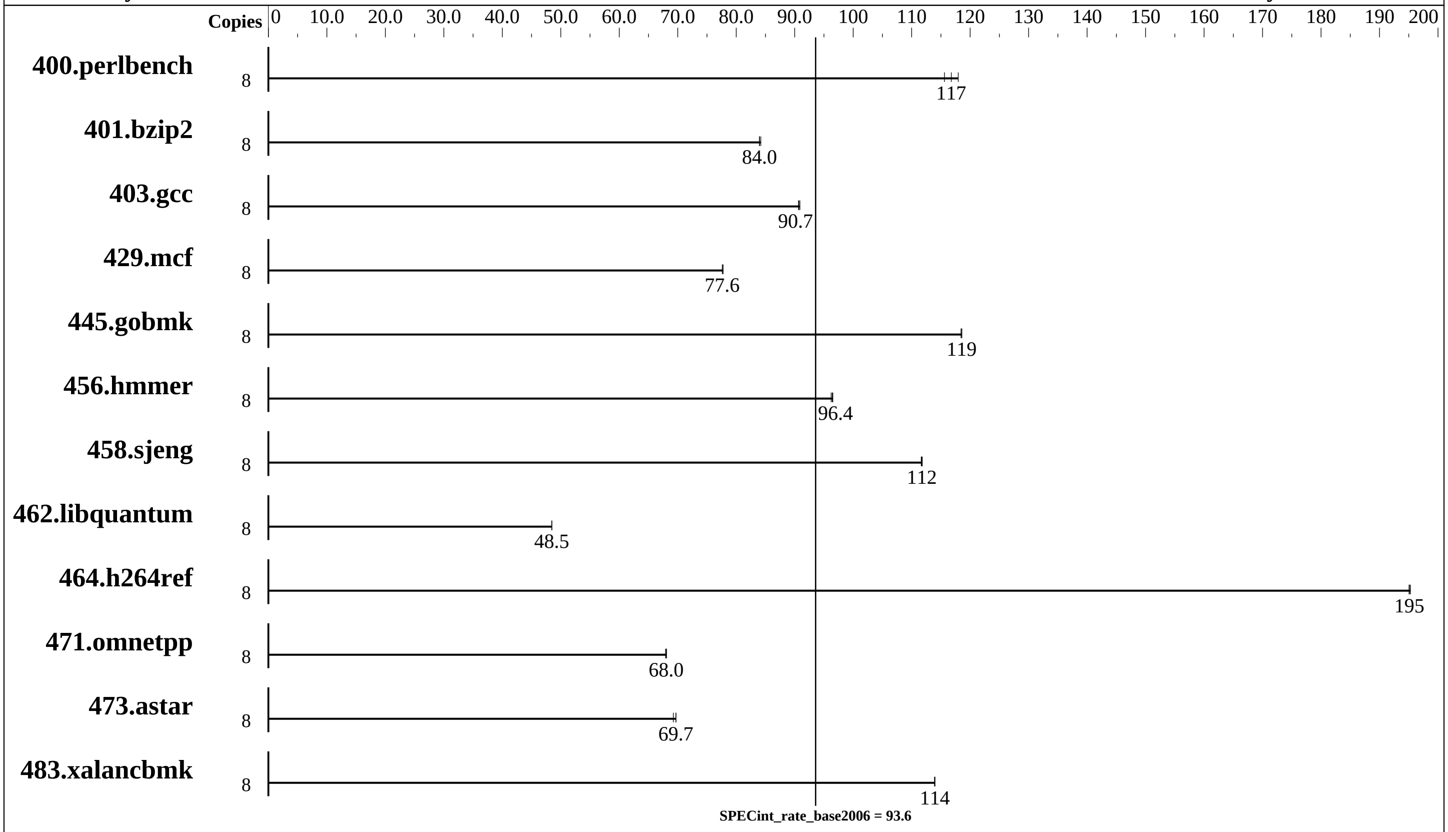
Dell Inc.

PowerEdge 2900 III (Intel Xeon E5410, 2.33 GHz)

SPECint®_rate2006 = Not Run

SPECint_rate_base2006 = 93.6

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.
Test date: Jul-2008
Hardware Availability: Nov-2007
Software Availability: Nov-2007



Hardware

CPU Name:	Intel Xeon E5410
CPU Characteristics:	1333 MHz system bus
CPU MHz:	2333
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	8 GB (8 x 1GB 667 MHz DDR2 FB-DIMM)
Disk Subsystem:	1 x 300GB 10000 RPM SAS
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16-60.0.21-smp
Compiler:	Intel C++ Compiler 10.1 for Linux Build 20070913 Package ID: l_cc_p_10.1.008
Auto Parallel:	No
File System:	ReiserFS
System State:	Multi-user, run level 3
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	Microquill SmartHeap V8.1 Binutils 2.17.50.0.15

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Results Table

	Base							Peak						
Benchmark	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	663	118	**669**	**117**	676	116							
401.bzip2	8	920	84.0	917	84.2	**919**	**84.0**							
403.gcc	8	711	90.6	709	90.9	**710**	**90.7**							
429.mcf	8	**940**	**77.6**	940	77.6	938	77.8							
445.gobmk	8	708	119	**708**	**119**	709	118							
456.hmmer	8	776	96.2	773	96.5	**774**	**96.4**							
458.sjeng	8	**866**	**112**	866	112	867	112							
462.libquantum	8	**3420**	**48.5**	3421	48.5	3420	48.5							
464.h264ref	8	**907**	**195**	907	195	908	195							
471.omnetpp	8	736	67.9	**735**	**68.0**	734	68.1							
473.astar	8	**806**	**69.7**	806	69.7	811	69.2							
483.xalancbmk	8	485	114	**484**	**114**	484	114							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Invocation Notes

```
OMP_NUM_THREADS set to number of cores
KMP_STACK_SIZE set to 64M
KMP_AFFINITY set to physical,0
```

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'/usr/bin/taskset' used to bind processes to CPUs
```

Platform Notes

```
BIOS Settings:
Adjacent Cache Line Prefetch = Disabled (default Enabled)
Hardware Prefetcher = Disabled (default Enabled)
```

Base Compiler Invocation

C benchmarks:
```
icc
```

C++ benchmarks:
```
icpc
```

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Dell-Intel-ic10.1-int-linux64-revC.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Dell-Intel-ic10.1-int-linux64-revC.xml

Tested with SPEC CPU2006 v1.0.
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