



SPEC® CINT2006 Result

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Bull SAS

SPECint®2006 = 16.6

NovaScale T840 (3.00 GHz, Intel Xeon 5160)

SPECint_base2006 = 16.6

CPU2006 license: 3

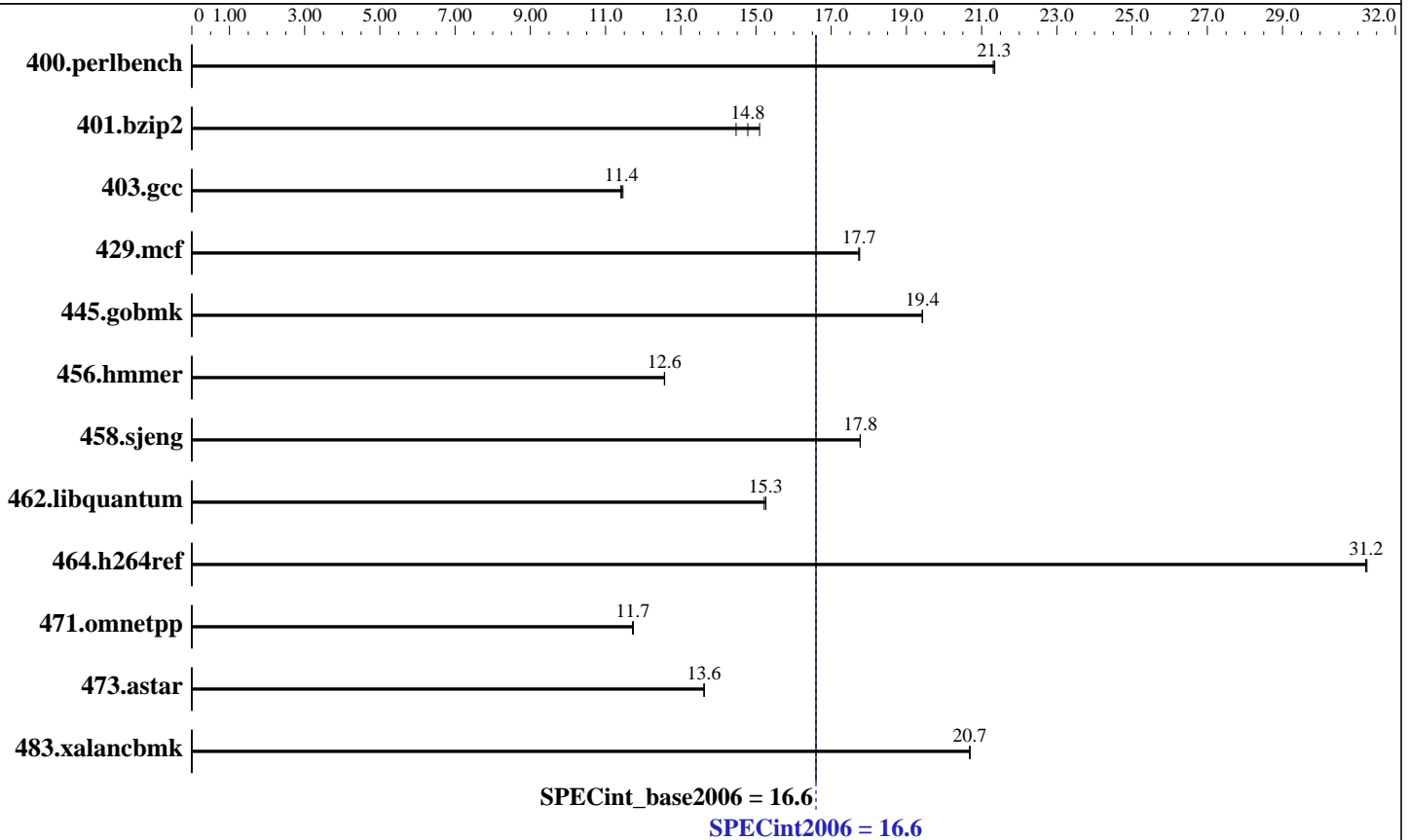
Test sponsor: Bull SAS

Tested by: Bull SAS

Test date: Feb-2007

Hardware Availability: Jul-2006

Software Availability: Dec-2006



Hardware

CPU Name: Intel Xeon 5160
 CPU Characteristics: 3.0GHz, 1332MHz bus
 CPU MHz: 3000
 FPU: Integrated
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 4 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 8 GB (667 MHz ECC CL5 DDR2 FB-DIMM)
 Disk Subsystem: 3x73GB SCSI 15000 rpm
 Other Hardware: None

Software

Operating System: Windows Server 2003 Enterprise X64 Edition
 Compiler: Intel C++ Compiler 9.1.033 for 32-bit apps, Build 20061103Z Package ID: W_CC_P_9.1.033 Microsoft Visual Studio .NET 2003 (libraries)
 Auto Parallel: No
 File System: NTFS
 System State: Default
 Base Pointers: 64-bit
 Peak Pointers: Not Applicable
 Other Software: MicroQuill SmartHeap Library 8.0 (shIW32M.lib)



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	459	21.3	458	21.3	458	21.3	459	21.3	458	21.3	458	21.3
401.bzip2	653	14.8	667	14.5	639	15.1	653	14.8	667	14.5	639	15.1
403.gcc	705	11.4	702	11.5	706	11.4	705	11.4	702	11.5	706	11.4
429.mcf	514	17.7	514	17.8	514	17.7	514	17.7	514	17.8	514	17.7
445.gobmk	540	19.4	540	19.4	540	19.4	540	19.4	540	19.4	540	19.4
456.hammer	742	12.6	742	12.6	742	12.6	742	12.6	742	12.6	742	12.6
458.sjeng	681	17.8	681	17.8	681	17.8	681	17.8	681	17.8	681	17.8
462.libquantum	1362	15.2	1358	15.3	1357	15.3	1362	15.2	1358	15.3	1357	15.3
464.h264ref	709	31.2	709	31.2	708	31.2	709	31.2	709	31.2	708	31.2
471.omnetpp	533	11.7	532	11.7	533	11.7	533	11.7	532	11.7	533	11.7
473.astar	516	13.6	516	13.6	515	13.6	516	13.6	516	13.6	515	13.6
483.xalancbmk	334	20.7	334	20.7	334	20.7	334	20.7	334	20.7	334	20.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99
C++ benchmarks:
icl -Qvc7.1

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Base Optimization Flags

C benchmarks:
-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE
C++ benchmarks:
-fast -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE



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Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Optimization Flags

C benchmarks:

400.perlbench: basepeak = yes

401.bzip2: basepeak = yes

403.gcc: basepeak = yes

429.mcf: basepeak = yes

445.gobmk: basepeak = yes

456.hmmer: basepeak = yes

458.sjeng: basepeak = yes

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: basepeak = yes

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/flags.20090714.00.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/flags.20090714.00.xml>



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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 10:41:30 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 6 March 2007.