



# CFP2000 Result

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## Itautec

Servidor Itautec ZX440 (Intel Xeon 7140M processor, 3.40 GHz)

SPECfp\_rate2000 = 107

SPECfp\_rate\_base2000 = 107

SPEC license #: 9001 | Tested by: Itautec | Test date: Jan-2007 | Hardware Avail: Jul-2006 | Software Avail: May-2006

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
168.wupwise	16	248	120	16	248	120
171.swim	16	1064	54.1	16	1064	54.1
172.mgrid	16	624	53.5	16	624	53.5
173.applu	16	704	55.4	16	704	55.4
177.mesa	16	212	122	16	212	122
178.galgel	16	153	351	16	153	351
179.art	16	97.9	493	16	97.9	493
183.equake	16	322	74.9	16	322	74.9
187.facerec	16	214	165	16	214	165
188.amp	16	326	125	16	326	125
189.lucas	16	614	60.4	16	614	60.4
191.fma3d	16	557	70.0	16	557	70.0
200.sixtrack	16	277	73.8	16	277	73.8
301.apsi	16	382	126	16	382	126

### Hardware

CPU: Intel Xeon 7140M processor (3.40 GHz, 2x1MB L2, 16MB L3, 800 MHz system bus)  
 CPU MHz: 3400  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 4 chips, 2 cores/chip (Hyper-Threading Technology enabled)  
 CPU(s) orderable: 1,2,3,4  
 Parallel: No  
 Primary Cache: 12k micro-ops I + 16KBD on chip, per core  
 Secondary Cache: 1024KB(I+D) on chip, per core  
 L3 Cache: 16MB on chip, per chip  
 Other Cache: N/A  
 Memory: 16x1GB DDR2-RAM PC2-3200R (CAS 3-3-3)  
 Disk Subsystem: 1 x 36GB SCSI 10000RPM  
 Other Hardware: None

### Software

Operating System: Windows Server 2003 Enterprise Edition + SP1 (32-bit)  
 Compiler: Intel C++ Compiler 9.1  
 Build 20060519Z (for 32-bit applications),  
 Intel Fortran Compiler 9.1  
 Build 20060519Z (for 32-bit applications),  
 Microsoft Visual Studio .NET 7.0.9466 (for libraries)  
 Microquill SmartHeap Library v.8.0 for SMP  
 File System: NTFS  
 System State: Default

## Notes/Tuning Information

### PORTABILITY FLAGS

178.galgel: -FI /F32000000

### GENERAL

+FDO: PASS1=-Qprof\_gen PASS2=-Qprof\_use

### Base tuning flags

for Fortran programs: -fast -Qansi\_alias +FDO  
 for C programs: -fast +FDO shlw32M.lib

### EXTRA LIBRARIES

<http://www.microquill.com>

### Bios Settings:

Hardware Prefetch enabled:  
 prefetch data in order to shorten execution cycles and maximize  
 data processing efficiency.



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## Notes/Tuning Information (Continued)

Snoop Filter enabled:

Preserves cache coherency while minimizing snoops to remote nodes.

Memory Array set to High-Performance Memory Array:

Memory is up to 4-way interleaved (Memory RAID disabled).