



CFP2000 Result

Copyright ©1999-2005, Standard Performance Evaluation Corporation

Fujitsu Siemens Computers

PRIMERGY RX200 S3, Intel Xeon processor E5310, 1.60 GHz

SPECfp_rate2000 = 75.5

SPECfp_rate_base2000 = 75.5

SPEC license #: 22 | Tested by: Fujitsu Siemens Computers | Test date: Nov-2006 | Hardware Avail: Nov-2006 | Software Avail: Aug-2006

				Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio	
250	200	150	100	50	168.wupwise	8	150	99.2	8	150	99.2
					171.swim	8	691	41.6	8	691	41.6
					172.mgrid	8	432	38.7	8	432	38.7
					173.applu	8	462	42.1	8	462	42.1
					177.mesa	8	87.8	148	8	87.8	148
					178.galgel	8	120	225	8	120	225
					179.art	8	115	209	8	115	209
					183.quake	8	332	36.3	8	332	36.3
					187.facerec	8	180	98.0	8	180	98.0
					188.amp	8	188	109	8	188	109
					189.lucas	8	388	47.8	8	388	47.8
					191.fma3d	8	375	51.9	8	375	51.9
					200.sixtrack	8	184	55.6	8	184	55.6
					301.apsi	8	306	79.0	8	306	79.0

Hardware

CPU: Intel Xeon processor E5310 (1.60 GHz, 2x4MB L2, 1066 MHz system bus)
CPU MHz: 1600
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
CPU(s) orderable: 1,2 chips
Parallel: No
Primary Cache: 32KB(I) + 32KB(D) on chip, per core
Secondary Cache: 8MB(I+D) on chip, per chip (4MB shared per 2 cores)
L3 Cache: N/A
Other Cache: N/A
Memory: 8x1GB DDR2-RAM PC2-5300F (CAS 5-5-5)
Disk Subsystem: Seagate ST373454SS (SAS, 15.4krpm, 73GB)
Other Hardware: none

Software

Operating System: 64-Bit SUSE LINUX Enterprise Server 10
Kernel 2.6.16.21-0.8-smp on an x86_64
Compiler: Intel C++ and Fortran Compiler 9.1 for EM64T
Build 20060816 (for 64-bit applications)
File System: reiserfs
System State: Multi-user run level 3

Notes/Tuning Information

GENERAL

+FDO implies feedback-directed optimization
PASS1: -prof_gen PASS2: -prof_use

Optimization flags

ONESTEP=yes set for all benchmarks

Portability flags

-DSPEC_CPU2000_LP64 applied to all benchmarks
178.galgel: -FI for fixed-format Fortran

Base tuning flags

for Fortran and C programs: -fast +FDO

Peak tuning flags

same as baseline (basepeak=true set globally)



CFP2000 Result

Copyright ©1999-2005, Standard Performance Evaluation Corporation

Fujitsu Siemens Computers

PRIMERGY RX200 S3, Intel Xeon processor E5310, 1.60 GHz

SPECfp_rate2000 = 75.5

SPECfp_rate_base2000 = 75.5

SPEC license #: 22 | Tested by: Fujitsu Siemens Computers | Test date: Nov-2006 | Hardware Avail: Nov-2006 | Software Avail: Aug-2006

Notes/Tuning Information (Continued)

The system bus runs at 1066 MHz

This result was measured with 64-bit binaries using the 64-bit version of the operating system.

BIOS Configuration:

Hardware Prefetch = Disable, Adjacent Sector Prefetch = Disable

For information about Fujitsu Siemens Computers in your country please see:
<http://www.fujitsu-siemens.com/countries>