



CFP2000 Result

Copyright ©1999-2005, Standard Performance Evaluation Corporation

Dell

PowerEdge 1950 (Intel Xeon processor E5320, 1.86 GHz)

SPECfp_rate2000 = 81.3

SPECfp_rate_base2000 = 81.3

SPEC license #: 55 | Tested by: Dell, Inc., Round Rock, TX | Test date: Nov-2006 | Hardware Avail: Dec-2006 | Software Avail: Aug-2006

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
168.wupwise	8	135	110	8	135	110
171.swim	8	583	49.4	8	583	49.4
172.mgrid	8	412	40.6	8	412	40.6
173.applu	8	383	50.8	8	383	50.8
177.mesa	8	73.3	177	8	73.3	177
178.galgel	8	118	228	8	118	228
179.art	8	170	142	8	170	142
183.equake	8	272	44.3	8	272	44.3
187.facerec	8	182	97.1	8	182	97.1
188.amp	8	179	114	8	179	114
189.lucas	8	331	56.1	8	331	56.1
191.fma3d	8	330	59.1	8	330	59.1
200.sixtrack	8	159	64.4	8	159	64.4
301.apsi	8	296	81.6	8	296	81.6

Hardware

CPU: Quad-Core Intel Xeon processor E5320 (1066 MHz system bus)
CPU MHz: 1866
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
CPU(s) orderable: 1,2 chips
Parallel: No
Primary Cache: 32KB(I) + 32KB(D) on chip, per core
Secondary Cache: 8 MB I+D on chip, per chip (4 MB shared per 2 cores)
L3 Cache: N/A
Other Cache: N/A
Memory: 8 GB (8 x 1 GB 667 MHz CL5 FB-DIMM DDR2 SDRAM)
Disk Subsystem: 1 x 80GB SATA 7200 RPM
Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux 4 Advanced Server Update 4 EM64T
Compiler: Intel C++ Compiler 9.0 for EM64T Build 20060120
Intel Fortran Compiler 9.0 for EM64T Build 20051201
File System: ext3
System State: Runlevel 3

Notes/Tuning Information

GENERAL

ONESTEP=yes for all benchmarks

+FDO implies feedback-directed optimization PASS1: -prof_gen PASS2: -prof_use

PORTABILITY FLAGS

-DSPEC_CPU2000_LP64 applied to all benchmarks

178.galgel: -FI for fixed-format Fortran

BASE TUNING

Baseline optimizations for C and Fortran: -fast +FDO

PEAK TUNING

basepeak=yes set for all benchmarks

BIOS SETTINGS

Adjacent Cache Line Prefetch disabled in BIOS