



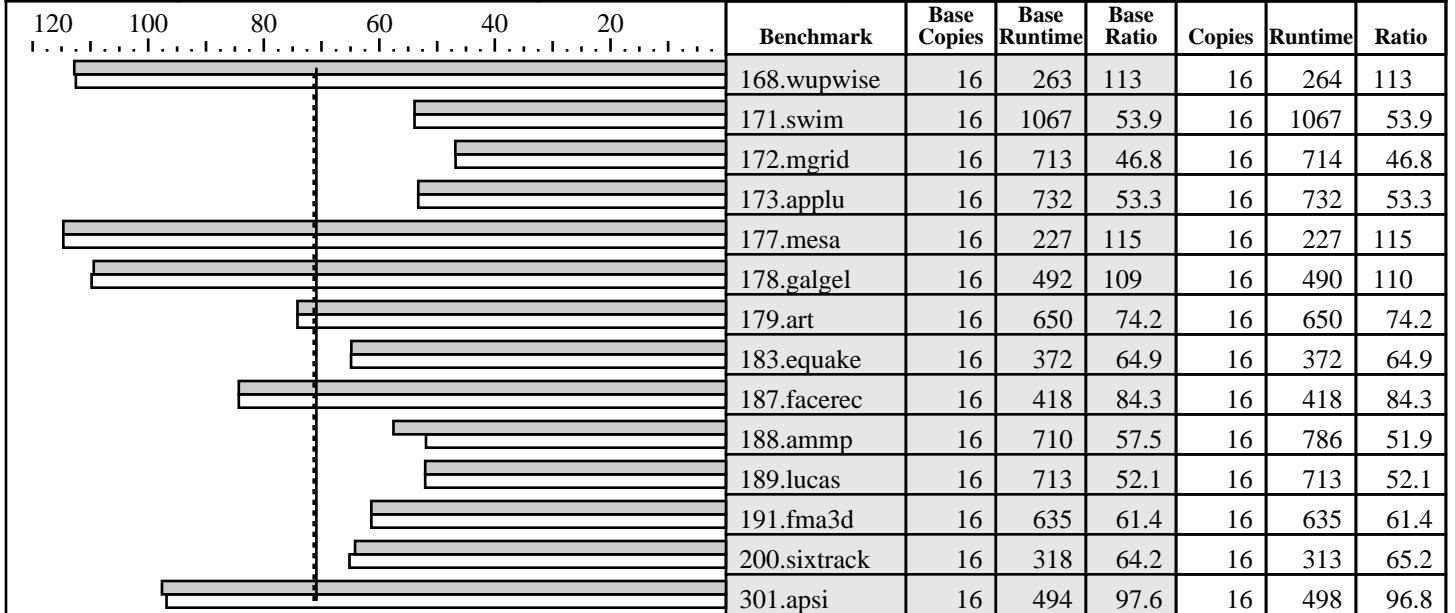
# CFP2000 Result

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**Acer Incorporated**  
Acer Altos R910 (3.0 GHz Intel Xeon Processor 7120M)

SPECfp\_rate2000 = **70.9**  
SPECfp\_rate\_base2000 = **71.4**

SPEC license #: 97 | Tested by: Acer Incorporated | Test date: Sep-2006 | Hardware Avail: Aug-2006 | Software Avail: Mar-2005



### Hardware

CPU: Intel Xeon Processor 7120M (3.0 GHz, 4MB L3, 800MHz system bus)  
 CPU MHz: 3000  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 4 chips, 2 cores/chip (Hyper-Threading Technology enabled)  
 CPU(s) orderable: 1,2,3,4  
 Parallel: No  
 Primary Cache: 12K(I) micro-ops + 16KB(D) on chip, per core  
 Secondary Cache: 1024KB(I+D) on chip, per core  
 L3 Cache: 4MB on chip (per chip), shared  
 Other Cache: N/A  
 Memory: 16 x 1024MB ECC DDR2-400 SDRAM  
 Disk Subsystem: 1 x 73GB 10000RPM SAS HDD  
 Other Hardware:

### Software

Operating System: Microsoft Windows Server 2003 Enterprise Edition (Build 3790), Service Pack 1  
 Compiler: Intel C++ 9.1 Build 20060323Z for IA32 and Fortran Compiler 9.1 Build 20060323Z for IA32  
Microsoft Visual Studio .Net 2003 (7.1.5091, for libraries)  
 shlw32M.lib is SmartHeap Library Version 7.4 from http://www.microquill.com/  
 File System: NTFS  
 System State: Default

## Notes/Tuning Information

```
+FDO: PASS1= -Qprof_gen PASS2=-Qprof_use
Base tuning for all Fortran Programs : -fast -Qansi_alias +FDO
Base tuning for all C Programs: -fast +FDO shlw32M.lib
Portability:
178.galgel: -FI /F32000000
Peak tuning:
168.wupwise: -fast -Qansi_alias +FDO
171.swim: -fast -Qansi_alias +FDO
172.mgrid: -fast -Qansi_alias +FDO
173.applu: basepeak=yes
177.mesa: basepeak=yes
178.galgel: -fast -Qansi_alias +FDO
179.art: basepeak=yes
183.quake: -fast -Oa -Qrcd +FDO shlw32M.lib
187.facerec: basepeak=yes
188.amp: -Oa -QxP
189.lucas: -fast -Qprefetch- +FDO
```



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## Notes/Tuning Information (Continued)

191.fma3d: basepeak=yes  
200.sixtrack: -Qipo -QxP +FDO  
301.apsi: -fast +FDO