



CINT2000 Result

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Hewlett-Packard Company

ProLiant DL360 G5 (3.73GHz, Intel Xeon processor 5080)

SPECint_rate2000 = 20.9

SPECint_rate_base2000 = 20.8

SPEC license #: 3 Tested by: Hewlett-Packard Company Test date: Jun-2006 Hardware Avail: Jun-2006 Software Avail: May-2006

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	1	109	14.9	1	109	14.9
175.vpr	1	120	13.6	1	117	13.9
176.gcc	1	50.3	25.4	1	50.3	25.4
181.mcf	1	94.5	22.1	1	94.5	22.1
186.crafty	1	71.5	16.2	1	71.4	16.2
197.parser	1	118	17.7	1	117	17.8
252.eon	1	51.1	29.5	1	51.1	29.5
253.perlbnk	1	83.1	25.1	1	81.5	25.6
254.gap	1	54.7	23.3	1	53.8	23.7
255.vortex	1	63.2	34.9	1	63.3	34.8
256.bzip2	1	117	14.8	1	117	14.8
300.twolf	1	158	22.0	1	158	22.0

Hardware

CPU: Intel Xeon processor 5080 (3.73GHz, 2x2MB L2, 1066MHz bus)
CPU MHz: 3730
FPU: Integrated
CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip (Hyper-Threading Technology disabled)
CPU(s) orderable: 1,2 chips
Parallel: No
Primary Cache: 12K micro-ops I + 16KBD (on chip) per core
Secondary Cache: 2048KB(I+D) (on chip) per core
L3 Cache: N/A
Other Cache: N/A
Memory: 8x1024MB PC2-5300F
Disk Subsystem: 1x36GB 10K SAS
Other Hardware:

Software

Operating System: Windows Server 2003 Enterprise SP1
Compiler: Intel C++ Compiler for 32-bit applications, (Version 9.1 Build 20060323Z)
Microsoft Visual Studio .NET 7.0.9466 (for libraries)
MicroQuill Smartheap Library 7.0
File System: NTFS
System State: Default

Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use
Base tuning for C programs: -fast +FDO ONESTEP=yes shlw32M.lib
Base tuning for C++ programs: -fast -Qcxx_features +FDO ONESTEP=yes
Portability flags:
176.gcc: -Dalloca=_alloca /F10000000
186.crafty: -DNT_i386
253.perlbnk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
Peak tuning:
164.gzip: -fast +FDO ONESTEP=yes
175.vpr: -fast +FDO ONESTEP=yes
176.gcc: basepeak=1
181.mcf: -fast +FDO ONESTEP=yes shlw32M.lib
186.crafty: -fast -Oa +FDO ONESTEP=yes shlw32M.lib
197.parser: -fast +FDO ONESTEP=yes
252.eon: -fast +FDO
253.perlbnk: -fast -Oa +FDO ONESTEP=yes shlw32M.lib
254.gap: -fast +FDO ONESTEP=yes
255.vortex: -fast +FDO ONESTEP=yes shlw32M.lib
256.bzip2: basepeak=1
```



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Notes/Tuning Information (Continued)

300.twolf: -fast +FDO shlw32M.lib
BIOS Configuration Notes
Power Regulator set to Static High Performance Mode
Hyper-Threading Technology disabled