



# CINT2000 Result

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## Hewlett-Packard Company

ProLiant ML350 G5 (3.0GHz, Intel Xeon processor 5160)

SPECint\_rate2000 = 116

SPECint\_rate\_base2000 = 115

SPEC license #: 3 Tested by: Hewlett-Packard Company Test date: Jun-2006 Hardware Avail: Jun-2006 Software Avail: May-2006

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	4	78.7	82.6	4	78.9	82.3
175.vpr	4	82.3	78.9	4	79.6	81.6
176.gcc	4	49.8	103	4	49.8	103
181.mcf	4	85.4	97.8	4	85.0	98.3
186.crafty	4	36.4	127	4	36.4	127
197.parser	4	85.3	97.9	4	84.6	98.7
252.eon	4	33.2	182	4	33.2	182
253.perlbnk	4	52.8	158	4	52.6	159
254.gap	4	56.1	91.0	4	55.6	91.8
255.vortex	4	45.8	192	4	46.0	192
256.bzip2	4	82.2	84.7	4	82.2	84.7
300.twolf	4	87.2	160	4	87.2	160

### Hardware

CPU: Intel Xeon processor 5160 (3.0GHz, 4MB L2 shared, 1333MHz bus)  
CPU MHz: 3000  
FPU: Integrated  
CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip  
CPU(s) orderable: 1,2 chips  
Parallel: No  
Primary Cache: 32KBI + 32KBD (on chip) per core  
Secondary Cache: 4096KB(I+D) (on chip) shared  
L3 Cache: N/A  
Other Cache: N/A  
Memory: 4x2048MB PC2-5300F  
Disk Subsystem: 1x146GB 15K SAS  
Other Hardware:

### Software

Operating System: Windows Server 2003 Enterprise SP1  
Compiler: Intel C++ Compiler for 32-bit applications, (Version 9.1 Build 20060323Z)  
Microsoft Visual Studio .NET 7.0.9466 (for libraries)  
MicroQuill Smartheap Library 7.0  
File System: NTFS  
System State: Default

## Notes/Tuning Information

+FDO: PASS1=-Qprof\_gen PASS2=-Qprof\_use

Base tuning for C programs: -fast +FDO ONESTEP=yes shlw32M.lib

Base tuning for C++ programs: -fast -Qcxx\_features +FDO ONESTEP=yes

Portability flags:

176.gcc: -Dalloca=\_alloca /F10000000

186.crafty: -DNT\_i386

253.perlbnk: -DSPEC\_CPU2000\_NTOS -DPERL DLL /MT

254.gap: -DSYS\_HAS\_CALLOC\_PROTO -DSYS\_HAS\_MALLOC\_PROTO

Peak tuning:

164.gzip: -fast +FDO ONESTEP=yes

175.vpr: -fast +FDO ONESTEP=yes

176.gcc: basepeak=1

181.mcf: -fast +FDO ONESTEP=yes shlw32M.lib

186.crafty: -fast -Oa +FDO ONESTEP=yes shlw32M.lib

197.parser: -fast +FDO ONESTEP=yes

252.eon: -fast +FDO

253.perlbnk: -fast -Oa +FDO ONESTEP=yes shlw32M.lib

254.gap: -fast +FDO ONESTEP=yes

255.vortex: -fast +FDO ONESTEP=yes shlw32M.lib

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## Notes/Tuning Information (Continued)

256.bzip2: basepeak=1  
300.twolf: -fast +FDO shlw32M.lib  
BIOS Configuration Notes  
Power Regulator set to Static High Performance Mode  
Adjacent Sector Prefetch Disabled