



CINT2000 Result

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Hewlett-Packard Company
HP Integrity rx2620-2 (1.3GHz/3MB Itanium 2)

SPECint_rate2000 = 13.6
SPECint_rate_base2000 = 13.6

SPEC license #: 3 Tested by: HP Richardson Test date: Dec-2004 Hardware Avail: Dec-2004 Software Avail: Jan-2005

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	1	160	10.2	1	160	10.2
175.vpr	1	146	11.1	1	146	11.1
176.gcc	1	76.5	16.7	1	76.5	16.7
181.mcf	1	156	13.4	1	156	13.4
186.crafty	1	94.3	12.3	1	94.3	12.3
197.parser	1	209	9.99	1	209	9.99
252.eon	1	81.9	18.4	1	81.9	18.4
253.perlbnk	1	153	13.6	1	153	13.6
254.gap	1	107	11.9	1	107	11.9
255.vortex	1	105	20.9	1	105	20.9
256.bzip2	1	133	13.1	1	133	13.1
300.twolf	1	227	15.3	1	227	15.3

Hardware

CPU: Intel Itanium 2 (1.3GHz/3MB, 400 MHz FSB)
CPU MHz: 1300
FPU: Integrated
CPU(s) enabled: 1 core, 1 chip, 1 core/chip
CPU(s) orderable: 1,2
Parallel: no
Primary Cache: 16KBI + 16KBD on chip, per core
Secondary Cache: 256KB (I+D) on chip, per core
L3 Cache: 3.0MB (I+D) on chip, per core
Other Cache: N/A
Memory: 24GB (12x2GB DIMMs)
Disk Subsystem: 1x146GB 15K SCSI
Other Hardware: N/A

Software

Operating System: HPUX11i-TCOE B.11.23.0409
Compiler: HP C/ANSI C Compiler C.06.00
HP aC++ Compiler C.06.00
File System: vxfs
System State: Multi-user

Notes/Tuning Information

Portability Flags

```
176.gcc : -DHOST_WORDS_BIG_ENDIAN
186.crafty : -DHP
252.eon : -DFMAX_IS_DOUBLE
253.perlbnk : -DSPEC_CPU2000_HP
254.gap : -DSPEC_CPU2000_HP -DSYS_IS_USG -DSYS_HAS_IOCTL_PROTO
        -DSYS_HAS_TIME_PROTO -DSYS_HAS_CALLOC_PROTO
```

Base Flags

```
all : +Oprofile=collect:all/+Oprofile=use
C : +Ofaster -exec +Otype_safety=ansi
C++ : +Ofaster +Otype_safety=ansi
```

Peak Flags

same as base (basepeak=true set globally)

Kernel Tunables:

```
dbc_max_pct=20
dbc_min_pct=20
maxdsiz=3221225472
```



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Notes/Tuning Information (Continued)

```
maxssiz=401604608
maxdsiz_64bit=4396972761584
maxssiz_64bit=1073741824
vps_pagesize=4096
vps_ceiling=16384
```

Notes:

The system under test had the HP-UX 11i v2 (version 11.23.0409, September, 2004) Technical Computing Operating Environment and AR1204 compilers installed, along with the following patches:

```
PHSS_31849 linker + fdp cumulative patch
PHSS_31850 assembler patch
PHSS_31851 Integrity Unwind Library
PHSS_31852 aC++ Runtime (IA: A.05.60, PA A.03.60)
PHSS_31853 Math Library Cumulative Patch
PHSS_31854 milli cumulative patch
```

EFI command "cpuconfig" used prior to booting to deconfigure other processors if necessary