



# CINT2000 Result

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## Advanced Micro Devices

Tyan Thunder K7 Motherboard, 1.2GHz Athlon MP Processor

SPECint2000 = 522

SPECint\_base2000 = 495

SPEC license #: 49 Tested by: AMD Test date: May-2001 Hardware Avail: Jun-2001 Software Avail: Nov-2000

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio	300 600 900 1200			
164.gzip	1400	230	608	226	619	[Bar chart showing ratio 619]			
175.vpr	1400	439	319	409	342	[Bar chart showing ratio 342]			
176.gcc	1100	308	357	256	430	[Bar chart showing ratio 430]			
181.mcf	1800	786	229	776	232	[Bar chart showing ratio 232]			
186.crafty	1000	150	665	145	692	[Bar chart showing ratio 692]			
197.parser	1800	413	436	415	434	[Bar chart showing ratio 434]			
252.eon	1300	155	839	124	1046	[Bar chart showing ratio 1046]			
253.perlbnk	1800	237	759	241	748	[Bar chart showing ratio 748]			
254.gap	1100	189	581	190	579	[Bar chart showing ratio 579]			
255.vortex	1900	249	764	239	796	[Bar chart showing ratio 796]			
256.bzip2	1500	374	401	354	423	[Bar chart showing ratio 423]			
300.twolf	3000	720	417	713	421	[Bar chart showing ratio 421]			

### Hardware

CPU: 1.2GHz AMD Athlon MP AHD1200AMS3C  
CPU MHz: 1200  
FPU: Integrated  
CPU(s) enabled: 1 core, 1 chip, 1 core/chip  
CPU(s) orderable: 1, 2  
Parallel: No  
Primary Cache: 64KBI + 64KBD on chip  
Secondary Cache: 256KB(I+D) on chip  
L3 Cache: N/A  
Other Cache: N/A  
Memory: 256MB PC2100 DDR SDRAM CL2 Registered  
Disk Subsystem: IDE IBM Deskstar DTLA-307020  
Other Hardware: None

### Software

Operating System: Windows 2000 SP1  
Compiler: Intel C/C++ 5.0 build 001120  
Microsoft Visual Studio 6.0 (libraries)  
MicroQuill Smartheap Library 5.0  
File System: NTFS  
System State: Default

## Notes/Tuning Information

shlw32M.lib is the SmartHeap library V5.0 from MicroQuill [www.microquill.com](http://www.microquill.com)

+FDO: PASS1=-Qprof\_gen PASS2=-Qprof\_use

Portability:

176.gcc: -Dalloca=\_alloca /F10000000

186.crafty: -DNT\_i386

253.perlbnk: -DSPEC\_CPU2000\_NTOS -DPERLDLL /MT

254.gap: -DSYS\_HAS\_CALLOC\_PROTO -DSYS\_HAS\_MALLOC\_PROTO

Baseline C: -QxK -Qipo +FDO shlw32M.lib

Baseline C++: -QxK -Qipo -GX -GR

Peak tuning:

164.gzip: -QxK -Qipo -Oi- +FDO

175.vpr: -Qxi -QaxK -Qwp\_ipo +FDO

176.gcc: -QxiM -QaxK -Qipo -Oi- +FDO shlw32M.lib

181.mcf: -Qxi -QaxK -Qwp\_ipo +FDO

186.crafty: -Qxi -Qwp\_ipo +FDO

197.parser: -Qxi -QaxK -Qwp\_ipo +FDO

252.eon: -QxiM -Qwp\_ipo +FDO

253.perlbnk: -QxiM -Qwp\_ipo +FDO shlw32M.lib

254.gap: -QaxK -Qwp\_ipo +FDO shlw32M.lib

255.vortex: -QxiM -Qwp\_ipo -Oa -Oi- +FDO shlw32M.lib

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<http://www.spec.org>



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## Notes/Tuning Information (Continued)

```
256.bzip2:    -Qxi -QaxK -Oa -Qwp_ipo
300.twolf:    -QxK -Qipo -Oa +FDO shlw32M.lib
ONESTEP is used for all base and peak runs
```

The tested system can be assembled using a case such as the Chenming 601 AE-F-O,  
a 460W power supply such as the Delta Electronics Model DPS-465 AB A  
The System bus runs at 266MHz