



# SPEC® OMPG2012 Result

Copyright 2012-2014 Standard Performance Evaluation Corporation

## Intel

### SPECompG\_peak2012 = 17.3

### S4TR1SY4Q (Intel Xeon E7-4890 v2)

### SPECompG\_base2012 = 16.3

OMP2012 license:13

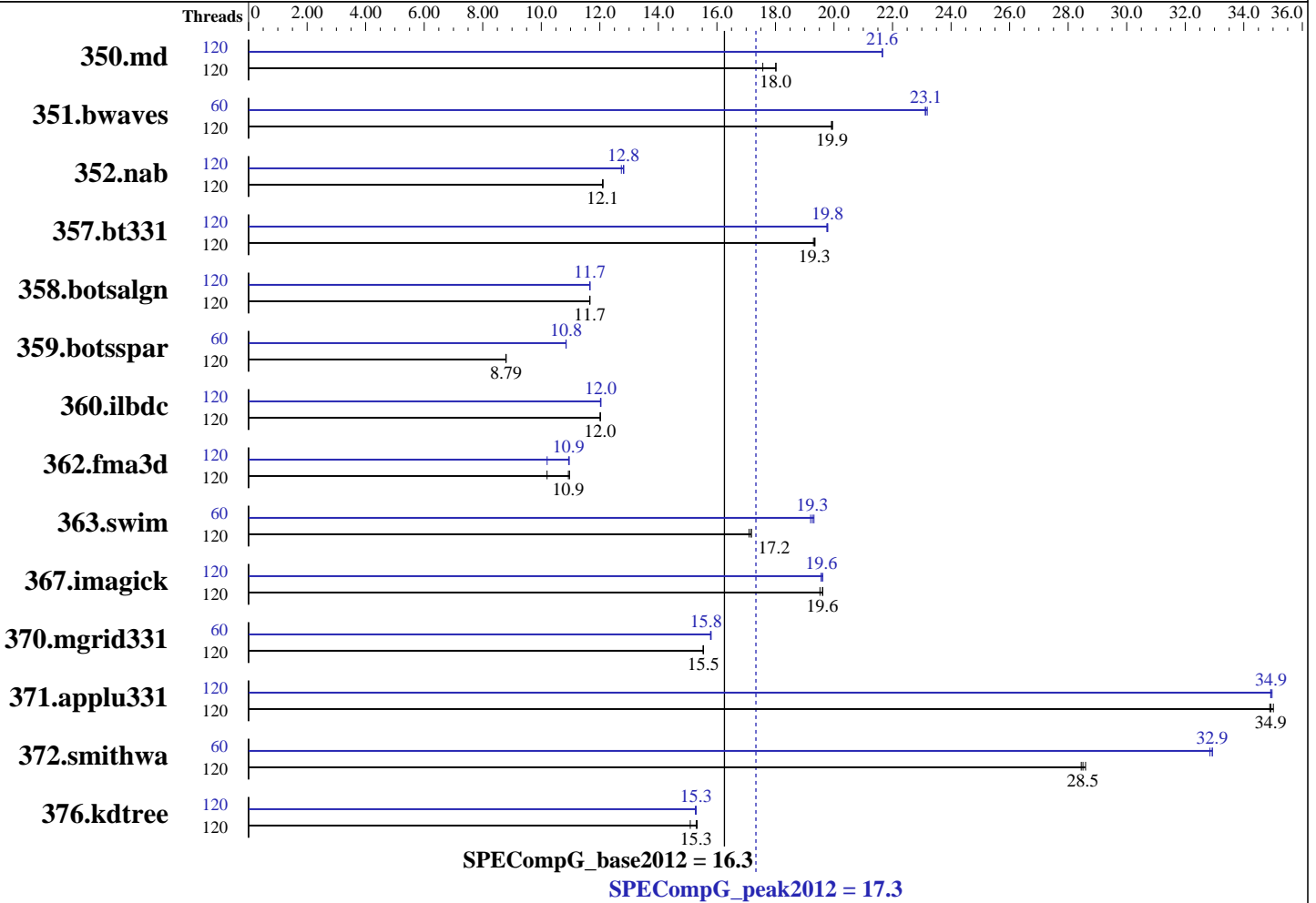
Test sponsor: Intel

Tested by: Intel

Test date: Feb-2014

Hardware Availability: Feb-2014

Software Availability: Jun-2013



### Hardware

CPU Name: Intel Xeon E7-4890 v2  
 CPU Characteristics: 2800  
 CPU MHz: 2800  
 CPU MHz Maximum: 2800  
 FPU: Integrated  
 CPU(s) enabled: 60 cores, 4 chips, 15 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2,3,4 Chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 38400 KB I+D on chip per chip  
 Other Cache: None  
 Memory: 512 GB (32 x 16 GB 2Rx4 PC3-12800R-11, ECC)  
 Disk Subsystem: Panasas ActiveStor 14  
 Other Hardware: --  
 Base Threads Run: 120  
 Minimum Peak Threads: 60

Continued on next page

### Software

Operating System: Red Hat Enterprise Linux Server release 6.4  
 Compiler: C/C++/Fortran: Version 13.1.3 of Intel Composer XE for Linux Build 20130607  
 Auto Parallel: No  
 File System: Linux ext3  
 System State: Default  
 Base Pointers: 64-bit  
 Peak Pointers: 64-bit  
 Other Software: None



# SPEC OMPG2012 Result

Copyright 2012-2014 Standard Performance Evaluation Corporation

Intel

SPECompG\_peak2012 = 17.3

S4TR1SY4Q (Intel Xeon E7-4890 v2)

SPECompG\_base2012 = 16.3

OMP2012 license:13

Test date: Feb-2014

Test sponsor: Intel

Hardware Availability: Feb-2014

Tested by: Intel

Software Availability: Jun-2013

Maximum Peak Threads: 120

## Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
350.md	120	<u>257</u>	<u>18.0</u>	257	18.0	264	17.6	120	214	21.7	<u>214</u>	<u>21.6</u>	214	21.6
351.bwaves	120	227	20.0	<u>227</u>	<u>19.9</u>	227	19.9	60	<u>196</u>	<u>23.1</u>	196	23.1	195	23.2
352.nab	120	321	12.1	<u>322</u>	<u>12.1</u>	322	12.1	120	305	12.7	303	12.8	<u>304</u>	<u>12.8</u>
357.bt331	120	<u>245</u>	<u>19.3</u>	245	19.4	246	19.3	120	<u>240</u>	<u>19.8</u>	240	19.8	240	19.8
358.botsalgn	120	373	11.7	373	11.7	<u>373</u>	<u>11.7</u>	120	373	11.7	<u>373</u>	<u>11.7</u>	373	11.7
359.botsspar	120	<u>597</u>	<u>8.79</u>	597	8.79	597	8.80	60	484	10.8	<u>484</u>	<u>10.8</u>	484	10.8
360.ilbdc	120	296	12.0	296	12.0	<u>296</u>	<u>12.0</u>	120	296	12.0	<u>296</u>	<u>12.0</u>	296	12.0
362.fma3d	120	<u>348</u>	<u>10.9</u>	347	11.0	373	10.2	120	373	10.2	<u>348</u>	<u>10.9</u>	347	11.0
363.swim	120	264	17.2	<u>264</u>	<u>17.2</u>	265	17.1	60	235	19.3	<u>235</u>	<u>19.3</u>	236	19.2
367.imagick	120	358	19.6	360	19.5	<u>359</u>	<u>19.6</u>	120	359	19.6	<u>359</u>	<u>19.6</u>	358	19.6
370.mgrid331	120	285	15.5	<u>284</u>	<u>15.5</u>	284	15.5	60	280	15.8	280	15.8	<u>280</u>	<u>15.8</u>
371.applu331	120	174	34.9	173	35.0	<u>174</u>	<u>34.9</u>	120	<u>174</u>	<u>34.9</u>	174	34.9	173	35.0
372.smithwa	120	<u>188</u>	<u>28.5</u>	188	28.4	187	28.6	60	163	32.8	<u>163</u>	<u>32.9</u>	163	32.9
376.kdtree	120	298	15.1	<u>294</u>	<u>15.3</u>	294	15.3	120	<u>294</u>	<u>15.3</u>	294	15.3	295	15.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Platform Notes

```

Sysinfo program /panfs/projects/innl/aknyazel/OMP2012/1.0/Docs/sysinfo
$Rev: 395 $ $Date:: 2012-07-25 $# 8f8c0fe9e19c658963ale67685e50647
running on eix05 Sat Feb 1 05:53:57 2014

```

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/omp2012/Docs/config.html#sysinfo>

```

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E7-4890 v2 @ 2.80GHz
4 "physical id"s (chips)
120 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores : 15
siblings : 30
physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14
physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14
physical 2: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14
physical 3: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14
cache size : 38400 KB

```

Continued on next page



# SPEC OMPG2012 Result

Copyright 2012-2014 Standard Performance Evaluation Corporation

Intel

SPECompG\_peak2012 = 17.3

S4TR1SY4Q (Intel Xeon E7-4890 v2)

SPECompG\_base2012 = 16.3

OMP2012 license:13

Test date: Feb-2014

Test sponsor: Intel

Hardware Availability: Feb-2014

Tested by: Intel

Software Availability: Jun-2013

## Platform Notes (Continued)

```

From /proc/meminfo
  MemTotal:      529160992 kB
  HugePages_Total:      0
  Hugepagesize:    2048 kB

/usr/bin/lsb_release -d
  Red Hat Enterprise Linux Server release 6.4 (Santiago)

From /etc/*release* /etc/*version*
  redhat-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
  system-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
  system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

uname -a:
  Linux eix05 2.6.32-358.6.2.el6.x86_64.crt1 #4 SMP Fri May 17 15:33:33 MDT
  2013 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Jan 27 20:46

SPEC is set to: /panfs/projects/innl/aknyazel/OMP2012/1.0
Filesystem      Type      Size  Used Avail Use% Mounted on
panfs://36.101.211.1/projects
  panfs        28T    18T   9.7T  65% /panfs/projects

Cannot run dmidecode; consider saying 'chmod +s /usr/sbin/dmidecode'

(End of data from sysinfo program)

```

## General Notes

```

=====
BIOS settings: Default
  Intel Turbo Boost Technology (Turbo) : Disabled
  Transparent Huge Pages Disabled via "echo never > /sys/kernel/mm/redhat_transparent_hugepage/enabled"

=====
General OMP Library Settings
  ENV_KMP_LIBRARY=throughput
  ENV_KMP_STACKSIZE=190M
  ENV_KMP_BLOCKTIME=infinite
  ENV_OMP_DYNAMIC=FALSE
  ENV_OMP_NESTED=FALSE
  ENV_OMP_SCHEDULE=static

=====
General base OMP Library Settings
  ENV_KMP_AFFINITY=compact,0,granularity=fine

=====
General peak OMP Library Settings
  ENV_KMP_AFFINITY=compact,0,granularity=fine

```

Continued on next page



# SPEC OMPG2012 Result

Copyright 2012-2014 Standard Performance Evaluation Corporation

Intel

SPECompG\_peak2012 = 17.3

S4TR1SY4Q (Intel Xeon E7-4890 v2)

SPECompG\_base2012 = 16.3

OMP2012 license:13

Test sponsor: Intel

Tested by: Intel

Test date: Feb-2014

Hardware Availability: Feb-2014

Software Availability: Jun-2013

## General Notes (Continued)

=====  
Per benchmark peak OMP Library Settings

=====  
351.bwaves:peak:  
ENV\_KMP\_AFFINITY=compact,1,granularity=fine  
ENV\_OMP\_SCHEDULE=static,1  
=====

357.bt331:peak:  
ENV\_OMP\_SCHEDULE=static,1  
=====

359.botsspar:peak:  
ENV\_KMP\_AFFINITY=compact,1,granularity=fine  
ENV\_OMP\_SCHEDULE=guided  
=====

362.fma3d:peak:  
ENV\_OMP\_SCHEDULE=static,1  
=====

363.swim:peak:  
ENV\_KMP\_AFFINITY=compact,1,granularity=fine  
=====

370.mgrid331:peak:  
ENV\_KMP\_AFFINITY=compact,1,granularity=fine  
=====

372.smithwa:peak:  
ENV\_OMP\_SCHEDULE=static,1  
ENV\_KMP\_AFFINITY=compact,1,granularity=fine  
=====

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc

Fortran benchmarks:  
ifort



# SPEC OMPG2012 Result

Copyright 2012-2014 Standard Performance Evaluation Corporation

Intel

SPECompG\_peak2012 = 17.3

S4TR1SY4Q (Intel Xeon E7-4890 v2)

SPECompG\_base2012 = 16.3

OMP2012 license:13

Test sponsor: Intel

Tested by: Intel

Test date: Feb-2014

Hardware Availability: Feb-2014

Software Availability: Jun-2013

## Base Portability Flags

350.md: -FR  
357.bt331: -mcmmodel=medium  
363.swim: -mcmmodel=medium  
367.imagick: -std=c99

## Base Optimization Flags

C benchmarks:  
-O2 -openmp -ipo -xAVX -ansi-alias  
C++ benchmarks:  
-O2 -openmp -ipo -xAVX -ansi-alias  
Fortran benchmarks:  
-O2 -openmp -ipo -xAVX -align array64byte

## Peak Compiler Invocation

C benchmarks:  
icc  
C++ benchmarks:  
icpc  
Fortran benchmarks:  
ifort

## Peak Portability Flags

350.md: -FR  
357.bt331: -mcmmodel=medium  
363.swim: -mcmmodel=medium  
367.imagick: -std=c99

## Peak Optimization Flags

C benchmarks:  
352.nab: -O3 -openmp -ipo -xAVX -fno-alias -opt-malloc-options=1  
-opt-calloc -fp-model fast=2 -no-prec-div -no-prec-sqrt  
-ansi-alias

Continued on next page



# SPEC OMPG2012 Result

Copyright 2012-2014 Standard Performance Evaluation Corporation

Intel

SPECompG\_peak2012 = 17.3

S4TR1SY4Q (Intel Xeon E7-4890 v2)

SPECompG\_base2012 = 16.3

OMP2012 license:13

Test sponsor: Intel

Tested by: Intel

Test date: Feb-2014

Hardware Availability: Feb-2014

Software Availability: Jun-2013

## Peak Optimization Flags (Continued)

358.botsalgn: -O2 -openmp -ipo -xAVX -ansi-alias

359.botsspar: -O3 -openmp -ipo -xAVX -fno-alias -ansi-alias

367.imagick: Same as 358.botsalgn

372.smithwa: -O2 -openmp -ipo -xSSE4.2 -fno-alias  
-opt-streaming-stores always -opt-malloc-options=1  
-ansi-alias

C++ benchmarks:

-O3 -openmp -ipo -xAVX -fno-alias -ansi-alias

Fortran benchmarks:

350.md: -O2 -openmp -ipo -xAVX -fno-alias -opt-malloc-options=1  
-fp-model fast=2 -no-prec-div -no-prec-sqrt  
-align array64byte

351.bwaves: -O3 -openmp -ipo -xAVX -fno-alias -fp-model fast=2  
-no-prec-div -no-prec-sqrt -align array64byte

357.bt331: Same as 351.bwaves

360.ilbdc: -O3 -openmp -ipo -xAVX -opt-malloc-options=1  
-align array64byte

362.fma3d: -O3 -openmp -ipo -xAVX -fno-alias -align array64byte

363.swim: -O3 -openmp -ipo -xSSE4.2 -fno-alias  
-opt-streaming-stores always -opt-malloc-options=3  
-align array64byte

370.mgrid331: -O2 -openmp -ipo -xSSE4.2 -fno-alias  
-opt-malloc-options=3 -align array64byte

371.applu331: -O2 -openmp -ipo -xAVX -align array64byte

The flags file that was used to format this result can be browsed at

<http://www.spec.org/omp2012/flags/Intel-ic13.0-linux64.20140219.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/omp2012/flags/Intel-ic13.0-linux64.20140219.xml>



# SPEC OMPG2012 Result

Copyright 2012-2014 Standard Performance Evaluation Corporation

Intel

SPECompG\_peak2012 = 17.3

S4TR1SY4Q (Intel Xeon E7-4890 v2)

SPECompG\_base2012 = 16.3

OMP2012 license:13

Test sponsor: Intel

Tested by: Intel

Test date: Feb-2014

Hardware Availability: Feb-2014

Software Availability: Jun-2013

SPEC is a registered trademark of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC OMP2012 v1.0.  
Report generated on Tue Jul 22 13:37:58 2014 by SPEC OMP2012 PS/PDF formatter v541.  
Originally published on 19 February 2014.