



SPEC CPU®2017 Integer Rate Result

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Dell Inc.

SPECrate®2017_int_base = 208

PowerEdge R670 (Intel Xeon 6507P)

SPECrate®2017_int_peak = 215

CPU2017 License: 6573

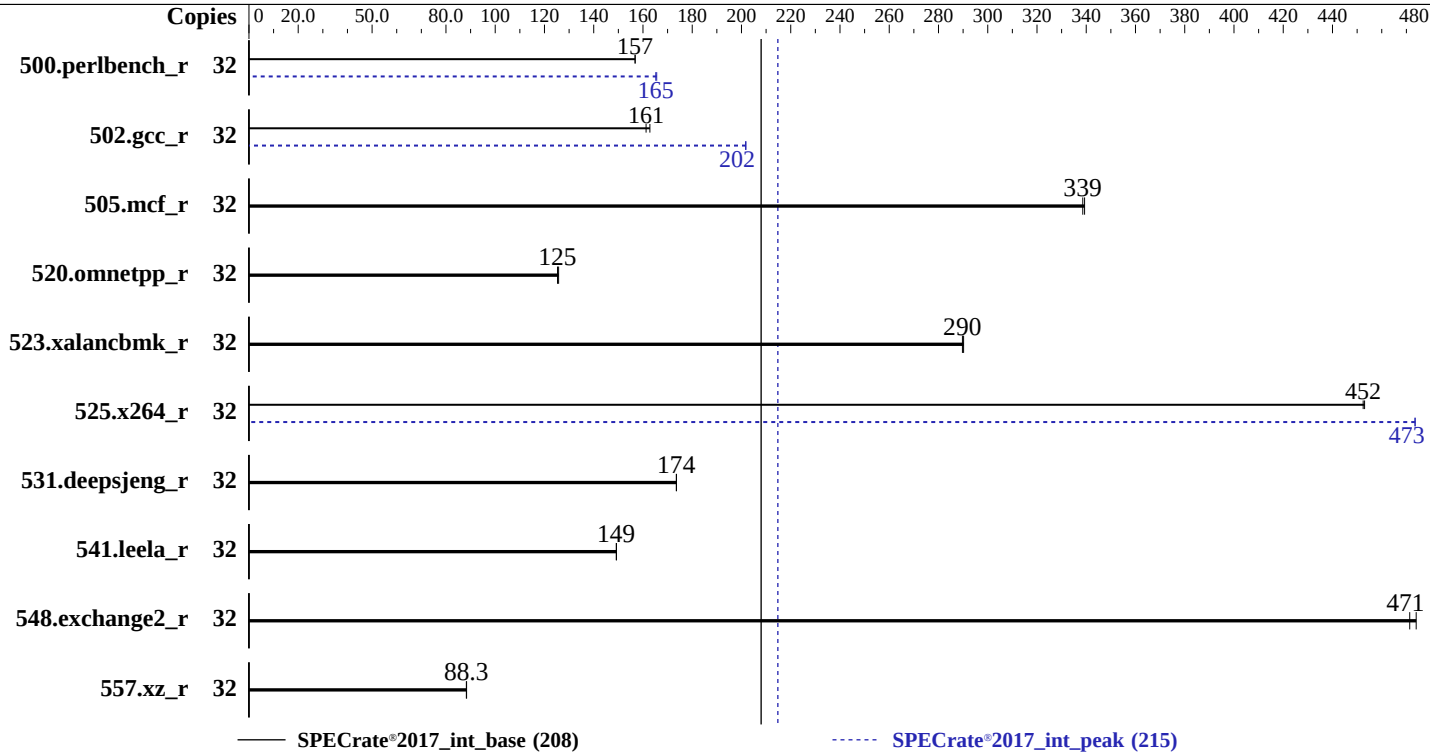
Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Jul-2025

Hardware Availability: Apr-2025

Software Availability: Jun-2024



Hardware

CPU Name: Intel Xeon 6507P
Max MHz: 4300
Nominal: 3500
Enabled: 16 cores, 2 chips, 2 threads/core
Orderable: 1,2 chips
Cache L1: 64 KB I + 48 KB D on chip per core
L2: 2 MB I+D on chip per core
L3: 48 MB I+D on chip per chip
Other: None
Memory: 1 TB (16 x 64 GB 2Rx4 PC5-6400B-R)
Storage: 50 GB on tmpfs
Other: CPU Cooling: DLC

Software

OS: SUSE Linux Enterprise Server 15 SP6
6.4.0-150600.21-default
Compiler: C/C++: Version 2024.1 of Intel oneAPI DPC++/C++
Compiler for Linux;
Fortran: Version 2024.1 of Intel Fortran Compiler
for Linux;
Parallel: No
Firmware: Version 1.3.2 released May-2025
File System: tmpfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other: jemalloc memory allocator V5.0.1
Power Management: BIOS set to prefer performance at the cost of
additional power usage.



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	32	<u>325</u>	<u>157</u>	325	157			32	<u>308</u>	<u>165</u>	308	166		
502.gcc_r	32	278	163	<u>281</u>	<u>161</u>			32	<u>225</u>	<u>202</u>	225	202		
505.mcf_r	32	<u>153</u>	<u>339</u>	152	339			32	<u>153</u>	<u>339</u>	152	339		
520.omnetpp_r	32	334	126	<u>335</u>	<u>125</u>			32	334	126	<u>335</u>	<u>125</u>		
523.xalancbmk_r	32	116	290	<u>117</u>	<u>290</u>			32	116	290	<u>117</u>	<u>290</u>		
525.x264_r	32	124	453	<u>124</u>	<u>452</u>			32	<u>118</u>	<u>473</u>	118	474		
531.deepsjeng_r	32	211	174	<u>211</u>	<u>174</u>			32	211	174	<u>211</u>	<u>174</u>		
541.leela_r	32	<u>355</u>	<u>149</u>	355	149			32	<u>355</u>	<u>149</u>	355	149		
548.exchange2_r	32	<u>178</u>	<u>471</u>	177	474			32	<u>178</u>	<u>471</u>	177	474		
557.xz_r	32	391	88.4	<u>391</u>	<u>88.3</u>			32	391	88.4	<u>391</u>	<u>88.3</u>		

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:

LD_LIBRARY_PATH =

"/mnt/ramdisk/cpu2017-1.1.9-ic2024.1/lib/intel64:/mnt/ramdisk/cpu2017-1.1.9-ic2024.1/lib/ia32:/mnt/ramdisk/cpu2017-1.1.9-ic2024.1/je5.0.1-32"

MALLOC_CONF = "retain:true"

General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM
memory using Red Hat Enterprise Linux 8.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

Benchmark run from a 50 GB ramdisk created with the cmd: "mount -t tmpfs -o size=50G tmpfs /mnt/ramdisk"



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Platform Notes

BIOS Settings:

DCU Streamer Prefetcher : Disabled
Sub NUMA Cluster : Enabled
Dead Line LLC Alloc : Disabled
Optimizer Mode : Enabled

System Profile : Custom
CPU Power Management : Maximum Performance
C1E : Disabled
C-States : Autonomous
Latency Optimized Mode : Enabled
Energy Efficient Policy : Performance

CPU Interconnect Bus -
Link Power Management : Disabled
PCI ASPM L1 Link Power Management : Disabled
DIMM Self Healing -
on Uncorrectable Memory Error : Disabled

Sysinfo program /mnt/ramdisk/cpu2017-1.1.9-ic2024.1/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on 1234567-R670 Wed Jul 23 17:46:38 2025

SUT (System Under Test) info as seen by some common utilities.

Table of contents

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. sysctl
16. /sys/kernel/mm/transparent_hugepage
17. /sys/kernel/mm/transparent_hugepage/khugepaged
18. OS release
19. Disk information
20. /sys/devices/virtual/dmi/id
21. dmidecode
22. BIOS

1. uname -a
Linux 1234567-R670 6.4.0-150600.21-default #1 SMP PREEMPT_DYNAMIC Thu May 16 11:09:22 UTC 2024 (36c1e09/lp)
x86_64 x86_64 x86_64 GNU/Linux

2. w
17:46:38 up 2 min, 1 user, load average: 0.16, 0.12, 0.05

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Platform Notes (Continued)

```
USER      TTY      FROM          LOGIN@  IDLE   JCPU   PCPU WHAT
root      tty1      -            17:45   38.00s 0.80s  0.00s /bin/bash
/home/DellFiles/bin/Intel/dell-run-speccpu.sh rate --define DL-VERS=6.3 --output_format html,pdf,txt
```

3. Username

From environment variable \$USER: root

4. ulimit -a

```
core file size          (blocks, -c) unlimited
data seg size           (kbytes, -d) unlimited
scheduling priority     (-e) 0
file size               (blocks, -f) unlimited
pending signals         (-i) 4126697
max locked memory       (kbytes, -l) 8192
max memory size         (kbytes, -m) unlimited
open files              (-n) 1024
pipe size               (512 bytes, -p) 8
POSIX message queues    (bytes, -q) 819200
real-time priority      (-r) 0
stack size              (kbytes, -s) unlimited
cpu time                (seconds, -t) unlimited
max user processes      (-u) 4126697
virtual memory          (kbytes, -v) unlimited
file locks              (-x) unlimited
```

5. sysinfo process ancestry

```
/usr/lib/systemd/systemd --switched-root --system --deserialize=42
login -- root
-bash
/bin/bash /home/DellFiles/bin/DELL_rate.sh
/bin/bash /home/DellFiles/bin/dell-run-main.sh rate
/bin/bash /home/DellFiles/bin/dell-run-main.sh rate
/bin/bash /home/DellFiles/bin/Intel/dell-run-speccpu.sh rate --define DL-VERS=6.3 --output_format
html,pdf,txt
/bin/bash /home/DellFiles/bin/Intel/dell-run-speccpu.sh rate --define DL-VERS=6.3 --output_format
html,pdf,txt
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=32 -c
ic2024.1-lin-sapphirerapids-rate-20240308.cfg --define smt-on --define cores=16 --define physicalfirst
--define invoke_with_interleave --define drop_caches --tune base,peak -o all --iterations 2 --define
DL-VERS=6.3 --output_format html,pdf,txt intrate
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=32 --configfile
ic2024.1-lin-sapphirerapids-rate-20240308.cfg --define smt-on --define cores=16 --define physicalfirst
--define invoke_with_interleave --define drop_caches --tune base,peak --output_format all --iterations 2
--define DL-VERS=6.3 --output_format html,pdf,txt --nopower --runmode rate --tune base:peak --size refrate
intrate --nopreenv --note-preenv --logfile $SPEC/tmp/CPU2017.001/templogs/preenv.intrate.001.0.log
--lognum 001.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /mnt/ramdisk/cpu2017-1.1.9-ic2024.1
```

6. /proc/cpuinfo

```
model name      : Intel(R) Xeon(R) 6507P
vendor_id      : GenuineIntel
cpu family     : 6
model          : 173
stepping       : 1
microcode      : 0x10003c2
```

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Platform Notes (Continued)

bugs : spectre_v1 spectre_v2 spec_store_bypass swaggs bhi
cpu cores : 8
siblings : 16
2 physical ids (chips)
32 processors (hardware threads)
physical id 0: core ids 0-7
physical id 1: core ids 0-7
physical id 0: apicids 0-15
physical id 1: apicids 128-143

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

7. lscpu

From lscpu from util-linux 2.39.3:

```
Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Address sizes:          52 bits physical, 57 bits virtual
Byte Order:             Little Endian
CPU(s):                 32
On-line CPU(s) list:    0-31
Vendor ID:              GenuineIntel
BIOS Vendor ID:         Intel
Model name:              Intel(R) Xeon(R) 6507P
BIOS Model name:         Intel(R) Xeon(R) 6507P   CPU @ 3.5GHz
BIOS CPU family:         179
CPU family:              6
Model:                   173
Thread(s) per core:      2
Core(s) per socket:      8
Socket(s):               2
Stepping:                1
BogoMIPS:                7000.00
Flags:                   fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat
                        pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx
                        pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good
                        nopl xtopology nonstop_tsc cpuid aperfmperf tsc_known_freq pni
                        pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
                        xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt
                        tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm
                        3dnowprefetch cpuid_fault epb cat_l3 cat_l2 cdp_l3 intel_ppin cdp_l2
                        ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow flexpriority ept
                        vpid ept_ad fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid
                        rtm cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt
                        clwb intel_pt avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec
                        xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local
                        split_lock_detect user_shstk avx_vnni avx512_bf16 wbnoinvd dtherm ida
                        arat pln pts vnm1 avx512vbmi umip pku ospke waitpkg avx512_vbmi2 gfni
                        vaes vpclmulqdq avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57
                        rdpid bus_lock_detect cldemote movdiri movdir64b enqcmd fsrm md_clear
                        serialize tsxldtrk pconfig arch_lbr ibt amx_bf16 avx512_fp16 amx_tile
                        amx_int8 flush_l1d arch_capabilities
Virtualization:          VT-x
L1d cache:               768 KiB (16 instances)
L1i cache:               1 MiB (16 instances)
L2 cache:                 32 MiB (16 instances)
L3 cache:                 96 MiB (2 instances)
NUMA node(s):            2
NUMA node0 CPU(s):       0, 2, 4, 6, 8, 10, 12, 14, 16, 18, 20, 22, 24, 26, 28, 30
```

(Continued on next page)



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Platform Notes (Continued)

NUMA node1 CPU(s): 1, 3, 5, 7, 9, 11, 13, 15, 17, 19, 21, 23, 25, 27, 29, 31
Vulnerability Gather data sampling: Not affected
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf: Not affected
Vulnerability Mds: Not affected
Vulnerability Meltdown: Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Reg file data sampling: Not affected
Vulnerability Retbleed: Not affected
Vulnerability Spec rstack overflow: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Enhanced / Automatic IBRS; IBPB conditional; RSB filling; PBRSE-IBRS Not affected; BHI BHI_DIS_S
Vulnerability Srbds: Not affected
Vulnerability Tsx async abort: Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	768K	12	Data	1	64	1	64
L1i	64K	1M	16	Instruction	1	64	1	64
L2	2M	32M	16	Unified	2	2048	1	64
L3	48M	96M	16	Unified	3	49152	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 2 nodes (0-1)
node 0 cpus: 0, 2, 4, 6, 8, 10, 12, 14, 16, 18, 20, 22, 24, 26, 28, 30
node 0 size: 515665 MB
node 0 free: 504096 MB
node 1 cpus: 1, 3, 5, 7, 9, 11, 13, 15, 17, 19, 21, 23, 25, 27, 29, 31
node 1 size: 516079 MB
node 1 free: 514605 MB
node distances:
node 0 1
0: 10 21
1: 21 10

9. /proc/meminfo

MemTotal: 1056506436 kB

10. who -r

run-level 3 Jul 23 17:44

11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)

Default Target Status
multi-user running

12. Services, from systemctl list-unit-files

STATE	UNIT FILES
enabled	ModemManager YaST2-Firstboot YaST2-Second-Stage apparmor appstream-sync-cache auditd cron display-manager firewalld getty@ irqbalance issue-generator kbdsettings klog lvm2-monitor nsd nvme-fc-boot-connections nvme-fc-autoconnect postfix purge-kernels rollback rsyslog smartd sshd systemd-pstore wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny wpa_supplicant

(Continued on next page)



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Platform Notes (Continued)

enabled-runtime systemd-remount-fs
disabled NetworkManager NetworkManager-dispatcher NetworkManager-wait-online accounts-daemon autofs
autoyast-initscripts blk-availability boot-sysctl ca-certificates chrony-wait chronyd
console-getty cups cups-browsed debug-shell dmraid-activation dnsmasq ebttables
exchange-bmc-os-info fsidd gpm grub2-once haveged hwloc-dump-hwdata ipmi ipmiev
issue-add-ssh-keys kexec-load ksm kvm_stat lunmask man-db-create multipathd nfs nfs-blkmap
nmb openvpn@ ostree-remount rpcbind rpmconfigcheck rsyncd rtkit-daemon serial-getty@
smartd_generate_opts smb snmpd snmptrapd speech-dispatcherd systemd-boot-check-no-failures
systemd-confext systemd-network-generator systemd-sysextd systemd-time-wait-sync
systemd-timesyncd udisks2 update-system-flatpaks upower vncserver@ wpa_supplicant@
indirect pcsd saned@ systemd-userdbd wickedd

13. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=/boot/vmlinuz-6.4.0-150600.21-default
root=UUID=f6b7b82f-7376-4741-aad6-a16a8014b2fb
splash=silent
resume=/dev/disk/by-uuid/3bfae0e2-56ee-4845-bdbe-561ecf485c49
mitigations=auto
quiet
security=apparmor

14. cpupower frequency-info
analyzing CPU 2:
Unable to determine current policy
boost state support:
Supported: yes
Active: yes

15. sysctl
kernel.numa_balancing 1
kernel.randomize_va_space 2
vm.compaction_proactiveness 20
vm.dirty_background_bytes 0
vm.dirty_background_ratio 10
vm.dirty_bytes 0
vm.dirty_expire_centisecs 3000
vm.dirty_ratio 20
vm.dirty_writeback_centisecs 500
vm.dirtytime_expire_seconds 43200
vm.extfrag_threshold 500
vm.min_unmapped_ratio 1
vm.nr_hugepages 0
vm.nr_hugepages_mempolicy 0
vm.nr_overcommit_hugepages 0
vm.swappiness 60
vm.watermark_boost_factor 15000
vm.watermark_scale_factor 10
vm.zone_reclaim_mode 0

16. /sys/kernel/mm/transparent_hugepage
defrag always defer defer+madvice [madvice] never
enabled [always] madvice never
hpage_pmd_size 2097152
shmem_enabled always within_size advise [never] deny force

(Continued on next page)



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Platform Notes (Continued)

17. /sys/kernel/mm/transparent_hugepage/khugepaged

```
alloc_sleep_millisecs 60000
defrag 1
max_ptes_none 511
max_ptes_shared 256
max_ptes_swap 64
pages_to_scan 4096
scan_sleep_millisecs 10000
```

18. OS release

```
From /etc/*-release /etc/*-version
os-release SUSE Linux Enterprise Server 15 SP6
```

19. Disk information

SPEC is set to: /mnt/ramdisk/cpu2017-1.1.9-ic2024.1

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
tmpfs	tmpfs	50G	5.0G	46G	10%	/mnt/ramdisk

20. /sys/devices/virtual/dmi/id

```
Vendor: Dell Inc.
Product: PowerEdge R670
Product Family: PowerEdge
Serial: 1234567
```

21. dmidecode

Additional information from dmidecode 3.4 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

```
2x 002C069D002C MTC40F2046S1RC64BD1 USFF 64 GB 2 rank 6400
1x 00AD063200AD HMC694AHBRA283N 64 GB 2 rank 6400
11x 00CE042300CE M321R8GA0PB2-CCPEC 64 GB 2 rank 6400
2x 00CE063200CE M321R8GA0PB1-CCPQC 64 GB 2 rank 6400
```

22. BIOS

(This section combines info from /sys/devices and dmidecode.)

```
BIOS Vendor: Dell Inc.
BIOS Version: 1.3.2
BIOS Date: 05/15/2025
BIOS Revision: 1.3
```

Compiler Version Notes

C | 502.gcc_r(peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

C | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak)

(Continued on next page)



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Compiler Version Notes (Continued)

| 557.xz_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

C | 502.gcc_r(peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

C | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak)
| 557.xz_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

C++ | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base, peak) 531.deepsjeng_r(base, peak)
| 541.leela_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

Fortran | 548.exchange2_r(base, peak)

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Base Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64

(Continued on next page)



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Software Availability: Jun-2024

Base Portability Flags (Continued)

502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/opt/intel/oneapi/compiler/2024.1/lib -lqkmalloc

C++ benchmarks:

-w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/opt/intel/oneapi/compiler/2024.1/lib -lqkmalloc

Fortran benchmarks:

-w -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-L/opt/intel/oneapi/compiler/2024.1/lib -lqkmalloc

Peak Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx



SPEC CPU®2017 Integer Rate Result

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Dell Inc.

SPECrate®2017_int_base = 208

PowerEdge R670 (Intel Xeon 6507P)

SPECrate®2017_int_peak = 215

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Jul-2025

Hardware Availability: Apr-2025

Software Availability: Jun-2024

Peak Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Peak Optimization Flags

C benchmarks:

500.perlbench_r: -w -std=c11 -m64 -Wl, -z,muldefs
-fprofile-generate(pass 1)
-fprofile-use=default.profddata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4
-fno-strict-overflow
-L/opt/intel/oneapi/compiler/2024.1/lib -lqkmallo

502.gcc_r: -m32 -L/opt/intel/oneapi/compiler/2024.1/lib32 -std=gnu89
-Wl, -z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profddata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4
-L/usr/local/jemalloc32-5.0.1/lib -ljemalloc

505.mcf_r: basepeak = yes

525.x264_r: -w -std=c11 -m64 -Wl, -z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fno-alias
-L/opt/intel/oneapi/compiler/2024.1/lib -lqkmallo

557.xz_r: basepeak = yes

C++ benchmarks:

520.omnetpp_r: basepeak = yes

(Continued on next page)



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Hardware Availability: Apr-2025

Software Availability: Jun-2024

Peak Optimization Flags (Continued)

523.xalancbmk_r: basepeak = yes

531.deepsjeng_r: basepeak = yes

541.leela_r: basepeak = yes

Fortran benchmarks:

548.exchange2_r: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.html>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-Xeon-v1.13.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.xml>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-Xeon-v1.13.xml>

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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