



SPEC CPU®2017 Floating Point Rate Result

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Dell Inc.

SPECrate®2017_fp_base = 529

PowerEdge R770 (Intel Xeon 6515P)

SPECrate®2017_fp_peak = 544

CPU2017 License: 6573

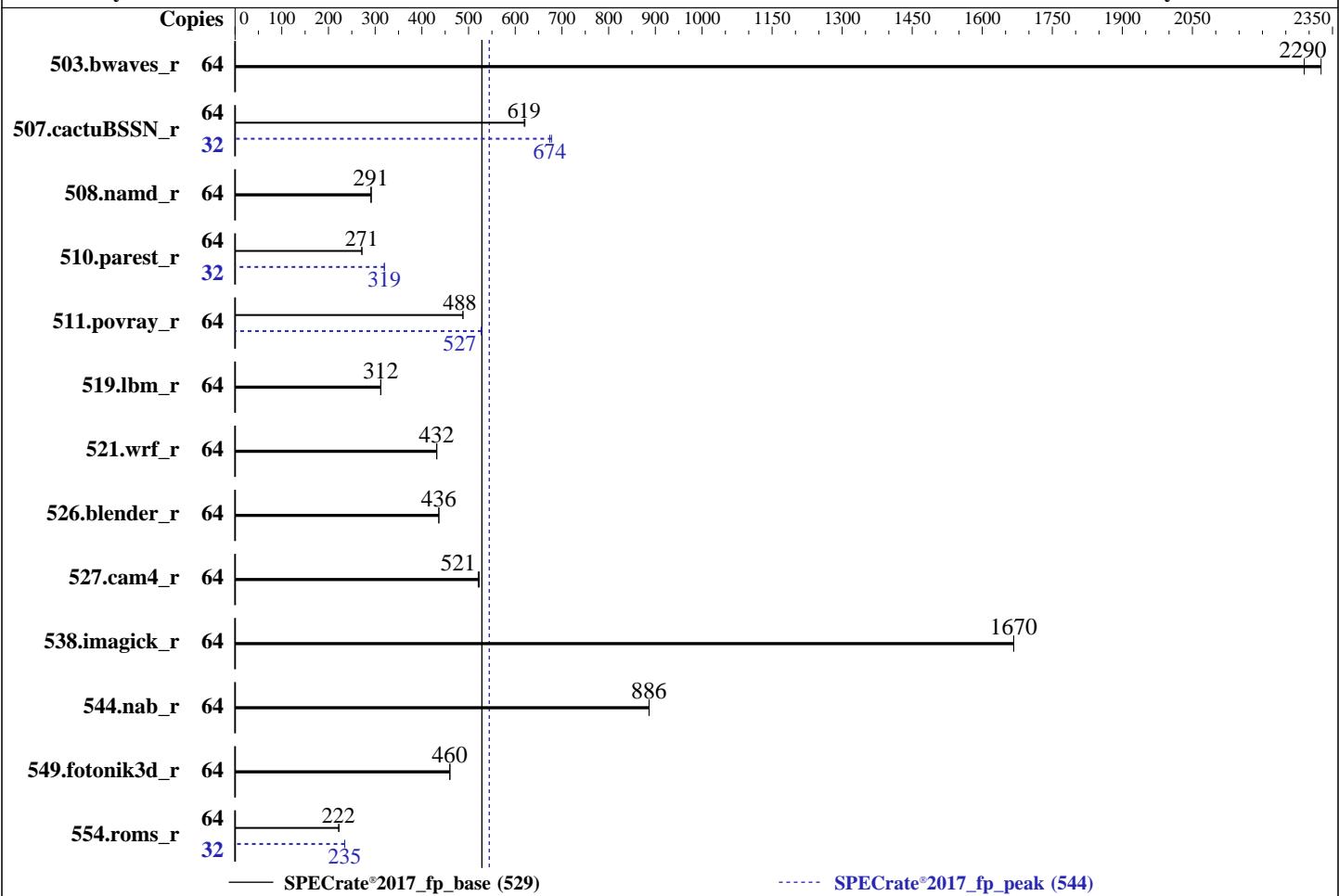
Test Date: Jun-2025

Test Sponsor: Dell Inc.

Hardware Availability: Apr-2025

Tested by: Dell Inc.

Software Availability: Jun-2024



— SPECrate®2017_fp_base (529)
----- SPECrate®2017_fp_peak (544)

Hardware

CPU Name: Intel Xeon 6515P
 Max MHz: 3800
 Nominal: 2300
 Enabled: 32 cores, 2 chips, 2 threads/core
 Orderable: 1,2 chips
 Cache L1: 64 KB I + 48 KB D on chip per core
 L2: 2 MB I+D on chip per core
 L3: 72 MB I+D on chip per chip
 Other: None
 Memory: 512 GB (16 x 32 GB 2Rx8 PC5-6400B-R)
 Storage: 60 GB on tmpfs
 Other: CPU Cooling: Air

OS:

SUSE Linux Enterprise Server 15 SP6
 6.4.0-150600.21-default

Compiler:
 C/C++: Version 2024.1 of Intel oneAPI DPC++/C++
 Compiler for Linux;
 Fortran: Version 2024.1 of Intel Fortran Compiler
 for Linux;

Parallel:
 Firmware:
 File System:
 System State:
 Base Pointers:
 Peak Pointers:
 Other:
 Power Management:

No
 Version 1.3.2 released May-2025
 tmpfs
 Run level 5 (graphical multi-user)
 64-bit
 64-bit
 jemalloc memory allocator V5.0.1
 BIOS set to prefer performance at the cost of
 additional power usage.

Software



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	64	276	2330	280	2290			64	276	2330	280	2290		
507.cactusBSSN_r	64	131	621	131	619			32	60.1	674	59.8	678		
508.namd_r	64	208	292	209	291			64	208	292	209	291		
510.parest_r	64	616	272	617	271			32	262	319	262	320		
511.povray_r	64	306	488	306	488			64	283	528	284	527		
519.lbm_r	64	216	312	216	312			64	216	312	216	312		
521.wrf_r	64	332	432	332	432			64	332	432	332	432		
526.blender_r	64	223	436	223	436			64	223	436	223	436		
527.cam4_r	64	215	521	214	523			64	215	521	214	523		
538.imagick_r	64	95.5	1670	95.5	1670			64	95.5	1670	95.5	1670		
544.nab_r	64	121	887	122	886			64	121	887	122	886		
549.fotonik3d_r	64	542	460	542	460			64	542	460	542	460		
554.roms_r	64	459	222	457	223			32	216	235	217	235		
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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH =
  "/mnt/ramdisk/cpu2017-1.1.9-ic2024.1/lib/intel64:/mnt/ramdisk/cpu2017-1.1.9-ic2024.1/je5.0.1-64"
MALLOC_CONF = "retain:true"
```

General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Red Hat Enterprise Linux 8.4

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

```
sync; echo 3> /proc/sys/vm/drop_caches
```

runcpu command invoked through numactl i.e.:

```
numactl --interleave=all runcpu <etc>
```

jemalloc, a general purpose malloc implementation

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General Notes (Continued)

built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

Benchmark run from a 60 GB ramdisk created with the cmd: "mount -t tmpfs -o size=60G tmpfs /mnt/ramdisk"

Platform Notes

BIOS Settings:

```
DCU Streamer Prefetcher : Disabled
    Sub NUMA Cluster : Enabled
    Dead Line LLC Alloc : Disabled
    Optimizer Mode : Enabled

    System Profile : Custom
    CPU Power Management : Maximum Performance
        C1E : Disabled
        C-States : Autonomous
    Latency Optimized Mode : Enabled
    Energy Efficient Policy : Performance
    CPU Interconnect Bus -
        Link Power Management : Disabled
    PCI ASPM L1 Link Power Management : Disabled
    Correctable Memory ECC SMI : Disabled
        DIMM Self Healing -
            on Uncorrectable Memory Error : Disabled
```

Sysinfo program /mnt/ramdisk/cpu2017-1.1.9-ic2024.1/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on 1234567-R770 Fri Jun 20 03:42:31 2025

SUT (System Under Test) info as seen by some common utilities.

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- ```
1. uname -a
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3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
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9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. sysctl
16. /sys/kernel/mm/transparent_hugepage
17. /sys/kernel/mm/transparent_hugepage/khugepaged
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20. /sys/devices/virtual/dmi/id
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```
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## Platform Notes (Continued)

```
1. uname -a
Linux 1234567-R770 6.4.0-150600.21-default #1 SMP PREEMPT_DYNAMIC Thu May 16 11:09:22 UTC 2024 (36c1e09)
x86_64 x86_64 x86_64 GNU/Linux

2. w
03:42:31 up 13 min, 2 users, load average: 0.14, 0.04, 0.01
USER TTY FROM LOGIN@ IDLE JCPU PCPU WHAT
root tty3 - 03:32 47.00s 0.85s 0.00s /bin/bash
/home/DellFiles/bin/Intel/dell-run-speccpu.sh rate --define DL-VERS=6.3 --output_format html,txt

3. Username
From environment variable $USER: root

4. ulimit -a
core file size (blocks, -c) unlimited
data seg size (kbytes, -d) unlimited
scheduling priority (-e) 0
file size (blocks, -f) unlimited
pending signals (-i) 2060680
max locked memory (kbytes, -l) 8192
max memory size (kbytes, -m) unlimited
open files (-n) 1024
pipe size (512 bytes, -p) 8
POSIX message queues (bytes, -q) 819200
real-time priority (-r) 0
stack size (kbytes, -s) unlimited
cpu time (seconds, -t) unlimited
max user processes (-u) 2060680
virtual memory (kbytes, -v) unlimited
file locks (-x) unlimited

5. sysinfo process ancestry
/usr/lib/systemd/systemd --switched-root --system --deserialize=31
login -- root
-bash
/bin/bash /home/DellFiles/bin/DELL_rate.sh
/bin/bash /home/DellFiles/bin/dell-run-main.sh rate
/bin/bash /home/DellFiles/bin/dell-run-main.sh rate
/bin/bash /home/DellFiles/bin/Intel/dell-run-speccpu.sh rate --define DL-VERS=6.3 --output_format
html,txt
/bin/bash /home/DellFiles/bin/Intel/dell-run-speccpu.sh rate --define DL-VERS=6.3 --output_format
html,txt
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=64 -c
ic2024.1-lin-sapphirerapids-rate-20240308.cfg --define smt-on --define cores=32 --define physicalfirst
--define invoke_with_interleave --define drop_caches --tune base,peak -o all --iterations 2 --define
DL-VERS=6.3 --output_format html,txt fprate
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=64 --configfile
ic2024.1-lin-sapphirerapids-rate-20240308.cfg --define smt-on --define cores=32 --define physicalfirst
--define invoke_with_interleave --define drop_caches --tune base,peak --output_format all --iterations 2
--define DL-VERS=6.3 --output_format html,txt --nopower --runmode rate --tune base:peak --size reffrate
fprate --nopreenv --note-preenv --logfile $SPEC/tmp/CPU2017.001/templogs/preenv.fprate.001.0.log --lognum
001.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /mnt/ramdisk/cpu2017-1.1.9-ic2024.1
```

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## Platform Notes (Continued)

```
6. /proc/cpuinfo
model name : Intel(R) Xeon(R) 6515P
vendor_id : GenuineIntel
cpu family : 6
model : 173
stepping : 1
microcode : 0x10003c2
bugs : spectre_v1 spectre_v2 spec_store_bypass swapgs bhi
cpu cores : 16
siblings : 32
2 physical ids (chips)
64 processors (hardware threads)
physical id 0: core ids 0-15
physical id 1: core ids 0-15
physical id 0: apicids 0-31
physical id 1: apicids 128-159
Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
virtualized systems. Use the above data carefully.
```

```
7. lscpu
```

```
From lscpu from util-linux 2.39.3:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Address sizes: 52 bits physical, 57 bits virtual
Byte Order: Little Endian
CPU(s): 64
On-line CPU(s) list: 0-63
Vendor ID: GenuineIntel
BIOS Vendor ID: Intel
Model name: Intel(R) Xeon(R) 6515P
BIOS Model name: Intel(R) Xeon(R) 6515P CPU @ 2.3GHz
BIOS CPU family: 179
CPU family: 6
Model: 173
Thread(s) per core: 2
Core(s) per socket: 16
Socket(s): 2
Stepping: 1
BogoMIPS: 4600.00
Flags:
fpu vme de pse tsc msr pae mce cx8 apic sep mttr pge mca cmov pat
pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx
pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good
nopl xtopology nonstop_tsc cpuid aperf mperf tsc_known_freq pnpi
pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt
tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm
3dnowprefetch cpuid_fault epb cat_l3 cat_l2 cdp_l3 intel_ppin cdp_l2
ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow flexpriority ept
vpid ept_ad fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid
rtm cqmq rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt
clwb intel_pt avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec
xgetbv1 xsaves cqmq_llc cqmq_occur_llc cqmq_mbm_total cqmq_mbm_local
split_lock_detect user_shstck avx_vnni avx512_bf16 wbnoinvd dtherm ida
arat pln pts vnmi avx512vbmi umip pku ospke waitpkg avx512_vbmi2 gfni
vaes vpclmulqdq avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57
rdpid bus_lock_detect cldemote movdiri movdir64b enqcmd fsrm md_clear
```

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## Platform Notes (Continued)

```

serialize tsxldtrk pconfig arch_lbr ibt amx_bf16 avx512_fp16 amx_tile
amx_int8 flush_lid arch_capabilities
VT-x
1.5 MiB (32 instances)
2 MiB (32 instances)
64 MiB (32 instances)
144 MiB (2 instances)
2
0,2,4,6,8,10,12,14,16,18,20,22,24,26,28,30,32,34,36,38,40,42,44,46,48
,50,52,54,56,58,60,62
1,3,5,7,9,11,13,15,17,19,21,23,25,27,29,31,33,35,37,39,41,43,45,47,49
,51,53,55,57,59,61,63
Vulnerability Gather data sampling: Not affected
Vulnerability Itlb multihit: Not affected
Vulnerability Llrf: Not affected
Vulnerability Mds: Not affected
Vulnerability Meltdown: Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Reg file data sampling: Not affected
Vulnerability Retbleed: Not affected
Vulnerability Spec rstack overflow: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Enhanced / Automatic IBRS; IBPB conditional; RSB filling;
PBRSB-eIBRS Not affected; BHI BHI_DIS_S
Vulnerability Srbds: Not affected
Vulnerability Tsx async abort: Not affected

```

From lscpu --cache:

| NAME | ONE-SIZE | ALL-SIZE | WAYS | TYPE        | LEVEL | SETS  | PHY-LINE | COHERENCY-SIZE |
|------|----------|----------|------|-------------|-------|-------|----------|----------------|
| L1d  | 48K      | 1.5M     | 12   | Data        | 1     | 64    | 1        | 64             |
| L1i  | 64K      | 2M       | 16   | Instruction | 1     | 64    | 1        | 64             |
| L2   | 2M       | 64M      | 16   | Unified     | 2     | 2048  | 1        | 64             |
| L3   | 72M      | 144M     | 16   | Unified     | 3     | 73728 | 1        | 64             |

-----  
8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 2 nodes (0-1)

node 0 cpus: 0,2,4,6,8,10,12,14,16,18,20,22,24,26,28,30,32,34,36,38,40,42,44,46,48,50,52,54,56,58,60,62

node 0 size: 257541 MB

node 0 free: 246394 MB

node 1 cpus: 1,3,5,7,9,11,13,15,17,19,21,23,25,27,29,31,33,35,37,39,41,43,45,47,49,51,53,55,57,59,61,63

node 1 size: 257654 MB

node 1 free: 255471 MB

node distances:

node 0 1

0: 10 21

1: 21 10

-----  
9. /proc/meminfo

MemTotal: 527560164 kB

-----  
10. who -r

run-level 5 Jun 20 03:29

-----  
11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)

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## Platform Notes (Continued)

Default Target Status  
graphical running

-----  
12. Services, from systemctl list-unit-files  
STATE UNIT FILES  
enabled YaST2-Firstboot YaST2-Second-Stage apparmor auditd bluetooth cron display-manager getty@  
irqbalance issue-generator kbdsettings kdump kdump-early kdump-notify lvm2-monitor  
nvmefc-boot-connections nvmf-autoconnect postfix purge-kernels rollback sshd  
systemd-pstore wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny  
enabled-runtime systemd-remount-fs  
disabled accounts-daemon autoyast-initscripts blk-availability bluetooth-mesh boot-sysctl  
ca-certificates chrony-wait chronyd console-getty cups cups-browsed debug-shell ebttables  
fcoe firewalld fsidd grub2-once haveged issue-add-ssh-keys kexec-load lldpad lunmask  
mariadb mariadb@ nfs nfs-blkmap nfs-server nfsserver nginx nmb rmt-server  
rmt-server-migration rmt-server-mirror rmt-server-sync rmt-server-systems-scc-sync rpcbind  
rpmconfigcheck rsyncd rtkit-daemon serial-getty@ smb speech-dispatcherd sync41  
systemd-boot-check-no-failures systemd-context systemd-network-generator systemd-sysext  
systemd-time-wait-sync systemd-timesyncd target targetcli targetclid udisks2 upower  
indirect systemd-userdbd wickedd

-----  
13. Linux kernel boot-time arguments, from /proc/cmdline  
BOOT\_IMAGE=/boot/vmlinuz-6.4.0-150600.21-default  
root=UUID=0da04382-047a-42b3-a599-217a8ccf8f05  
splash=silent  
resume=/dev/disk/by-uuid/6a44a562-4c9d-472d-a94f-958a78796243  
mitigations=auto  
quiet  
security=apparmor  
crashkernel=364M,high  
crashkernel=72M,low

-----  
14. cpupower frequency-info  
analyzing CPU 57:  
Unable to determine current policy  
boost state support:  
Supported: yes  
Active: yes

-----  
15. sysctl  
kernel.numa\_balancing 1  
kernel.randomize\_va\_space 2  
vm.compaction\_proactiveness 20  
vm.dirty\_background\_bytes 0  
vm.dirty\_background\_ratio 10  
vm.dirty\_bytes 0  
vm.dirty\_expire\_centisecs 3000  
vm.dirty\_ratio 20  
vm.dirty\_writeback\_centisecs 500  
vm.dirtytime\_expire\_seconds 43200  
vm.extfrag\_threshold 500  
vm.min\_unmapped\_ratio 1  
vm.nr\_hugepages 0  
vm.nr\_hugepages\_mempolicy 0  
vm.nr\_overcommit\_hugepages 0  
vm.swappiness 60  
vm.watermark\_boost\_factor 15000

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## Platform Notes (Continued)

vm.watermark\_scale\_factor 10  
vm.zone\_reclaim\_mode 0

-----  
16. /sys/kernel/mm/transparent\_hugepage  
defrag always defer defer+madvise [madvise] never  
enabled [always] madvise never  
hpage\_pmd\_size 2097152  
shmem\_enabled always within\_size advise [never] deny force

-----  
17. /sys/kernel/mm/transparent\_hugepage/khugepaged  
alloc\_sleep\_millisecs 60000  
defrag 1  
max\_ptes\_none 511  
max\_ptes\_shared 256  
max\_ptes\_swap 64  
pages\_to\_scan 4096  
scan\_sleep\_millisecs 10000

-----  
18. OS release  
From /etc/\*-release /etc/\*-version  
os-release SUSE Linux Enterprise Server 15 SP6

-----  
19. Disk information  
SPEC is set to: /mnt/ramdisk/cpu2017-1.1.9-ic2024.1  
Filesystem Type Size Used Avail Use% Mounted on  
tmpfs tmpfs 60G 5.0G 56G 9% /mnt/ramdisk

-----  
20. /sys/devices/virtual/dmi/id  
Vendor: Dell Inc.  
Product: PowerEdge R770  
Product Family: PowerEdge  
Serial: 1234567

-----  
21. dmidecode  
Additional information from dmidecode 3.4 follows. WARNING: Use caution when you interpret this section.  
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.  
Memory:  
14x 00AD063200AD HMCG88AHBRA292N 32 GB 2 rank 6400  
2x 00CE063200CE M321R4GA3PB1-CCPPC 32 GB 2 rank 6400

-----  
22. BIOS  
(This section combines info from /sys/devices and dmidecode.)  
BIOS Vendor: Dell Inc.  
BIOS Version: 1.3.2  
BIOS Date: 05/15/2025  
BIOS Revision: 1.3



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## Compiler Version Notes

```
=====
C | 519.lbm_r(base, peak) 538.imagick_r(base, peak) 544.nab_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

```

```
=====
C++ | 508.namd_r(base, peak) 510.parest_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

```

```
=====
C++, C | 511.povray_r(base, peak) 526.blender_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

```

```
=====
C++, C, Fortran | 507.cactuBSSN_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
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Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

```

```
=====
Fortran | 503.bwaves_r(base, peak) 549.fotonik3d_r(base, peak) 554.roms_r(base, peak)

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

```

```
=====
Fortran, C | 521.wrf_r(base, peak) 527.cam4_r(base, peak)

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
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```

## Base Compiler Invocation

C benchmarks:

icx

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Dell Inc.

SPECCrate®2017\_fp\_base = 529

PowerEdge R770 (Intel Xeon 6515P)

SPECCrate®2017\_fp\_peak = 544

CPU2017 License: 6573

Test Date: Jun-2025

Test Sponsor: Dell Inc.

Hardware Availability: Apr-2025

Tested by: Dell Inc.

Software Availability: Jun-2024

## Base Compiler Invocation (Continued)

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using both C and C++:

icpx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx

## Base Portability Flags

503.bwaves\_r: -DSPEC\_LP64  
507.cactuBSSN\_r: -DSPEC\_LP64  
508.namd\_r: -DSPEC\_LP64  
510.parest\_r: -DSPEC\_LP64  
511.povray\_r: -DSPEC\_LP64  
519.lbm\_r: -DSPEC\_LP64  
521.wrf\_r: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG -convert big\_endian  
526.blender\_r: -DSPEC\_LP64 -DSPEC\_LINUX -funsigned-char  
527.cam4\_r: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG  
538.imagick\_r: -DSPEC\_LP64  
544.nab\_r: -DSPEC\_LP64  
549.fotonik3d\_r: -DSPEC\_LP64  
554.roms\_r: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math  
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-Wno-implicit-int -mprefer-vector-width=512 -ljemalloc  
-L/usr/local/jemalloc64-5.0.1/lib

C++ benchmarks:

-w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast  
-ffast-math -flto -mfpmath=sse -funroll-loops

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

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SPECrate®2017\_fp\_peak = 544

CPU2017 License: 6573

Test Date: Jun-2025

Test Sponsor: Dell Inc.

Hardware Availability: Apr-2025

Tested by: Dell Inc.

Software Availability: Jun-2024

## Base Optimization Flags (Continued)

C++ benchmarks (continued):

```
-qopt-mem-layout-trans=4 -mprefer-vector-width=512 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

Fortran benchmarks:

```
-w -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math -futto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using both Fortran and C:

```
-w -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math
-futto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-Wno-implicit-int -mprefer-vector-width=512 -nostandard-realloc-lhs
-align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using both C and C++:

```
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -futto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -mprefer-vector-width=512
-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using Fortran, C, and C++:

```
-w -m64 -std=c++14 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -futto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -mprefer-vector-width=512
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

## Peak Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

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# SPEC CPU®2017 Floating Point Rate Result

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Dell Inc.

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SPECrate®2017\_fp\_peak = 544

CPU2017 License: 6573

Test Date: Jun-2025

Test Sponsor: Dell Inc.

Hardware Availability: Apr-2025

Tested by: Dell Inc.

Software Availability: Jun-2024

## Peak Compiler Invocation (Continued)

Benchmarks using both C and C++:

icpx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

519.lbm\_r: basepeak = yes

538.imagick\_r: basepeak = yes

544.nab\_r: basepeak = yes

C++ benchmarks:

508.namd\_r: basepeak = yes

510.parest\_r: -w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids  
-Ofast -ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -mprefer-vector-width=512  
-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

Fortran benchmarks:

503.bwaves\_r: basepeak = yes

549.fotonik3d\_r: basepeak = yes

554.roms\_r: -w -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast  
-ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -nostandard-realloc-lhs  
-align array32byte -auto -ljemalloc  
-L/usr/local/jemalloc64-5.0.1/lib

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# SPEC CPU®2017 Floating Point Rate Result

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## Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:

521.wrf\_r: basepeak = yes

527.cam4\_r: basepeak = yes

Benchmarks using both C and C++:

```
511.povray_r: -w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs
-fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4 -Wno-implicit-int
-mprefer-vector-width=512 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

526.blender\_r: basepeak = yes

Benchmarks using Fortran, C, and C++:

```
-w -m64 -std=c++14 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -mprefer-vector-width=512
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.html>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-Xeon-v1.13.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.xml>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-Xeon-v1.13.xml>

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