



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

## Tyrone Systems

(Test Sponsor: Netweb Technologies India Ltd)

Tyrone Camarero SDI200A2N-224  
(2.8 GHz, Intel Xeon Platinum 8562Y+)

**SPECrate®2017\_int\_base = 694**

**SPECrate®2017\_int\_peak = 721**

CPU2017 License: 006802

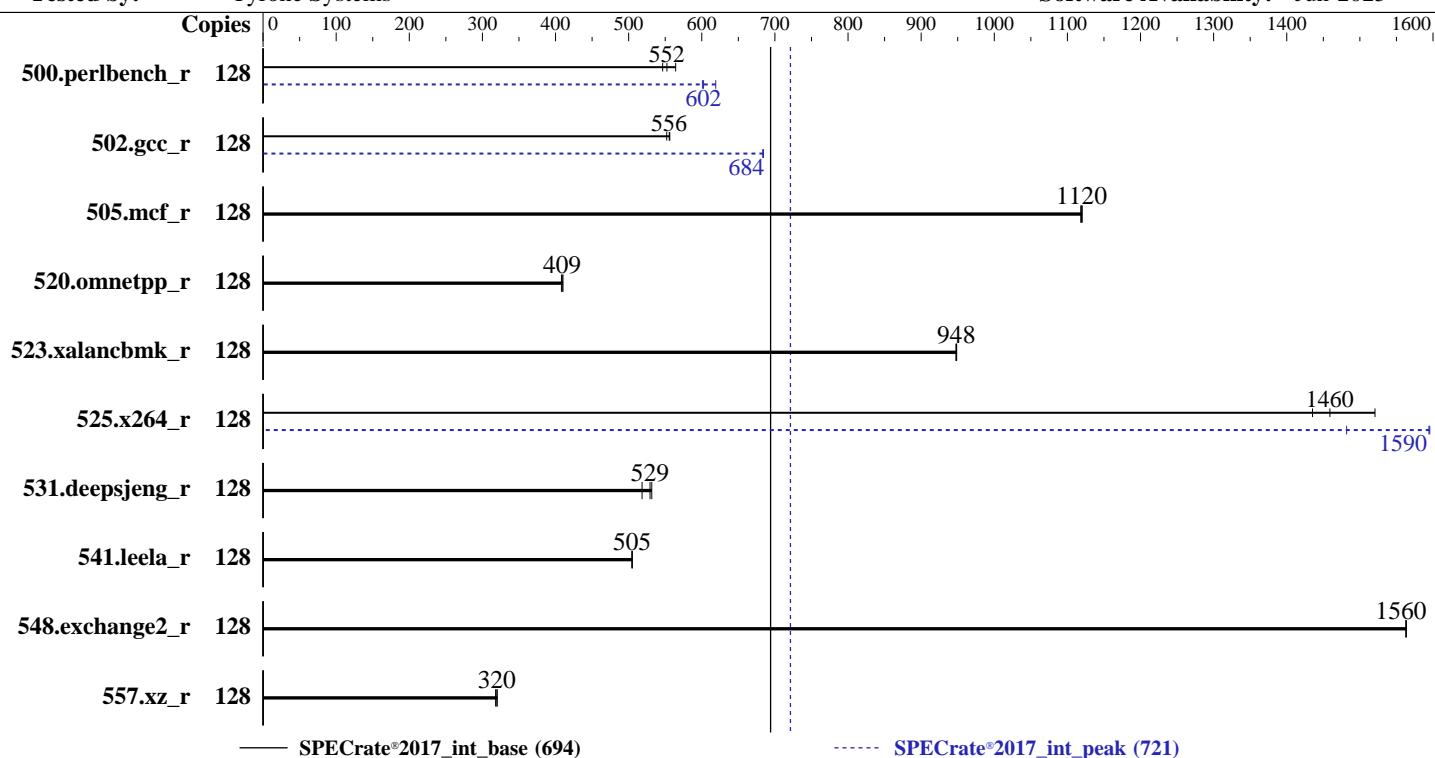
Test Date: Jul-2025

Test Sponsor: Netweb Technologies India Ltd

Hardware Availability: Jan-2024

Tested by: Tyrone Systems

Software Availability: Jun-2025



## Hardware

CPU Name: Intel Xeon Platinum 8562Y+  
Max MHz: 4100  
Nominal: 2800  
Enabled: 64 cores, 2 chips, 2 threads/core  
Orderable: 1,2 chips  
Cache L1: 32 KB I + 48 KB D on chip per core  
L2: 2 MB I+D on chip per core  
L3: 60 MB I+D on chip per chip  
Other: None  
Memory: 1 TB (16 x 64 GB 2Rx4 PC5-5600B-R)  
Storage: 1 x 480 GB NVMe  
Other: CPU Cooling: Air

## Software

OS: Ubuntu 22.04.1 LTS  
Compiler: 5.15.0-143-generic  
C/C++: Version 2024.0.2 of Intel oneAPI DPC++/C++ Compiler for Linux;  
Fortran: Version 2024.0.2 of Intel Fortran Compiler for Linux;  
Parallel: No  
Firmware: Version 2.6 released May-2025  
File System: ext4  
System State: Run level 3 (Multi-user)  
Base Pointers: 64-bit  
Peak Pointers: 32/64-bit  
Other: jemalloc memory allocator V5.0.1  
Power Management: BIOS and set to prefer performance at cost of additional power.



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

## Tyrone Systems

(Test Sponsor: Netweb Technologies India Ltd)

Tyrone Camarero SDI200A2N-224  
(2.8 GHz, Intel Xeon Platinum 8562Y+)

**SPECrate®2017\_int\_base = 694**

**SPECrate®2017\_int\_peak = 721**

CPU2017 License: 006802

Test Date: Jul-2025

Test Sponsor: Netweb Technologies India Ltd

Hardware Availability: Jan-2024

Tested by: Tyrone Systems

Software Availability: Jun-2025

## Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	128	361	564	<b>369</b>	<b>552</b>	373	546	128	339	601	<b>338</b>	<b>602</b>	329	619		
502.gcc_r	128	328	552	<b>326</b>	<b>556</b>	326	556	128	265	683	<b>265</b>	<b>684</b>	265	684		
505.mcf_r	128	185	1120	<b>185</b>	<b>1120</b>	185	1120	128	185	1120	<b>185</b>	<b>1120</b>	185	1120		
520.omnetpp_r	128	409	410	<b>410</b>	<b>409</b>	412	408	128	409	410	<b>410</b>	<b>409</b>	412	408		
523.xalancbmk_r	128	142	949	<b>143</b>	<b>948</b>	143	948	128	142	949	<b>143</b>	<b>948</b>	143	948		
525.x264_r	128	147	1520	<b>154</b>	<b>1460</b>	156	1440	128	151	1480	<b>141</b>	<b>1590</b>	140	1600		
531.deepsjeng_r	128	276	532	<b>277</b>	<b>529</b>	283	518	128	276	532	<b>277</b>	<b>529</b>	283	518		
541.leela_r	128	420	505	420	505	<b>420</b>	<b>505</b>	128	420	505	420	505	<b>420</b>	<b>505</b>		
548.exchange2_r	128	215	1560	<b>215</b>	<b>1560</b>	215	1560	128	215	1560	<b>215</b>	<b>1560</b>	215	1560		
557.xz_r	128	432	320	435	318	<b>432</b>	<b>320</b>	128	432	320	435	318	<b>432</b>	<b>320</b>		

**SPECrate®2017\_int\_base = 694**

**SPECrate®2017\_int\_peak = 721**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor.  
For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:  
LD\_LIBRARY\_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/lib/ia32:/home/cpu2017/je5.0.1-32"  
MALLOC\_CONF = "retain:true"

## General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM  
memory using Red Hat Enterprise Linux 8.4

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

```
sync; echo 3> /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
```

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

## Tyrone Systems

(Test Sponsor: Netweb Technologies India Ltd)

Tyrone Camarero SDI200A2N-224  
(2.8 GHz, Intel Xeon Platinum 8562Y+)

SPECrate®2017\_int\_base = 694

SPECrate®2017\_int\_peak = 721

CPU2017 License: 006802

Test Sponsor: Netweb Technologies India Ltd

Tested by: Tyrone Systems

Test Date: Jul-2025

Hardware Availability: Jan-2024

Software Availability: Jun-2025

## General Notes (Continued)

jemalloc, a general purpose malloc implementation  
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5  
sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

## Platform Notes

BIOS Settings:

Power Technology = Custom  
ENERGY\_PERF\_BIAS\_CFG mode = Maximum Performance  
KTI Prefetch = Enable  
LLC Dead Line Alloc = Disable  
SNC = Enable SNC2 (2-clusters)

```
Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on intelbenchmark Sat Jul 19 06:13:02 2025
```

SUT (System Under Test) info as seen by some common utilities.

-----  
Table of contents

1. uname -a  
2. w  
3. Username  
4. ulimit -a  
5. sysinfo process ancestry  
6. /proc/cpuinfo  
7. lscpu  
8. numactl --hardware  
9. /proc/meminfo  
10. who -r  
11. Systemd service manager version: systemd 249 (249.11-0ubuntu3.16)  
12. Failed units, from systemctl list-units --state=failed  
13. Services, from systemctl list-unit-files  
14. Linux kernel boot-time arguments, from /proc/cmdline  
15. sysctl  
16. /sys/kernel/mm/transparent\_hugepage  
17. /sys/kernel/mm/transparent\_hugepage/khugepaged  
18. OS release  
19. Disk information  
20. /sys/devices/virtual/dmi/id  
21. dmidecode  
22. BIOS

-----  
1. uname -a  
Linux intelbenchmark 5.15.0-143-generic #153-Ubuntu SMP Fri Jun 13 19:10:45 UTC 2025 x86\_64 x86\_64 x86\_64  
GNU/Linux

-----  
2. w  
06:13:02 up 5 min, 1 user, load average: 0.00, 0.02, 0.00  
USER TTY FROM LOGIN@ IDLE JCPU PCPU WHAT  
intel tty1 - 06:10 3.00s 0.88s 0.01s -bash

-----  
3. Username

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

## Tyrone Systems

(Test Sponsor: Netweb Technologies India Ltd)

Tyrone Camarero SDI200A2N-224  
(2.8 GHz, Intel Xeon Platinum 8562Y+)

SPECrate®2017\_int\_base = 694

SPECrate®2017\_int\_peak = 721

CPU2017 License: 006802

Test Sponsor: Netweb Technologies India Ltd

Tested by: Tyrone Systems

Test Date: Jul-2025

Hardware Availability: Jan-2024

Software Availability: Jun-2025

## Platform Notes (Continued)

From environment variable \$USER: intel

```
4. ulimit -a
time(seconds)          unlimited
file(blocks)           unlimited
data(kbytes)           unlimited
stack(kbytes)          unlimited
coredump(blocks)       0
memory(kbytes)         unlimited
locked memory(kbytes)  132062872
process                4126512
nofiles                1024
vmemory(kbytes)        unlimited
locks                 unlimited
rtprio                 0
```

```
5. sysinfo process ancestry
/sbin/init
/bin/login -p --
-bash
su
bash
bash
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=128 -c
  ic2024.0.2-lin-sapphirerapids-rate-20231213.cfg --define smt-on --define cores=64 --define physicalfirst
  --define invoke_with_interleave --define drop_caches --tune base,peak -o all intrate
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=128 --configfile
  ic2024.0.2-lin-sapphirerapids-rate-20231213.cfg --define smt-on --define cores=64 --define physicalfirst
  --define invoke_with_interleave --define drop_caches --tune base,peak --output_format all --nopower
  --runmode rate --tune base:peak --size refrate intrate --nopreenv --note-preenv --logfile
  $SPEC/tmp/CPU2017.004/templogs/preenv.intrate.004.0.log --lognum 004.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017
```

```
6. /proc/cpuinfo
model name      : INTEL(R) XEON(R) PLATINUM 8562Y+
vendor_id       : GenuineIntel
cpu family     : 6
model          : 207
stepping        : 2
microcode       : 0x210002a9
bugs            : spectre_v1 spectre_v2 spec_store_bypass swapgs eibrss bhi
cpu cores       : 32
siblings        : 64
2 physical ids (chips)
128 processors (hardware threads)
physical id 0: core ids 0-31
physical id 1: core ids 0-31
physical id 0: apicids 0-63
physical id 1: apicids 128-191
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

```
7. lscpu
```

From lscpu from util-linux 2.37.2:

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

## Tyrone Systems

(Test Sponsor: Netweb Technologies India Ltd)

Tyrone Camarero SDI200A2N-224  
(2.8 GHz, Intel Xeon Platinum 8562Y+)

**SPECrate®2017\_int\_base = 694**

**SPECrate®2017\_int\_peak = 721**

**CPU2017 License:** 006802

**Test Date:** Jul-2025

**Test Sponsor:** Netweb Technologies India Ltd

**Hardware Availability:** Jan-2024

**Tested by:** Tyrone Systems

**Software Availability:** Jun-2025

## Platform Notes (Continued)

Architecture:	x86_64
CPU op-mode(s):	32-bit, 64-bit
Address sizes:	46 bits physical, 57 bits virtual
Byte Order:	Little Endian
CPU(s):	128
On-line CPU(s) list:	0-127
Vendor ID:	GenuineIntel
Model name:	INTEL(R) XEON(R) PLATINUM 8562Y+
CPU family:	6
Model:	207
Thread(s) per core:	2
Core(s) per socket:	32
Socket(s):	2
Stepping:	2
BogoMIPS:	5600.00
Flags:	fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperf mperf tsc_known_freq pnpi pclmulqdq dtes64 monitor ds_cpl smx est tm2 ssse3 sdbg fma cx16 xtrm pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsaves avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_13 cat_12 cdp_13 invpcid_single intel_ppin cdp_12 ssbd mba ibrs ibpb stibp ibrs_enhanced fsgsbase tsc_adjust bmi1 avx2 smep bmi2 erms invpcid cqmq rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqmq_llc cqmq_occup_llc cqmq_mbm_total cqmq_mbm_local split_lock_detect avx_vnni avx512_bf16 wbnoinvd dtherm ida arat pln pts avx512vbmi umip pku ospke waitpkg avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57 rdpid bus_lock_detect cldemote movdiri movdir64b enqcmd fsrm md_clear serialize tsxldtrk pconfig arch_lbr amx_bf16 avx512_fp16 amx_tile amx_int8 flush_l1d arch_capabilities
L1d cache:	3 MiB (64 instances)
L1i cache:	2 MiB (64 instances)
L2 cache:	128 MiB (64 instances)
L3 cache:	120 MiB (2 instances)
NUMA node(s):	4
NUMA node0 CPU(s):	0-15,64-79
NUMA node1 CPU(s):	16-31,80-95
NUMA node2 CPU(s):	32-47,96-111
NUMA node3 CPU(s):	48-63,112-127
Vulnerability Gather data sampling:	Not affected
Vulnerability Itlb multihit:	Not affected
Vulnerability Llft:	Not affected
Vulnerability Mds:	Not affected
Vulnerability Meltdown:	Not affected
Vulnerability Mmio stale data:	Not affected
Vulnerability Reg file data sampling:	Not affected
Vulnerability Retbleed:	Not affected
Vulnerability Spec rstack overflow:	Not affected
Vulnerability Spec store bypass:	Mitigation; Speculative Store Bypass disabled via prctl and seccomp
Vulnerability Spectre v1:	Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2:	Mitigation; Enhanced / Automatic IBRS; IBPB conditional; RSB filling; PBRSB-eIBRS SW sequence; BHI BHI_DIS_S
Vulnerability Srbds:	Not affected
Vulnerability Tsx async abort:	Not affected

```
From lscpu --cache:
      NAME  ONE-SIZE  ALL-SIZE  WAYS  TYPE      LEVEL  SETS  PHY-LINE  COHERENCY-SIZE
```

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

## Tyrone Systems

(Test Sponsor: Netweb Technologies India Ltd)

Tyrone Camarero SDI200A2N-224  
(2.8 GHz, Intel Xeon Platinum 8562Y+)

**SPECrate®2017\_int\_base = 694**

**SPECrate®2017\_int\_peak = 721**

**CPU2017 License:** 006802

**Test Date:** Jul-2025

**Test Sponsor:** Netweb Technologies India Ltd

**Hardware Availability:** Jan-2024

**Tested by:** Tyrone Systems

**Software Availability:** Jun-2025

## Platform Notes (Continued)

L1d	48K	3M	12 Data	1	64	1	64
L1i	32K	2M	8 Instruction	1	64	1	64
L2	2M	128M	16 Unified	2	2048	1	64
L3	60M	120M	15 Unified	3	65536	1	64

### 8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```
available: 4 nodes (0-3)
node 0 cpus: 0-15,64-79
node 0 size: 257676 MB
node 0 free: 256756 MB
node 1 cpus: 16-31,80-95
node 1 size: 258039 MB
node 1 free: 256881 MB
node 2 cpus: 32-47,96-111
node 2 size: 258039 MB
node 2 free: 257619 MB
node 3 cpus: 48-63,112-127
node 3 size: 257985 MB
node 3 free: 257530 MB
node distances:
node 0 1 2 3
 0: 10 12 21 21
 1: 12 10 21 21
 2: 21 21 10 12
 3: 21 21 12 10
```

### 9. /proc/meminfo

```
MemTotal: 1056502988 kB
```

### 10. who -r

```
run-level 3 Jul 19 06:10
```

### 11. Systemd service manager version: systemd 249 (249.11-0ubuntu3.16)

```
Default Target Status
multi-user degraded
```

### 12. Failed units, from systemctl list-units --state=failed

UNIT	LOAD	ACTIVE	SUB	DESCRIPTION
* systemd-networkd-wait-online.service	loaded	failed		Wait for Network to be Configured

### 13. Services, from systemctl list-unit-files

STATE	UNIT FILES
enabled	ModemManager apparmor blk-availability cloud-config cloud-final cloud-init cloud-init-local console-setup cron dmesg e2scrub_reap finalrd getty@ gpu-manager grub-common grub-initrd-fallback irqbalance keyboard-setup lvm2-monitor lxd-agent multipathd networkd-dispatcher open-iscsi open-vm-tools pollinate rsyslog secureboot-db setvtrgb snapd ssh systemd-networkd systemd-networkd-wait-online systemd-pstore systemd-resolved systemd-timesyncd thermald ua-reboot-cmds ubuntu-advantage udisks2 ufw unattended-upgrades vgaauth
enabled-runtime	netplan-ovs-cleanupsystemd-fsck-root systemd-remount-fs
disabled	console-getty debug-shell ipmievd iscsid nftables rsync serial-getty@ systemd-boot-check-no-failures systemd-network-generator systemd-sysext systemd-time-wait-sync upower

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

## Tyrone Systems

(Test Sponsor: Netweb Technologies India Ltd)

Tyrone Camarero SDI200A2N-224  
(2.8 GHz, Intel Xeon Platinum 8562Y+)

SPECrate®2017\_int\_base = 694

SPECrate®2017\_int\_peak = 721

CPU2017 License: 006802

Test Date: Jul-2025

Test Sponsor: Netweb Technologies India Ltd

Hardware Availability: Jan-2024

Tested by: Tyrone Systems

Software Availability: Jun-2025

## Platform Notes (Continued)

```
generated      apport openipmi
indirect       uuidd
masked        cryptdisks cryptdisks-early hwclock lvm2 multipath-tools-boot rc rcS screen-cleanup sudo
x11-common

-----
14. Linux kernel boot-time arguments, from /proc/cmdline
    BOOT_IMAGE=/vmlinuz-5.15.0-143-generic
    root=UUID=9aebdeef-10e6-4d72-af3-0a1b298270ed
    ro

-----
15. sysctl
    kernel.numa_balancing          1
    kernel.randomize_va_space      2
    vm.compaction_proactiveness   20
    vm.dirty_background_bytes      0
    vm.dirty_background_ratio     10
    vm.dirty_bytes                 0
    vm.dirty_expire_centisecs    3000
    vm.dirty_ratio                 20
    vm.dirty_writeback_centisecs  500
    vm.dirtytime_expire_seconds   43200
    vm.extfrag_threshold          500
    vm.min_unmapped_ratio         1
    vm.nr_hugepages                0
    vm.nr_hugepages_mempolicy     0
    vm.nr_overcommit_hugepages    0
    vm.swappiness                  60
    vm.watermark_boost_factor     15000
    vm.watermark_scale_factor     10
    vm.zone_reclaim_mode          0

-----
16. /sys/kernel/mm/transparent_hugepage
    defrag           always defer defer+madvise [madvise] never
    enabled          always [madvise] never
    hpage_pmd_size  2097152
    shmem_enabled    always within_size advise [never] deny force

-----
17. /sys/kernel/mm/transparent_hugepage/khugepaged
    alloc_sleep_millisecs  60000
    defrag               1
    max_ptes_none        511
    max_ptes_shared      256
    max_ptes_swap        64
    pages_to_scan        4096
    scan_sleep_millisecs 10000

-----
18. OS release
    From /etc/*-release /etc/*-version
    os-release Ubuntu 22.04.1 LTS

-----
19. Disk information
    SPEC is set to: /home/cpu2017
    Filesystem  Type  Size  Used  Avail Use% Mounted on
    /dev/nvme0n1p5 ext4  380G  77G  284G  22% /home
```

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

## Tyrone Systems

(Test Sponsor: Netweb Technologies India Ltd)

Tyrone Camarero SDI200A2N-224  
(2.8 GHz, Intel Xeon Platinum 8562Y+)

SPECrate®2017\_int\_base = 694

SPECrate®2017\_int\_peak = 721

CPU2017 License: 006802

Test Sponsor: Netweb Technologies India Ltd

Tested by: Tyrone Systems

Test Date: Jul-2025

Hardware Availability: Jan-2024

Software Availability: Jun-2025

## Platform Notes (Continued)

20. /sys/devices/virtual/dmi/id  
Vendor: Tyrone Systems  
Product: SDI200A3N-212  
Product Family: Family  
Serial: A495115X4412722

21. dmidecode  
Additional information from dmidecode 3.3 follows. WARNING: Use caution when you interpret this section.  
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.  
Memory:  
16x NO DIMM NO DIMM  
16x Samsung M321R8GA0PB0-CWMCJ 64 GB 2 rank 5600

22. BIOS  
(This section combines info from /sys/devices and dmidecode.)  
BIOS Vendor: American Megatrends International, LLC.  
BIOS Version: 2.6  
BIOS Date: 05/15/2025  
BIOS Revision: 5.32

## Compiler Version Notes

=====| 502.gcc\_r(peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2024.0.2 Build 20231213  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

=====| 500.perlbench\_r(base, peak) 502.gcc\_r(base) 505.mcf\_r(base, peak) 525.x264\_r(base, peak)  
| 557.xz\_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.0.2 Build 20231213  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

=====| 502.gcc\_r(peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2024.0.2 Build 20231213  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

=====| 500.perlbench\_r(base, peak) 502.gcc\_r(base) 505.mcf\_r(base, peak) 525.x264\_r(base, peak)  
| 557.xz\_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.0.2 Build 20231213  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

## Tyrone Systems

(Test Sponsor: Netweb Technologies India Ltd)

Tyrone Camarero SDI200A2N-224  
(2.8 GHz, Intel Xeon Platinum 8562Y+)

**SPECrate®2017\_int\_base = 694**

**SPECrate®2017\_int\_peak = 721**

CPU2017 License: 006802

Test Sponsor: Netweb Technologies India Ltd

Tested by: Tyrone Systems

Test Date: Jul-2025

Hardware Availability: Jan-2024

Software Availability: Jun-2025

## Compiler Version Notes (Continued)

```
=====
C++      | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base, peak) 531.deepsjeng_r(base, peak)
          | 541.leela_r(base, peak)
```

```
=====
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.0.2 Build 20231213
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
```

```
=====
Fortran | 548.exchange2_r(base, peak)
```

```
=====
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.0.2 Build 20231213
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
```

## Base Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

## Base Portability Flags

```
500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64
```

## Base Optimization Flags

C benchmarks:

```
-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
```

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

## Tyrone Systems

(Test Sponsor: Netweb Technologies India Ltd)

Tyrone Camarero SDI200A2N-224  
(2.8 GHz, Intel Xeon Platinum 8562Y+)

**SPECrate®2017\_int\_base = 694**

**SPECrate®2017\_int\_peak = 721**

CPU2017 License: 006802

Test Sponsor: Netweb Technologies India Ltd

Tested by: Tyrone Systems

Test Date: Jul-2025

Hardware Availability: Jan-2024

Software Availability: Jun-2025

## Base Optimization Flags (Continued)

C benchmarks (continued):

```
-L/opt/intel/oneapi/compiler/2024.0/lib -lqkmalloc
```

C++ benchmarks:

```
-w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/opt/intel/oneapi/compiler/2024.0/lib -lqkmalloc
```

Fortran benchmarks:

```
-w -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-L/opt/intel/oneapi/compiler/2024.0/lib -lqkmalloc
```

## Peak Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

## Peak Portability Flags

```
500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64
```



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

## Tyrone Systems

(Test Sponsor: Netweb Technologies India Ltd)

Tyrone Camarero SDI200A2N-224  
(2.8 GHz, Intel Xeon Platinum 8562Y+)

SPECrate®2017\_int\_base = 694

SPECrate®2017\_int\_peak = 721

CPU2017 License: 006802

Test Sponsor: Netweb Technologies India Ltd

Tested by: Tyrone Systems

Test Date: Jul-2025

Hardware Availability: Jan-2024

Software Availability: Jun-2025

## Peak Optimization Flags

C benchmarks:

```
500.perlbench_r: -w -std=c11 -m64 -Wl,-z,muldefs
-fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4
-fno-strict-overflow
-L/opt/intel/oneapi/compiler/2024.0/lib -lqkmalloc

502.gcc_r: -m32 -L/opt/intel/oneapi/compiler/2024.0/lib32 -std=gnu89
-Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4
-L/usr/local/jemalloc32-5.0.1/lib -ljemalloc

505.mcf_r: basepeak = yes

525.x264_r: -w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fno-alias
-L/opt/intel/oneapi/compiler/2024.0/lib -lqkmalloc

557.xz_r: basepeak = yes
```

C++ benchmarks:

```
520.omnetpp_r: basepeak = yes

523.xalancbmk_r: basepeak = yes

531.deepsjeng_r: basepeak = yes

541.leela_r: basepeak = yes
```

Fortran benchmarks:

```
548.exchange2_r: basepeak = yes
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.html>

<http://www.spec.org/cpu2017/flags/Tyrone-Platform-Settings-V1.2-SPR-revC.html>



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

## Tyrone Systems

(Test Sponsor: Netweb Technologies India Ltd)

Tyrone Camarero SDI200A2N-224  
(2.8 GHz, Intel Xeon Platinum 8562Y+)

**SPECrate®2017\_int\_base = 694**

**SPECrate®2017\_int\_peak = 721**

**CPU2017 License:** 006802

**Test Sponsor:** Netweb Technologies India Ltd

**Tested by:** Tyrone Systems

**Test Date:** Jul-2025

**Hardware Availability:** Jan-2024

**Software Availability:** Jun-2025

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.xml>

<http://www.spec.org/cpu2017/flags/Tyrone-Platform-Settings-V1.2-SPR-revC.xml>

SPEC CPU and SPECrate are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

Tested with SPEC CPU®2017 v1.1.9 on 2025-07-19 02:13:01-0400.

Report generated on 2025-08-12 15:47:48 by CPU2017 PDF formatter v6716.

Originally published on 2025-08-12.