



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem ST45 V3  
(3.80 GHz, AMD EPYC 4344P)

SPECrate®2017\_fp\_base = 99.5

SPECrate®2017\_fp\_peak = 108

CPU2017 License: 9017

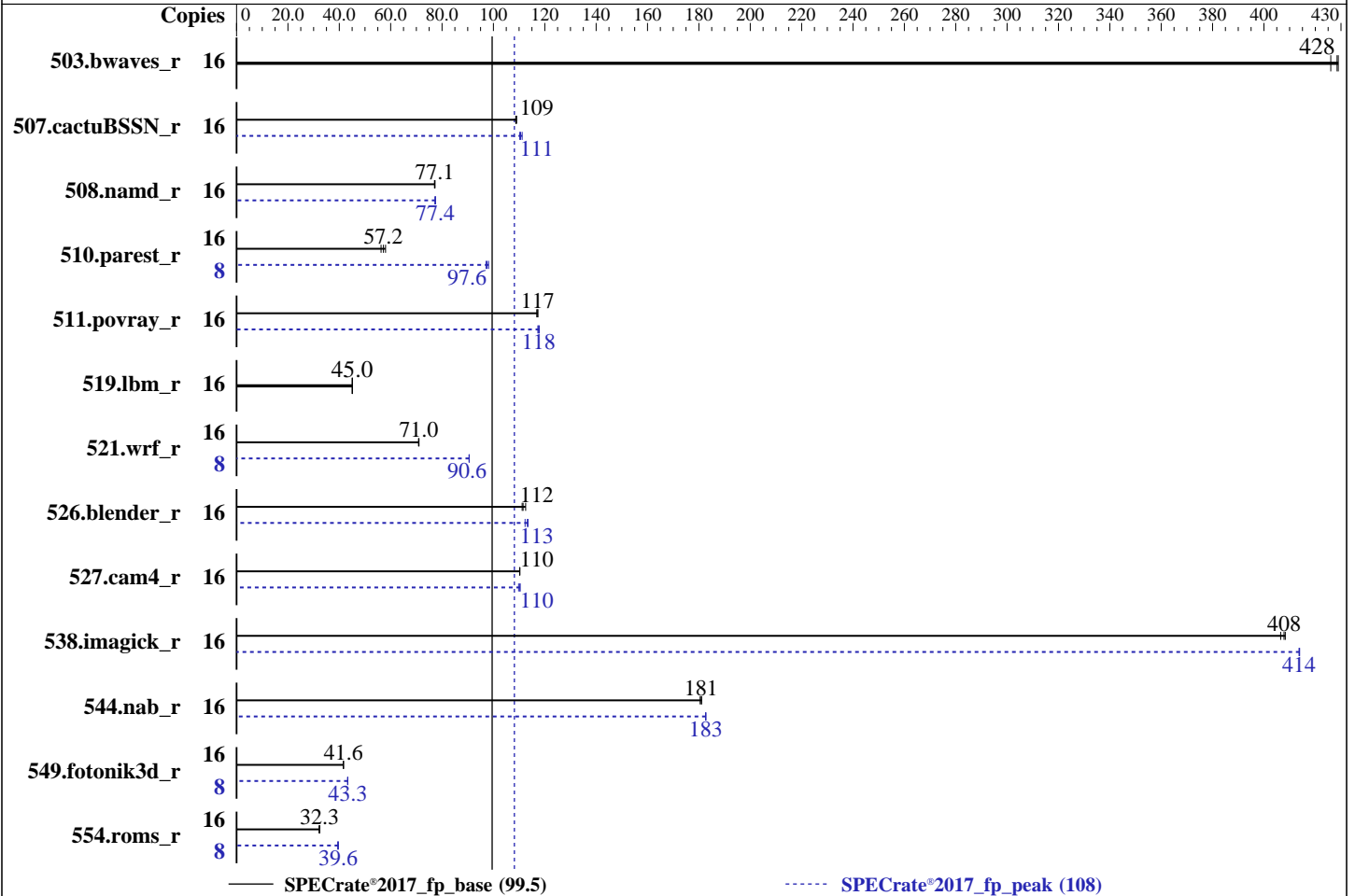
Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Dec-2024

Hardware Availability: Dec-2024

Software Availability: Oct-2024



### Hardware

CPU Name: AMD EPYC 4344P  
 Max MHz: 5300  
 Nominal: 3800  
 Enabled: 8 cores, 1 chip, 2 threads/core  
 Orderable: 1 chip  
 Cache L1: 32 KB I + 32 KB D on chip per core  
 L2: 1 MB I+D on chip per core  
 L3: 32 MB I+D on chip per chip  
 Other: None  
 Memory: 64 GB (2 x 32 GB 2Rx8 PC5-5600B-E, running at 5200)  
 Storage: 1 x 960 GB SATA SSD  
 Other: CPU Cooling: Air

### Software

OS: SUSE Linux Enterprise Server 15 SP6  
 Kernel 6.4.0-150600.21-default  
 Compiler: C/C++/Fortran: Version 5.0.0 of AOCC  
 Parallel: No  
 Firmware: Lenovo BIOS Version QIE101W 1.10 released Nov-2024  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 64-bit  
 Other: None  
 Power Management: OS set to balance power and performance



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem ST45 V3  
(3.80 GHz, AMD EPYC 4344P)

SPECrate®2017\_fp\_base = 99.5

SPECrate®2017\_fp\_peak = 108

**CPU2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology

**Test Date:** Dec-2024  
**Hardware Availability:** Dec-2024  
**Software Availability:** Oct-2024

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	16	374	429	<b>374</b>	<b>428</b>	377	426	16	374	429	<b>374</b>	<b>428</b>	377	426
507.cactuBSSN_r	16	186	109	<b>186</b>	<b>109</b>	186	109	16	<b>183</b>	<b>111</b>	184	110	182	111
508.namd_r	16	197	77.1	<b>197</b>	<b>77.1</b>	197	77.3	16	<b>196</b>	<b>77.4</b>	197	77.2	196	77.5
510.parest_r	16	744	56.3	<b>732</b>	<b>57.2</b>	722	58.0	8	<b>215</b>	<b>97.6</b>	215	97.1	213	98.1
511.povray_r	16	318	117	320	117	<b>319</b>	<b>117</b>	16	<b>318</b>	<b>118</b>	318	117	317	118
519.lbm_r	16	<b>374</b>	<b>45.0</b>	375	45.0	374	45.1	16	<b>374</b>	<b>45.0</b>	375	45.0	374	45.1
521.wrf_r	16	<b>505</b>	<b>71.0</b>	506	70.8	505	71.0	8	<b>198</b>	<b>90.6</b>	198	90.5	198	90.6
526.blender_r	16	216	113	219	111	<b>218</b>	<b>112</b>	16	217	112	<b>215</b>	<b>113</b>	215	113
527.cam4_r	16	254	110	254	110	<b>254</b>	<b>110</b>	16	<b>254</b>	<b>110</b>	254	110	255	110
538.imagick_r	16	<b>97.6</b>	<b>408</b>	97.5	408	97.9	407	16	96.2	414	<b>96.2</b>	<b>414</b>	96.1	414
544.nab_r	16	<b>149</b>	<b>181</b>	149	181	149	180	16	<b>147</b>	<b>183</b>	147	183	147	183
549.fotonik3d_r	16	<b>1499</b>	<b>41.6</b>	1500	41.6	1498	41.6	8	720	43.3	721	43.3	<b>720</b>	<b>43.3</b>
554.roms_r	16	784	32.4	<b>788</b>	<b>32.3</b>	791	32.1	8	321	39.6	<b>321</b>	<b>39.6</b>	323	39.4

SPECrate®2017\_fp\_base = **99.5**

SPECrate®2017\_fp\_peak = **108**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Notes

The AMD64 AOCC Compiler Suite is available at  
<http://developer.amd.com/amd-aocc/>

## Submit Notes

The config file option 'submit' was used.  
'numactl' was used to bind copies to the cores.  
See the configuration file for details.

## Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit  
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty\_ratio=8' run as root.  
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.  
To free node-local memory and avoid remote memory usage,  
'sysctl -w vm.zone\_reclaim\_mode=1' run as root.  
To clear filesystem caches, 'sync; sysctl -w vm.drop\_caches=3' run as root.  
To disable address space layout randomization (ASLR) to reduce run-to-run  
variability, 'sysctl -w kernel.randomize\_va\_space=0' run as root.

To enable Transparent Hugepages (THP) for all allocations:  
'echo always > /sys/kernel/mm/transparent\_hugepage/enabled' and

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem ST45 V3  
(3.80 GHz, AMD EPYC 4344P)

SPECrate®2017\_fp\_base = 99.5

SPECrate®2017\_fp\_peak = 108

**CPU2017 License:** 9017

**Test Sponsor:** Lenovo Global Technology

**Tested by:** Lenovo Global Technology

**Test Date:** Dec-2024

**Hardware Availability:** Dec-2024

**Software Availability:** Oct-2024

## Operating System Notes (Continued)

'echo always > /sys/kernel/mm/transparent\_hugepage/defrag' run as root.

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH =
    "/home/cpu2017-1.1.9-amd-aocc500_znver5_A1/amd_rate_aocc500_znver5_A_lib/lib:/home/cpu2017-1.1.9-amd-aocc500_znver5_A1/amd_rate_aocc500_znver5_A_lib/lib32:"
MALLOC_CONF = "retain:true"
```

## General Notes

Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

## Platform Notes

```
Sysinfo program /home/cpu2017-1.1.9-amd-aocc500_znver5_A1/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on localhost Tue Apr 23 06:45:11 2024
```

SUT (System Under Test) info as seen by some common utilities.

-----  
Table of contents  
-----

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. sysctl
16. /sys/kernel/mm/transparent\_hugepage
17. /sys/kernel/mm/transparent\_hugepage/khugepaged
18. OS release
19. Disk information
20. /sys/devices/virtual/dmi/id
21. dmidecode

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem ST45 V3  
(3.80 GHz, AMD EPYC 4344P)

SPECrate®2017\_fp\_base = 99.5

SPECrate®2017\_fp\_peak = 108

**CPU2017 License:** 9017

**Test Sponsor:** Lenovo Global Technology

**Tested by:** Lenovo Global Technology

**Test Date:** Dec-2024

**Hardware Availability:** Dec-2024

**Software Availability:** Oct-2024

### Platform Notes (Continued)

22. BIOS

```
1. uname -a
Linux localhost 6.4.0-150600.21-default #1 SMP PREEMPT_DYNAMIC Thu May 16 11:09:22 UTC 2024 (36cle09)
x86_64 x86_64 x86_64 GNU/Linux
```

```
2. w
06:45:11 up 10:45, 2 users, load average: 3.82, 11.98, 14.57
USER      TTY      FROM          LOGIN@   IDLE   JCPU   PCPU WHAT
root      pts/0    172.30.81.2   20:00   10:44m 0.80s  0.04s /bin/bash ./amd_rate_aocc500_znver5_A1.sh
root      pts/1    172.30.81.2   20:07   10:36m 0.03s  0.03s -bash
```

```
3. Username
From environment variable $USER: root
```

```
4. ulimit -a
core file size          (blocks, -c) unlimited
data seg size           (kbytes, -d) unlimited
scheduling priority     (-e) 0
file size               (blocks, -f) unlimited
pending signals         (-i) 253429
max locked memory       (kbytes, -l) 2097152
max memory size         (kbytes, -m) unlimited
open files              (-n) 1024
pipe size               (512 bytes, -p) 8
POSIX message queues    (bytes, -q) 819200
real-time priority      (-r) 0
stack size              (kbytes, -s) unlimited
cpu time                (seconds, -t) unlimited
max user processes      (-u) 253429
virtual memory          (kbytes, -v) unlimited
file locks              (-x) unlimited
```

```
5. sysinfo process ancestry
/usr/lib/systemd/systemd --switched-root --system --deserialize=42
sshd: /usr/sbin/sshd -D [listener] 0 of 10-100 startups
sshd: root [priv]
sshd: root@pts/0
-bash
/bin/bash ./03.local_run_SPECcpu.sh
/bin/bash ./Run026-compliant-amd-ratefp.sh
python3 ./run_amd_rate_aocc500_znver5_A1.py
/bin/bash ./amd_rate_aocc500_znver5_A1.sh
runcpu --config amd_rate_aocc500_znver5_A1.cfg --tune all --reportable --iterations 3 fprate
runcpu --configfile amd_rate_aocc500_znver5_A1.cfg --tune all --reportable --iterations 3 --nopower
--runmode rate --tune base:peak --size test:train:refrate fprate --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.030/temlogs/preenv.fprate.030.0.log --lognum 030.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017-1.1.9-amd-aocc500_znver5_A1
```

```
6. /proc/cpuinfo
model name      : AMD EPYC 4344P 8-Core Processor
vendor_id      : AuthenticAMD
```

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem ST45 V3  
(3.80 GHz, AMD EPYC 4344P)

SPECrate®2017\_fp\_base = 99.5

SPECrate®2017\_fp\_peak = 108

CPU2017 License: 9017

Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Dec-2024

Hardware Availability: Dec-2024

Software Availability: Oct-2024

### Platform Notes (Continued)

```

cpu family      : 25
model          : 97
stepping       : 2
microcode      : 0xa601209
bugs           : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass srso
TLB size       : 3584 4K pages
cpu cores      : 8
siblings       : 16
1 physical ids (chips)
16 processors (hardware threads)
physical id 0: core ids 0-7
physical id 0: apicids 0-15

```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

#### 7. lscpu

From lscpu from util-linux 2.39.3:

```

Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Address sizes:         48 bits physical, 48 bits virtual
Byte Order:            Little Endian
CPU(s):                16
On-line CPU(s) list:  0-15
Vendor ID:             AuthenticAMD
BIOS Vendor ID:       Advanced Micro Devices, Inc.
Model name:            AMD EPYC 4344P 8-Core Processor
BIOS Model name:      AMD EPYC 4344P 8-Core Processor
BIOS CPU family:      107
CPU family:            25
Model:                 97
Thread(s) per core:   2
Core(s) per socket:   8
Socket(s):             1
Stepping:              2
Frequency boost:      enabled
CPU(s) scaling MHz:   57%
CPU max MHz:          5388.2808
CPU min MHz:          3000.0000
BogoMIPS:              7585.32
Flags:                 fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat
pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb
rdtscp lm constant_tsc rep_good amd_lbr_v2 nopl nonstop_tsc cpuid
extd_apicid aperfmperf rapl pni pclmulqdq monitor ssse3 fma cx16
sse4_1 sse4_2 movbe popcnt aes xsave avx f16c rdrand lahf_lm
cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch
osvw ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext
perfctr_llc mwaitx cpb cat_l3 cdp_l3 hw_pstate ssbd mba perfmon_v2
ibrs ibpb stibp ibrs_enhanced vmmcall fsgsbase bmi1 avx2 smep bmi2
invpcid cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma
clflushopt clwb avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec
xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local
user_shstk avx512_bf16 clzero irperf xsaveerptr rdpru wbnoinvd cppc
arat npt lbrv svm_lock nrip_save tsc_scale vmcb_clean flushbyasid
decodeassists pausefilter pfthreshold avic v_vmsave_vmload vgif
x2avic v_spec_ctrl vnmi avx512vbmi umip pku ospke avx512_vbmi2 gfni
vaes vpclmulqdq avx512_vnni avx512_bitalg avx512_vpopcntdq rdpid
overflow_recov succor smca flush_l1d
Virtualization:       AMD-V

```

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

## Lenovo Global Technology

SPECrate®2017\_fp\_base = 99.5

ThinkSystem ST45 V3  
(3.80 GHz, AMD EPYC 4344P)

SPECrate®2017\_fp\_peak = 108

CPU2017 License: 9017

Test Date: Dec-2024

Test Sponsor: Lenovo Global Technology

Hardware Availability: Dec-2024

Tested by: Lenovo Global Technology

Software Availability: Oct-2024

### Platform Notes (Continued)

```

L1d cache:                256 KiB (8 instances)
L1i cache:                256 KiB (8 instances)
L2 cache:                 8 MiB (8 instances)
L3 cache:                 32 MiB (1 instance)
NUMA node(s):             1
NUMA node0 CPU(s):       0-15
Vulnerability Gather data sampling: Not affected
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf:      Not affected
Vulnerability Mds:       Not affected
Vulnerability Meltdown: Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Reg file data sampling: Not affected
Vulnerability Retbleed:  Not affected
Vulnerability Spec rstack overflow: Mitigation; Safe RET
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Enhanced / Automatic IBRS; IBPB conditional; STIBP
                           always-on; RSB filling; PBRBS-eIBRS Not affected; BHI Not affected
Vulnerability Srbds:     Not affected
Vulnerability Tsx async abort: Not affected

```

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	32K	256K	8	Data	1	64	1	64
L1i	32K	256K	8	Instruction	1	64	1	64
L2	1M	8M	8	Unified	2	2048	1	64
L3	32M	32M	16	Unified	3	32768	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```

available: 1 nodes (0)
node 0 cpus: 0-15
node 0 size: 63407 MB
node 0 free: 62831 MB
node distances:
node 0
0: 10

```

9. /proc/meminfo

MemTotal: 64929396 kB

10. who -r

run-level 3 Apr 22 20:00

11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)

```

Default Target Status
multi-user      running

```

12. Services, from systemctl list-unit-files

```

STATE UNIT FILES
enabled YaST2-Firstboot YaST2-Second-Stage apparmor auditd cron getty@ irqbalance issue-generator
          kbdsettings klog lvm2-monitor nscd postfix purge-kernels rollback rsyslog smartd sshd
          systemd-pstore wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny
enabled-runtime systemd-remount-fs

```

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem ST45 V3  
(3.80 GHz, AMD EPYC 4344P)

SPECrate®2017\_fp\_base = 99.5

SPECrate®2017\_fp\_peak = 108

CPU2017 License: 9017

Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Dec-2024

Hardware Availability: Dec-2024

Software Availability: Oct-2024

### Platform Notes (Continued)

```

disabled      autofsd autofast-initscripts blk-availability boot-sysctl ca-certificates chrony-wait
              chronyd console-getty cups cups-browsed debug-shell ebttables exchange-bmc-os-info
              firewalld fsidd gpm grub2-once haveged hwloc-dump-hwdata ipmi ipmievd issue-add-ssh-keys
              kexec-load lunmask man-db-create multipathd nfs nfs-blkmap rpcbind rpmconfigcheck rsyncd
              serial-getty@ smartd_generate_opts snmpd snmptrapd systemd-boot-check-no-failures
              systemd-confext systemd-network-generator systemd-sysext systemd-time-wait-sync
              systemd-timesyncd
indirect      systemd-userdbd wickedd

```

-----  
13. Linux kernel boot-time arguments, from /proc/cmdline

```

BOOT_IMAGE=/boot/vmlinuz-6.4.0-150600.21-default
root=UUID=69af5974-2680-4d0c-9805-7dae997bdc7d
splash=silent
mitigations=auto
quiet
security=apparmor

```

-----  
14. cpupower frequency-info

```

analyzing CPU 8:
  current policy: frequency should be within 3.00 GHz and 3.80 GHz.
                  The governor "ondemand" may decide which speed to use
                  within this range.

boost state support:
  Supported: yes
  Active: yes

```

-----  
15. sysctl

```

kernel.numa_balancing      0
kernel.randomize_va_space  0
vm.compaction_proactiveness 20
vm.dirty_background_bytes   0
vm.dirty_background_ratio   10
vm.dirty_bytes              0
vm.dirty_expire_centisecs   3000
vm.dirty_ratio              8
vm.dirty_writeback_centisecs 500
vm.dirtytime_expire_seconds 43200
vm.extfrag_threshold        500
vm.min_unmapped_ratio       1
vm.nr_hugepages             0
vm.nr_hugepages_mempolicy   0
vm.nr_overcommit_hugepages  0
vm.swappiness                1
vm.watermark_boost_factor    15000
vm.watermark_scale_factor    10
vm.zone_reclaim_mode        1

```

-----  
16. /sys/kernel/mm/transparent\_hugepage

```

defrag      [always] defer defer+madvise madvise never
enabled     [always] madvise never
hpage_pmd_size 2097152
shmem_enabled always within_size advise [never] deny force

```

-----  
17. /sys/kernel/mm/transparent\_hugepage/khugepaged

```

alloc_sleep_millisecs 60000

```

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem ST45 V3  
(3.80 GHz, AMD EPYC 4344P)

SPECrate®2017\_fp\_base = 99.5

SPECrate®2017\_fp\_peak = 108

**CPU2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology

**Test Date:** Dec-2024  
**Hardware Availability:** Dec-2024  
**Software Availability:** Oct-2024

### Platform Notes (Continued)

```
defrag                1
max_ptes_none         511
max_ptes_shared       256
max_ptes_swap         64
pages_to_scan         4096
scan_sleep_millisecs  10000
```

-----  
18. OS release  
From /etc/\*-release /etc/\*-version  
os-release SUSE Linux Enterprise Server 15 SP6  
-----

19. Disk information  
SPEC is set to: /home/cpu2017-1.1.9-amd-aocc500\_znver5\_A1  
Filesystem Type Size Used Avail Use% Mounted on  
/dev/sda3 xfs 893G 43G 851G 5% /  
-----

20. /sys/devices/virtual/dmidecode  
Vendor: LENOVO  
Product: ThinkSystem ST45 V3  
Product Family: ThinkSystem  
Serial: INVALID  
-----

21. dmidecode  
Additional information from dmidecode 3.4 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.  
Memory:  
2x SK Hynix HMC88AGBEA084N 32 GB 2 rank 5600, configured at 5200  
-----

22. BIOS  
(This section combines info from /sys/devices and dmidecode.)  
BIOS Vendor: LENOVO  
BIOS Version: QIE101W-1.10  
BIOS Date: 11/11/2024  
BIOS Revision: 1.10  
Firmware Revision: 12.65  
ST45 V3 CPU performance result based on 65W maximum consumption limit.  
-----

### Compiler Version Notes

=====  
C | 519.lbm\_r(base, peak) 538.imagick\_r(base, peak) 544.nab\_r(base, peak)  
-----

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin  
-----

=====  
C++ | 508.namd\_r(base, peak) 510.parest\_r(base, peak)  
-----

(Continued on next page)





# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem ST45 V3  
(3.80 GHz, AMD EPYC 4344P)

SPECrate®2017\_fp\_base = 99.5

SPECrate®2017\_fp\_peak = 108

**CPU2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology

**Test Date:** Dec-2024  
**Hardware Availability:** Dec-2024  
**Software Availability:** Oct-2024

### Compiler Version Notes (Continued)

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

=====  
C++, C | 511.povray\_r(base, peak) 526.blender\_r(base, peak)

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin  
AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

=====  
C++, C, Fortran | 507.cactuBSSN\_r(base, peak)

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin  
AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin  
AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

=====  
Fortran | 503.bwaves\_r(base, peak) 549.fotonik3d\_r(base, peak) 554.roms\_r(base, peak)

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

=====  
Fortran, C | 521.wrf\_r(base, peak) 527.cam4\_r(base, peak)

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin  
AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

**Lenovo Global Technology**

ThinkSystem ST45 V3  
(3.80 GHz, AMD EPYC 4344P)

SPECrate®2017\_fp\_base = 99.5

SPECrate®2017\_fp\_peak = 108

**CPU2017 License:** 9017

**Test Sponsor:** Lenovo Global Technology

**Tested by:** Lenovo Global Technology

**Test Date:** Dec-2024

**Hardware Availability:** Dec-2024

**Software Availability:** Oct-2024

## Base Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using both C and C++:

clang++ clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

## Base Portability Flags

```
503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_CASE_FLAG -Mbyteswapio -DSPEC_LP64
526.blender_r: -funsigned-char -DSPEC_LP64
527.cam4_r: -DSPEC_CASE_FLAG -DSPEC_LP64
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64
```

## Base Optimization Flags

C benchmarks:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-ldist-scalar-expand -fenable-aggressive-gather -O3
-march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie -fltto
-fstruct-layout=7 -mllvm -unroll-threshold=50
```

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem ST45 V3  
(3.80 GHz, AMD EPYC 4344P)

SPECrate®2017\_fp\_base = 99.5

SPECrate®2017\_fp\_peak = 108

**CPU2017 License:** 9017

**Test Sponsor:** Lenovo Global Technology

**Tested by:** Lenovo Global Technology

**Test Date:** Dec-2024

**Hardware Availability:** Dec-2024

**Software Availability:** Oct-2024

## Base Optimization Flags (Continued)

C benchmarks (continued):

```
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lamdalloc
-lflang -ldl
```

C++ benchmarks:

```
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-mllvm -unroll-threshold=100 -mllvm -loop-unswitch-threshold=200000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lamdalloc
-lflang -ldl
```

Fortran benchmarks:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-aggressive-gather=true
-Wl,-mllvm -Wl,-enable-masked-gather-sequence=false -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -flto -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop -mllvm -reduce-array-computations=3
-fepilog-vectorization-of-inductions -zopt -lamdlibm -lamdalloc
-lflang -ldl
```

Benchmarks using both Fortran and C:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-aggressive-gather=true
-Wl,-mllvm -Wl,-enable-masked-gather-sequence=false -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop -fepilog-vectorization-of-inductions
-lamdlibm -lamdalloc -lflang -ldl
```

Benchmarks using both C and C++:

```
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie
-flto -fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -mllvm -unroll-threshold=100
```

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

**Lenovo Global Technology**

ThinkSystem ST45 V3  
(3.80 GHz, AMD EPYC 4344P)

SPECrate®2017\_fp\_base = 99.5

SPECrate®2017\_fp\_peak = 108

**CPU2017 License:** 9017

**Test Sponsor:** Lenovo Global Technology

**Tested by:** Lenovo Global Technology

**Test Date:** Dec-2024

**Hardware Availability:** Dec-2024

**Software Availability:** Oct-2024

## Base Optimization Flags (Continued)

Benchmarks using both C and C++ (continued):

```
-mllvm -loop-unswitch-threshold=200000 -lamdlibm -lamdalloc -lflang  
-ldl
```

Benchmarks using Fortran, C, and C++:

```
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner  
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie  
-flto -fstruct-layout=7 -mllvm -unroll-threshold=50  
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining  
-mllvm -reduce-array-computations=3 -zopt -mllvm -unroll-threshold=100  
-mllvm -loop-unswitch-threshold=200000 -Mrecursive -funroll-loops  
-mllvm -lsr-in-nested-loop -fepilog-vectorization-of-inductions  
-lamdlibm -lamdalloc -lflang -ldl
```

## Base Other Flags

C benchmarks:

```
-Wno-unused-command-line-argument
```

C++ benchmarks:

```
-Wno-unused-command-line-argument
```

Fortran benchmarks:

```
-Wno-unused-command-line-argument
```

Benchmarks using both Fortran and C:

```
-Wno-unused-command-line-argument
```

Benchmarks using both C and C++:

```
-Wno-unused-command-line-argument
```

Benchmarks using Fortran, C, and C++:

```
-Wno-unused-command-line-argument
```

## Peak Compiler Invocation

C benchmarks:

```
clang
```

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

**Lenovo Global Technology**

SPECrate®2017\_fp\_base = 99.5

ThinkSystem ST45 V3  
(3.80 GHz, AMD EPYC 4344P)

SPECrate®2017\_fp\_peak = 108

**CPU2017 License:** 9017

**Test Date:** Dec-2024

**Test Sponsor:** Lenovo Global Technology

**Hardware Availability:** Dec-2024

**Tested by:** Lenovo Global Technology

**Software Availability:** Oct-2024

## Peak Compiler Invocation (Continued)

C++ benchmarks:

clang++

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using both C and C++:

clang++ clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

519.lbm\_r: basepeak = yes

538.imagick\_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast  
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto  
-fstruct-layout=7 -mllvm -unroll-threshold=50  
-fremap-arrays -fstrip-mining  
-mllvm -inline-threshold=1000  
-mllvm -reduce-array-computations=3 -zopt -lamdlibm  
-lamdalloc -ldl

544.nab\_r: -m64 -flto -Wl,-mllvm -Wl,-ldist-scalar-expand  
-fenable-aggressive-gather -Ofast -march=znver5  
-fveclib=AMDLIBM -ffast-math -fstruct-layout=7  
-mllvm -unroll-threshold=50 -fremap-arrays -fstrip-mining  
-mllvm -inline-threshold=1000  
-mllvm -reduce-array-computations=3 -zopt -lamdlibm  
-lamdalloc -ldl

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem ST45 V3  
(3.80 GHz, AMD EPYC 4344P)

SPECrate®2017\_fp\_base = 99.5

SPECrate®2017\_fp\_peak = 108

**CPU2017 License:** 9017

**Test Sponsor:** Lenovo Global Technology

**Tested by:** Lenovo Global Technology

**Test Date:** Dec-2024

**Hardware Availability:** Dec-2024

**Software Availability:** Oct-2024

## Peak Optimization Flags (Continued)

C++ benchmarks:

```
508.namd_r: -m64 -std=c++14
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-mllvm -unroll-threshold=100
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lamdalloc -ldl
```

```
510.parest_r: -m64 -std=c++14 -flto -Wl,-mllvm -Wl,-suppress-fmas
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math
-mllvm -unroll-threshold=100
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lamdalloc -ldl
```

Fortran benchmarks:

```
503.bwaves_r: basepeak = yes
```

```
549.fotonik3d_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-Mrecursive -mllvm -reduce-array-computations=3
-fepilog-vectorization-of-inductions -fvector-transform
-fscalar-transform -lamdlibm -lamdalloc -ldl -lflang
```

```
554.roms_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-Mrecursive -mllvm -reduce-array-computations=3
-fepilog-vectorization-of-inductions -zopt -lamdlibm
-lamdalloc -ldl -lflang
```

Benchmarks using both Fortran and C:

```
521.wrf_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-fremap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -Mrecursive
-funroll-loops -mllvm -lsr-in-nested-loop
```

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

## Lenovo Global Technology

SPECrate®2017\_fp\_base = 99.5

ThinkSystem ST45 V3  
(3.80 GHz, AMD EPYC 4344P)

SPECrate®2017\_fp\_peak = 108

CPU2017 License: 9017

Test Date: Dec-2024

Test Sponsor: Lenovo Global Technology

Hardware Availability: Dec-2024

Tested by: Lenovo Global Technology

Software Availability: Oct-2024

## Peak Optimization Flags (Continued)

521.wrf\_r (continued):

```
-fepilog-vectorization-of-inductions -lamdlibm -lamdalloc  
-ldl -lflang
```

```
527.cam4_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-enable-X86-prefetching -Ofast  
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto  
-fstruct-layout=7 -mllvm -unroll-threshold=50  
-mllvm -inline-threshold=1000 -fremap-arrays  
-mllvm -reduce-array-computations=3 -zopt -Mrecursive  
-funroll-loops -mllvm -lsr-in-nested-loop  
-fepilog-vectorization-of-inductions -lamdlibm -lamdalloc  
-ldl -lflang
```

Benchmarks using both C and C++:

```
511.povray_r: -m64 -std=c++14  
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false  
-Wl,-mllvm -Wl,-extra-inliner -Ofast -march=znver5  
-fveclib=AMDLIBM -ffast-math -flto -fstruct-layout=7  
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000  
-fremap-arrays -mllvm -reduce-array-computations=3 -zopt  
-mllvm -unroll-threshold=100  
-mllvm -loop-unswitch-threshold=200000 -lamdlibm  
-lamdalloc -ldl
```

```
526.blender_r: -m64 -std=c++14  
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast  
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto  
-fstruct-layout=7 -mllvm -unroll-threshold=50  
-fremap-arrays -fstrip-mining  
-mllvm -inline-threshold=1000  
-mllvm -reduce-array-computations=3 -zopt  
-mllvm -unroll-threshold=100 -lamdlibm -lamdalloc -ldl
```

Benchmarks using Fortran, C, and C++:

```
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast -march=znver5  
-fveclib=AMDLIBM -ffast-math -flto -fstruct-layout=7  
-mllvm -unroll-threshold=50 -fremap-arrays -fstrip-mining
```

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem ST45 V3  
(3.80 GHz, AMD EPYC 4344P)

SPECrate®2017\_fp\_base = 99.5

SPECrate®2017\_fp\_peak = 108

**CPU2017 License:** 9017

**Test Sponsor:** Lenovo Global Technology

**Tested by:** Lenovo Global Technology

**Test Date:** Dec-2024

**Hardware Availability:** Dec-2024

**Software Availability:** Oct-2024

## Peak Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++ (continued):

```
-mllvm -inline-threshold=1000 -mllvm -reduce-array-computations=3 -zopt
-mllvm -unroll-threshold=100 -mllvm -loop-unswitch-threshold=200000
-faggressive-loop-transform -fvector-transform -fscalar-transform
-Mrecursive -fepilog-vectorization-of-inductions -lamdlibm -lamdalloc
-ldl -lflang
```

## Peak Other Flags

C benchmarks:

```
-Wno-unused-command-line-argument
```

C++ benchmarks:

```
-Wno-unused-command-line-argument
```

Fortran benchmarks:

```
-Wno-unused-command-line-argument
```

Benchmarks using both Fortran and C:

```
-Wno-unused-command-line-argument
```

Benchmarks using both C and C++:

```
-Wno-unused-command-line-argument
```

Benchmarks using Fortran, C, and C++:

```
-Wno-unused-command-line-argument
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Turin-C.html>

<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Turin-C.xml>

<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.xml>

SPEC CPU and SPECrate are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

Tested with SPEC CPU®2017 v1.1.9 on 2024-04-22 18:45:10-0400.

Report generated on 2025-01-15 12:33:00 by CPU2017 PDF formatter v6716.

Originally published on 2025-01-14.