



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017_int_base = 254

PowerEdge R6625 (AMD EPYC 9224 24-Core Processor)

SPECrate®2017_int_peak = 265

CPU2017 License: 6573

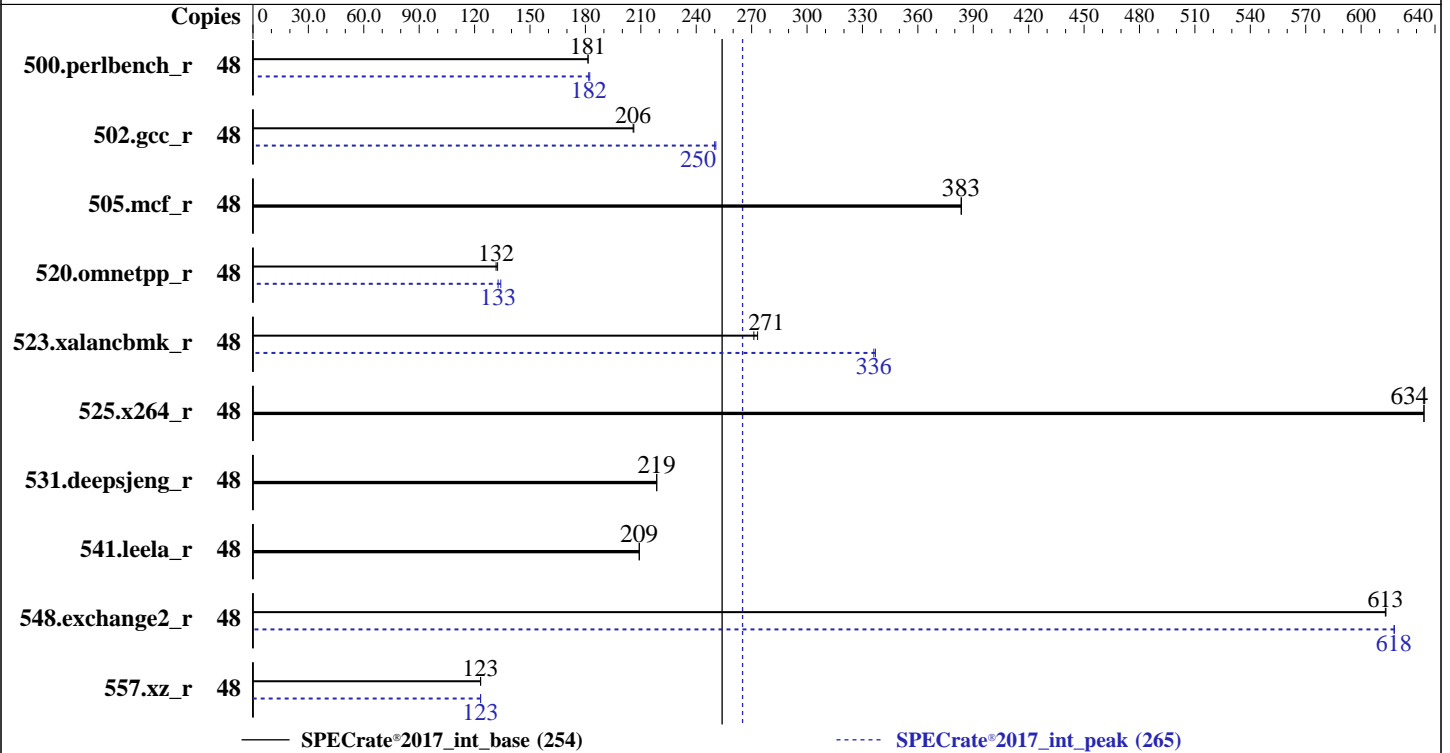
Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Apr-2024

Hardware Availability: Feb-2023

Software Availability: Mar-2024



Hardware

CPU Name: AMD EPYC 9224
 Max MHz: 3700
 Nominal: 2500
 Enabled: 24 cores, 1 chip, 2 threads/core
 Orderable: 1 chip
 Cache L1: 32 KB I + 32 KB D on chip per core
 L2: 1 MB I+D on chip per core
 L3: 64 MB I+D on chip per chip, 16 MB shared / 6 cores
 Other: None
 Memory: 768 GB (12 x 64 GB 2Rx4 PC5-4800B-R)
 Storage: 50 GB on tmpfs
 Other: CPU Cooling: Air

Software

OS: Ubuntu 22.04.4 LTS
 5.15.0-102-generic
 Compiler: C/C++/Fortran: Version 4.0.0 of AOCC
 Parallel: No
 Firmware: Version 1.6.8 released Nov-2023
 File System: tmpfs
 System State: Run level 5 (graphical multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other: None
 Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	48	421	181	<u>421</u>	<u>181</u>			48	<u>420</u>	<u>182</u>	419	182		
502.gcc_r	48	<u>330</u>	<u>206</u>	330	206			48	<u>272</u>	<u>250</u>	271	251		
505.mcf_r	48	202	384	<u>202</u>	<u>383</u>			48	202	384	<u>202</u>	<u>383</u>		
520.omnetpp_r	48	<u>478</u>	<u>132</u>	475	132			48	<u>474</u>	<u>133</u>	469	134		
523.xalancbmk_r	48	186	273	<u>187</u>	<u>271</u>			48	150	337	<u>151</u>	<u>336</u>		
525.x264_r	48	<u>133</u>	<u>634</u>	133	634			48	<u>133</u>	<u>634</u>	133	634		
531.deepsjeng_r	48	252	219	<u>252</u>	<u>219</u>			48	252	219	<u>252</u>	<u>219</u>		
541.leela_r	48	<u>380</u>	<u>209</u>	380	209			48	<u>380</u>	<u>209</u>	380	209		
548.exchange2_r	48	205	613	<u>205</u>	<u>613</u>			48	<u>204</u>	<u>618</u>	203	618		
557.xz_r	48	<u>421</u>	<u>123</u>	420	123			48	420	123	<u>421</u>	<u>123</u>		

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

The AMD64 AOCC Compiler Suite is available at <http://developer.amd.com/amd-aocc/>

Submit Notes

The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.
To free node-local memory and avoid remote memory usage,
'sysctl -w vm.zone_reclaim_mode=1' run as root.
To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.
To disable address space layout randomization (ASLR) to reduce run-to-run
variability, 'sysctl -w kernel.randomize_va_space=0' run as root.

To enable Transparent Hugepages (THP) only on request for base runs,
'echo madvise > /sys/kernel/mm/transparent_hugepage/enabled' run as root.
To enable THP for all allocations for peak runs,
'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.



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Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH =
"/mnt/ramdisk/cpu2017-1.1.9-aocc400-znver4-A1.1/amd_rate_aocc400_znver4_A_lib/lib:/mnt/ramdisk/cpu2017
-1.1.9-aocc400-znver4-A1.1/amd_rate_aocc400_znver4_A_lib/lib32:"
MALLOC_CONF = "retain:true"
```

Environment variables set by runcpu during the 523.xalancbmk_r peak run:

```
MALLOC_CONF = "thp:never"
```

General Notes

Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Benchmark run from a 50 GB ramdisk created with the cmd: "mount -t tmpfs -o size=50G tmpfs /mnt/ramdisk"

Platform Notes

BIOS settings:

```
DRAM Refresh Delay : Performance
DIMM Self Healing on
Uncorrectable Memory Error : Disabled
```

```
Virtualization Technology : Disabled
NUMA Nodes per Socket : 4
L3 cache as NUMA Domain : Enabled
```

```
System Profile : Custom
C-States : Disabled
Memory Patrol Scrub : Disabled
PCI ASPM L1 Link
Power Management : Disabled
Determinism Slider : Power Determinism
```

Sysinfo program /mnt/ramdisk/cpu2017-1.1.9-aocc400-znver4-A1.1/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on ubuntu-genova Fri Apr 12 16:08:57 2024

SUT (System Under Test) info as seen by some common utilities.

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1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo

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Platform Notes (Continued)

```

7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 249 (249.11-0ubuntu3.12)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. tuned-adm active
16. sysctl
17. /sys/kernel/mm/transparent_hugepage
18. /sys/kernel/mm/transparent_hugepage/khugepaged
19. OS release
20. Disk information
21. /sys/devices/virtual/dmi/id
22. dmidecode
23. BIOS

```

```

-----
1. uname -a
Linux ubuntu-genoa 5.15.0-102-generic #112-Ubuntu SMP Tue Mar 5 16:50:32 UTC 2024 x86_64 x86_64 x86_64
GNU/Linux

```

```

-----
2. w
 16:08:57 up 31 min,  1 user,  load average: 0.06, 0.09, 0.14
USER      TTY      FROM          LOGIN@   IDLE   JCPU   PCPU WHAT
root     tty1      -              08:50    24.00s  2.49s  0.48s /bin/bash ./amd_rate_aocc400_znver4_A1.sh

```

```

-----
3. Username
From environment variable $USER:  root

```

```

-----
4. ulimit -a
time(seconds)      unlimited
file(blocks)       unlimited
data(kbytes)       unlimited
stack(kbytes)      unlimited
coredump(blocks)   0
memory(kbytes)     unlimited
locked memory(kbytes) 2097152
process            3093905
nofiles            1024
vmemory(kbytes)    unlimited
locks              unlimited
rtprio             0

```

```

-----
5. sysinfo process ancestry
/sbin/init
/bin/login -p --
-bash
/bin/bash ./DELL_rate.sh
/bin/bash /home/DellFiles/bin/dell-run-main.sh rate
/bin/bash /home/DellFiles/bin/dell-run-main.sh rate
/bin/bash /home/DellFiles/bin/AMD/dell-run-speccpu.sh rate --define DL-BIOSinc=Dell-BIOS_EPYC-4.inc --define
DL-BIOS-LogProc=1 --define DL-BIOS-adddcD=1 --define DL-VERS=5.2 --output_format html,pdf,txt
python3 ./run_amd_rate_aocc400_znver4_A1.py

```

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Platform Notes (Continued)

```
/bin/bash ./amd_rate_aocc400_znver4_A1.sh
runcpu --config amd_rate_aocc400_znver4_A1.cfg --tune all --reportable --iterations 2 --define
DL-BIOS-L3NUMA=1 --define DL-BIOS-NPS=4 --define DL-BIOSinc=Dell-BIOS_EPYC-4.inc --define
DL-BIOS-LogProc=1 --define DL-BIOS-adddcD=1 --define DL-VERS=5.2 --output_format html,pdf,txt intrate
runcpu --configfile amd_rate_aocc400_znver4_A1.cfg --tune all --reportable --iterations 2 --define
DL-BIOS-L3NUMA=1 --define DL-BIOS-NPS=4 --define DL-BIOSinc=Dell-BIOS_EPYC-4.inc --define
DL-BIOS-LogProc=1 --define DL-BIOS-adddcD=1 --define DL-VERS=5.2 --output_format html,pdf,txt --nopower
--runmode rate --tune base:peak --size test:train:refrate intrate --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.001/templots/preenv.intrate.001.0.log --lognum 001.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /mnt/ramdisk/cpu2017-1.1.9-aocc400-znver4-A1.1
```

```
6. /proc/cpuinfo
model name      : AMD EPYC 9224 24-Core Processor
vendor_id      : AuthenticAMD
cpu family     : 25
model          : 17
stepping       : 1
microcode      : 0xa10113e
bugs           : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass srso
TLB size       : 3584 4K pages
cpu cores      : 24
siblings       : 48
1 physical ids (chips)
48 processors (hardware threads)
physical id 0: core ids 0-5,8-13,16-21,24-29
physical id 0: apicids 0-11,16-27,32-43,48-59
Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
virtualized systems. Use the above data carefully.
```

```
7. lscpu

From lscpu from util-linux 2.37.2:
Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Address sizes:         52 bits physical, 57 bits virtual
Byte Order:            Little Endian
CPU(s):                48
On-line CPU(s) list:   0-47
Vendor ID:             AuthenticAMD
Model name:            AMD EPYC 9224 24-Core Processor
CPU family:            25
Model:                 17
Thread(s) per core:    2
Core(s) per socket:    24
Socket(s):              1
Stepping:              1
BogoMIPS:               5001.80
Flags:                 fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp
lm constant_tsc rep_good nopl nonstop_tsc cpuid extd_apicid aperfmperf
rapl pni pclmulqdq monitor sse3 fma cx16 pcid sse4_1 sse4_2 x2apic
movbe popcnt aes xsave avx f16c rdrand lahf_lm cmp_legacy svm extapic
cr8_legacy abm sse4a misalignsse 3dnowprefetch osvw ibs skinit wdt tce
topoext perfctr_core perfctr_nb bpext perfctr_llc mwaitx cpb cat_l3
cdp_l3 invpcid_single hw_pstate ssbd mba ibrs ibpb stibp vmmcall
fsgsbase bmi1 avx2 smep bmi2 erms invpcid cqm rdt_a avx512f avx512dq
rdseed adx smap avx512ifma clflushopt clwb avx512cd sha_ni avx512bw
```

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Platform Notes (Continued)

```
avx512vl xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc
cqm_mbm_total cqm_mbm_local avx512_bf16 clzero irperf xsaveerptr rdpru
wbnoinvd amd_ppin cppc arat npt lbrv svm_lock nrip_save tsc_scale
vmcb_clean flushbyasid decodeassists pausefilter pfthreshold avic
v_vmsave_vmload vgif v_spec_ctrl avx512vbmi umip pku ospke avx512_vbmi2
gfni vaes vpclmulqdq avx512_vnni avx512_bitalg avx512_vpopcntdq la57
rdpid overflow_recov succor smca fsrm flush_lld
```

```
Virtualization: AMD-V
L1d cache: 768 KiB (24 instances)
L1i cache: 768 KiB (24 instances)
L2 cache: 24 MiB (24 instances)
L3 cache: 64 MiB (4 instances)
NUMA node(s): 4
NUMA node0 CPU(s): 0-5,24-29
NUMA node1 CPU(s): 6-11,30-35
NUMA node2 CPU(s): 12-17,36-41
NUMA node3 CPU(s): 18-23,42-47
Vulnerability Gather data sampling: Not affected
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf: Not affected
Vulnerability Mds: Not affected
Vulnerability Meltdown: Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Retbleed: Not affected
Vulnerability Spec rstack overflow: Mitigation; safe RET
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp
Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Retpolines, IBPB conditional, IBRS_FW, STIBP always-on, RSB
filling, PBRSE-eIBRS Not affected
Vulnerability Srbds: Not affected
Vulnerability Tsx async abort: Not affected
```

```
From lscpu --cache:
NAME ONE-SIZE ALL-SIZE WAYS TYPE LEVEL SETS PHY-LINE COHERENCY-SIZE
L1d 32K 768K 8 Data 1 64 1 64
L1i 32K 768K 8 Instruction 1 64 1 64
L2 1M 24M 8 Unified 2 2048 1 64
L3 16M 64M 16 Unified 3 16384 1 64
```

```
8. numactl --hardware
NOTE: a numactl 'node' might or might not correspond to a physical chip.
available: 4 nodes (0-3)
node 0 cpus: 0-5,24-29
node 0 size: 193031 MB
node 0 free: 192483 MB
node 1 cpus: 6-11,30-35
node 1 size: 193532 MB
node 1 free: 193003 MB
node 2 cpus: 12-17,36-41
node 2 size: 193532 MB
node 2 free: 189412 MB
node 3 cpus: 18-23,42-47
node 3 size: 193493 MB
node 3 free: 193051 MB
node distances:
node 0 1 2 3
0: 10 12 12 12
1: 12 10 12 12
2: 12 12 10 12
```

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Platform Notes (Continued)

3: 12 12 12 10

9. /proc/meminfo
MemTotal: 792155712 kB

10. who -r
run-level 5 Apr 12 08:37

11. Systemd service manager version: systemd 249 (249.11-0ubuntu3.12)
Default Target Status
graphical running

12. Services, from systemctl list-unit-files

STATE	UNIT FILES
enabled	ModemManager apparmor blk-availability console-setup cron dmesg e2scrub_reap finalrd getty@ gpu-manager grub-common grub-initrd-fallback irqbalance keyboard-setup lm-sensors lvm2-monitor lxd-agent multipathd networkd-dispatcher open-vm-tools pollinate rsyslog secureboot-db setvtrgb ssh systemd-networkd systemd-pstore systemd-resolved systemd-timesyncd thermald tuned ua-reboot-cmds ubuntu-advantage udisks2 ufw vgauth
enabled-runtime	netplan-ovs-cleanup systemd-fsck-root systemd-remount-fs
disabled	console-getty debug-shell iscsid nftables open-iscsi rsync serial-getty@ systemd-boot-check-no-failures systemd-network-generator systemd-sysext systemd-time-wait-sync upower
generated	apport
indirect	uuid
masked	cryptdisks cryptdisks-early hwclock lvm2 multipath-tools-boot rc rcS screen-cleanup sudo systemd-networkd-wait-online x11-common

13. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=/vmlinuz-5.15.0-102-generic
root=/dev/mapper/ubuntu--vg-ubuntu--lv
ro

14. cpupower frequency-info
analyzing CPU 0:
Unable to determine current policy
boost state support:
Supported: yes
Active: yes
Boost States: 0
Total States: 3
Pstate-P0: 2500MHz

15. tuned-adm active
Current active profile: latency-performance

16. sysctl

kernel.numa_balancing	1
kernel.randomize_va_space	0
vm.compaction_proactiveness	20
vm.dirty_background_bytes	0
vm.dirty_background_ratio	3

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Platform Notes (Continued)

```

vm.dirty_bytes          0
vm.dirty_expire_centisecs 3000
vm.dirty_ratio         8
vm.dirty_writeback_centisecs 500
vm.dirtytime_expire_seconds 43200
vm.extfrag_threshold   500
vm.min_unmapped_ratio  1
vm.nr_hugepages        0
vm.nr_hugepages_mempolicy 0
vm.nr_overcommit_hugepages 0
vm.swappiness          1
vm.watermark_boost_factor 15000
vm.watermark_scale_factor 10
vm.zone_reclaim_mode  1

```

```

-----
17. /sys/kernel/mm/transparent_hugepage
defrag      [always] defer+madvise madvise never
enabled     [always] madvise never
hpage_pmd_size 2097152
shmem_enabled always within_size advise [never] deny force

```

```

-----
18. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millsecs 60000
defrag               1
max_ptes_none       511
max_ptes_shared     256
max_ptes_swap       64
pages_to_scan       4096
scan_sleep_millsecs 10000

```

```

-----
19. OS release
From /etc/*-release /etc/*-version
os-release Ubuntu 22.04.4 LTS

```

```

-----
20. Disk information
SPEC is set to: /mnt/ramdisk/cpu2017-1.1.9-aocc400-znver4-A1.1
Filesystem      Type  Size  Used Avail Use% Mounted on
tmpfs            tmpfs 50G  3.4G  47G   7% /mnt/ramdisk

```

```

-----
21. /sys/devices/virtual/dmi/id
Vendor:      Dell Inc.
Product:     PowerEdge R6625
Product Family: PowerEdge
Serial:      SLR6625

```

```

-----
22. dmidecode
Additional information from dmidecode 3.3 follows.  WARNING: Use caution when you interpret this section.
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
"DMTF SMBIOS" standard.
Memory:
6x 80CE000080CE M321R8GA0BB0-CQKEG 64 GB 2 rank 4800
6x 80CE000080CE M321R8GA0BB0-CQKVG 64 GB 2 rank 4800
12x Not Specified Not Specified

```

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```

-----
23. BIOS
(This section combines info from /sys/devices and dmidecode.)
  BIOS Vendor:      Dell Inc.
  BIOS Version:     1.6.8
  BIOS Date:        11/24/2023
  BIOS Revision:    1.6

```

Compiler Version Notes

```

=====
C      | 502.gcc_r(peak)
-----
AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#434 2022_10_28) (based on LLVM Mirror.Version.14.0.6)
Target: i386-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin
-----

```

```

=====
C      | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak)
      | 557.xz_r(base, peak)
-----
AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#434 2022_10_28) (based on LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin
-----

```

```

=====
C      | 502.gcc_r(peak)
-----
AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#434 2022_10_28) (based on LLVM Mirror.Version.14.0.6)
Target: i386-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin
-----

```

```

=====
C      | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak)
      | 557.xz_r(base, peak)
-----
AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#434 2022_10_28) (based on LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin
-----

```

```

=====
C++    | 523.xalancbmk_r(peak)
-----
AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#434 2022_10_28) (based on LLVM Mirror.Version.14.0.6)
Target: i386-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin
-----

```

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Tested by: Dell Inc.

Test Date: Apr-2024

Hardware Availability: Feb-2023

Software Availability: Mar-2024

Compiler Version Notes (Continued)

C++ | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base) 531.deepsjeng_r(base, peak) 541.leela_r(base, peak)

AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#434 2022_10_28) (based on LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin

C++ | 523.xalancbmk_r(peak)

AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#434 2022_10_28) (based on LLVM Mirror.Version.14.0.6)
Target: i386-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin

C++ | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base) 531.deepsjeng_r(base, peak) 541.leela_r(base, peak)

AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#434 2022_10_28) (based on LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin

Fortran | 548.exchange2_r(base, peak)

AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#434 2022_10_28) (based on LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin

Base Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang



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Base Portability Flags

```
500.perlbench_r: -DSPEC_LINUX_X64 -DSPEC_LP64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LINUX -DSPEC_LP64
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64
```

Base Optimization Flags

C benchmarks:

```
-m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-ldist-scalar-expand -fenable-aggressive-gather
-z muldefs -O3 -march=znver4 -fveclib=AMDLIBM -ffast-math
-fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lflang
-lamdalloc
```

C++ benchmarks:

```
-m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -z muldefs -O3
-march=znver4 -fveclib=AMDLIBM -ffast-math
-mllvm -unroll-threshold=100 -finline-aggressive
-mllvm -loop-unswitch-threshold=200000
-mllvm -reduce-array-computations=3 -zopt
-fvirtual-function-elimination -fvisibility=hidden -lamdlibm -lflang
-lamdalloc-ext
```

Fortran benchmarks:

```
-m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-inline-recursion=4 -Wl,-mllvm -Wl,-lsr-in-nested-loop
-Wl,-mllvm -Wl,-enable-iv-split -z muldefs -O3 -march=znver4
-fveclib=AMDLIBM -ffast-math -fepilog-vectorization-of-inductions
-mllvm -optimize-strided-mem-cost -floop-transform
-mllvm -unroll-aggressive -mllvm -unroll-threshold=500 -lamdlibm
-lflang -lamdalloc
```



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Base Other Flags

C benchmarks:

-Wno-unused-command-line-argument

C++ benchmarks:

-Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

Peak Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

Peak Portability Flags

500.perlbench_r: -DSPEC_LINUX_X64 -DSPEC_LP64

502.gcc_r: -D_FILE_OFFSET_BITS=64

505.mcf_r: -DSPEC_LP64

520.omnetpp_r: -DSPEC_LP64

523.xalancbmk_r: -DSPEC_LINUX -DSPEC_LP64

525.x264_r: -DSPEC_LP64

531.deepsjeng_r: -DSPEC_LP64

541.leela_r: -DSPEC_LP64

548.exchange2_r: -DSPEC_LP64

557.xz_r: -DSPEC_LP64

Peak Optimization Flags

C benchmarks:

500.perlbench_r: -m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6

-Wl,-mllvm -Wl,-reduce-array-computations=3

-fprofile-instr-generate(pass 1)

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Peak Optimization Flags (Continued)

500.perlbench_r (continued):

```
-fprofile-instr-use(pass 2) -Ofast -march=znver4
-fveclib=AMDLIBM -ffast-math -fstruct-layout=7
-mllvm -unroll-threshold=50 -fremap-arrays
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3
-faggressive-loop-transform -fvector-transform
-fscalar-transform -lamdlibm -lflang -lamdalloc
```

502.gcc_r: -m32 -flto -z muldefs -Ofast -march=znver4

```
-fveclib=AMDLIBM -ffast-math -fstruct-layout=7
-mllvm -unroll-threshold=50 -fremap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -fgnu89-inline
-lamdalloc
```

505.mcf_r: basepeak = yes

525.x264_r: basepeak = yes

557.xz_r: -m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6

```
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver4 -fveclib=AMDLIBM -ffast-math
-fstruct-layout=7 -mllvm -unroll-threshold=50
-fremap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lflang -lamdalloc
```

C++ benchmarks:

520.omnetpp_r: -m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6

```
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver4 -fveclib=AMDLIBM -ffast-math
-finline-aggressive -mllvm -unroll-threshold=100
-mllvm -reduce-array-computations=3 -zopt
-fvirtual-function-elimination -fvisibility=hidden
-lamdlibm -lamdalloc-ext
```

523.xalancbmk_r: -m32 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6

```
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-do-block-reorder=aggressive
-fno-loop-reroll -Ofast -march=znver4 -fveclib=AMDLIBM
-ffast-math -finline-aggressive
-mllvm -unroll-threshold=100
-mllvm -reduce-array-computations=3 -zopt
```

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Peak Optimization Flags (Continued)

523.xalancbmk_r (continued):

```
-mllvm -do-block-reorder=aggressive  
-fvirtual-function-elimination -fvisibility=hidden  
-lamdalloc-ext
```

531.deepsjeng_r: basepeak = yes

541.leela_r: basepeak = yes

Fortran benchmarks:

```
-m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-inline-recursion=4 -Wl,-mllvm -Wl,-lsr-in-nested-loop  
-Wl,-mllvm -Wl,-enable-iv-split -O3 -march=znver4 -fveclib=AMDLIBM  
-ffast-math -fepilog-vectorization-of-inductions  
-mllvm -optimize-strided-mem-cost -floop-transform  
-mllvm -unroll-aggressive -mllvm -unroll-threshold=500 -lamdlibm  
-lflang -lamdalloc
```

Peak Other Flags

C benchmarks (except as noted below):

```
-Wno-unused-command-line-argument
```

502.gcc_r: -L/usr/lib32 -Wno-unused-command-line-argument

```
-L/home/work/cpu2017/v119/aocc4/znver4/rate/amd_rate_aocc400_znver4_A_lib/lib32
```

C++ benchmarks (except as noted below):

```
-Wno-unused-command-line-argument
```

523.xalancbmk_r: -L/usr/lib32 -Wno-unused-command-line-argument

```
-L/home/work/cpu2017/v119/aocc4/znver4/rate/amd_rate_aocc400_znver4_A_lib/lib32
```

Fortran benchmarks:

```
-Wno-unused-command-line-argument
```

The flags files that were used to format this result can be browsed at

http://www.spec.org/cpu2017/flags/aocc400-flags_A1.1.html

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-AMD-EPYC-v1.2.html>

You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2017/flags/aocc400-flags_A1.1.xml

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-AMD-EPYC-v1.2.xml>



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