



# SPEC CPU®2017 Floating Point Rate Result

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## Lenovo Global Technology

ThinkSystem SR250 V3  
(3.50 GHz, Intel Xeon E-2486)

SPECrate®2017\_fp\_base = 94.0

SPECrate®2017\_fp\_peak = 94.3

CPU2017 License: 9017

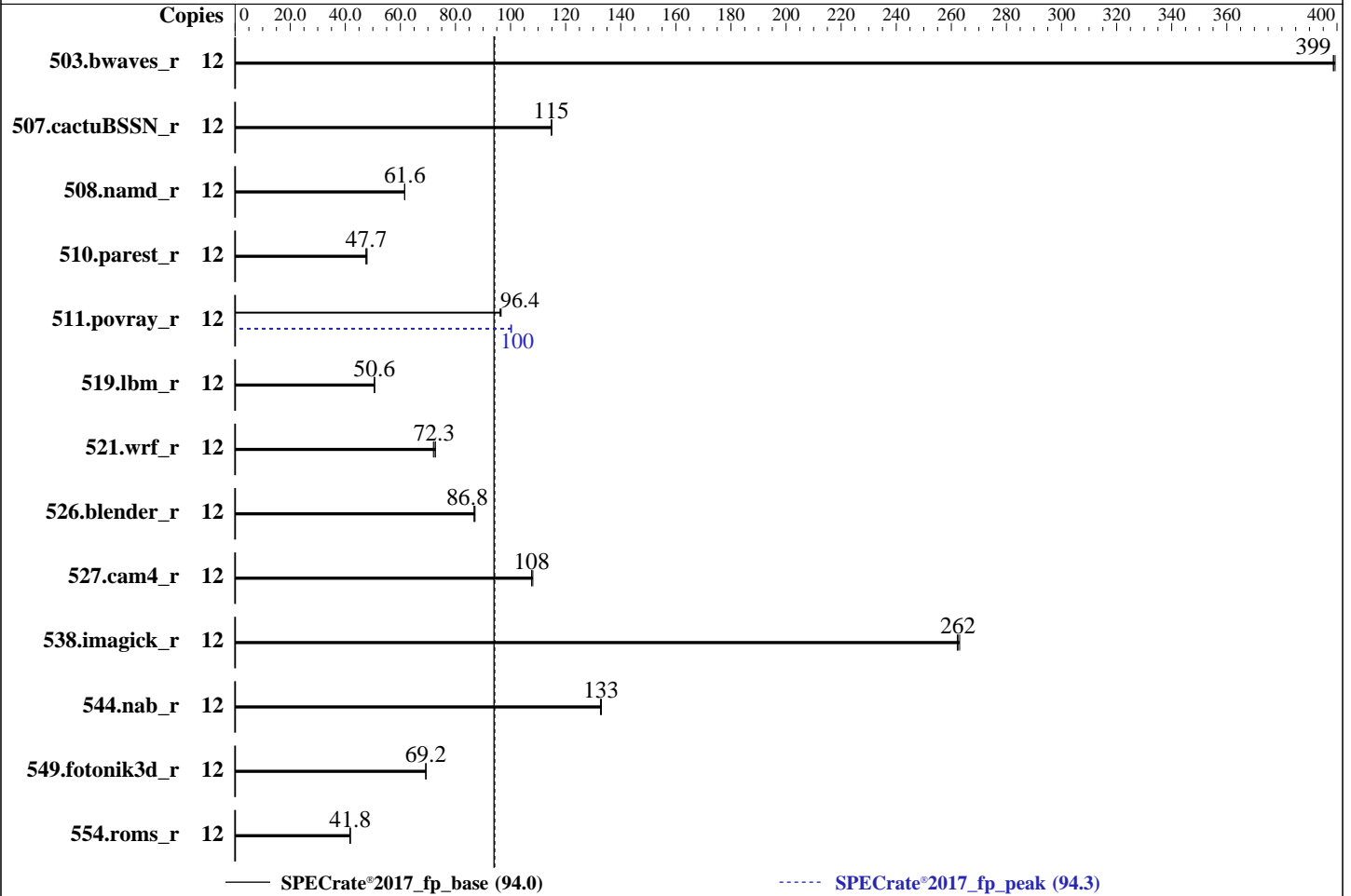
Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Feb-2024

Hardware Availability: Feb-2024

Software Availability: Dec-2023



### Hardware

CPU Name: Intel Xeon E-2486  
 Max MHz: 5600  
 Nominal: 3500  
 Enabled: 6 cores, 1 chip, 2 threads/core  
 Orderable: 1 chip  
 Cache L1: 32 KB I + 48 KB D on chip per core  
 L2: 2 MB I+D on chip per core  
 L3: 18 MB I+D on chip per chip  
 Other: None  
 Memory: 64 GB (2 x 32 GB 2Rx8 PC5-4800B-E, running at 4400)  
 Storage: 1 x 480 GB SATA SSD  
 Other: None

### Software

OS: Red Hat Enterprise Linux 9.3 (Plow)  
 Kernel 5.14.0-362.8.1.el9\_3.x86\_64  
 Compiler: C/C++: Version 2023.2.3 of Intel oneAPI DPC++/C++ Compiler for Linux;  
 Fortran: Version 2023.2.3 of Intel Fortran Compiler for Linux;  
 Parallel: No  
 Firmware: Lenovo BIOS Version CTE101Z 1.10 released Dec-2023  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 64-bit  
 Other: jemalloc memory allocator V5.0.1  
 Power Management: BIOS and OS set to prefer performance at the cost of additional power usage



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	12	302	399	301	399	<b>302</b>	<b>399</b>	12	302	399	301	399	<b>302</b>	<b>399</b>
507.cactuBSSN_r	12	<b>132</b>	<b>115</b>	132	115	132	115	12	<b>132</b>	<b>115</b>	132	115	132	115
508.namd_r	12	185	61.5	185	61.6	<b>185</b>	<b>61.6</b>	12	185	61.5	185	61.6	<b>185</b>	<b>61.6</b>
510.parest_r	12	655	47.9	<b>659</b>	<b>47.7</b>	661	47.5	12	655	47.9	<b>659</b>	<b>47.7</b>	661	47.5
511.povray_r	12	290	96.6	<b>291</b>	<b>96.4</b>	291	96.2	12	279	100	280	100	<b>280</b>	<b>100</b>
519.lbm_r	12	250	50.6	250	50.6	<b>250</b>	<b>50.6</b>	12	250	50.6	250	50.6	<b>250</b>	<b>50.6</b>
521.wrf_r	12	370	72.7	373	72.0	<b>372</b>	<b>72.3</b>	12	370	72.7	373	72.0	<b>372</b>	<b>72.3</b>
526.blender_r	12	<b>210</b>	<b>86.8</b>	210	87.1	211	86.8	12	<b>210</b>	<b>86.8</b>	210	87.1	211	86.8
527.cam4_r	12	194	108	195	108	<b>195</b>	<b>108</b>	12	194	108	195	108	<b>195</b>	<b>108</b>
538.imagick_r	12	113	263	<b>114</b>	<b>262</b>	114	262	12	113	263	<b>114</b>	<b>262</b>	114	262
544.nab_r	12	<b>152</b>	<b>133</b>	152	133	152	133	12	<b>152</b>	<b>133</b>	152	133	152	133
549.fotonik3d_r	12	675	69.3	<b>675</b>	<b>69.2</b>	675	69.2	12	675	69.3	<b>675</b>	<b>69.2</b>	675	69.2
554.roms_r	12	<b>456</b>	<b>41.8</b>	457	41.7	456	41.8	12	<b>456</b>	<b>41.8</b>	457	41.7	456	41.8

SPECrate®2017\_fp\_base = **94.0**

SPECrate®2017\_fp\_peak = **94.3**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:

LD\_LIBRARY\_PATH = "/home/cpu2017-1.1.9-ic2023.2.3/lib/intel64:/home/cpu2017-1.1.9-ic2023.2.3/je5.0.1-64"  
MALLOCONF = "retain:true"

## General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Red Hat Enterprise Linux 8.4  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation

Filesystem page cache synced and cleared with:  
sync; echo 3> /proc/sys/vm/drop\_caches

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)

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### General Notes (Continued)

is mitigated in the system as tested and documented.  
jemalloc, a general purpose malloc implementation  
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5  
sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

### Platform Notes

BIOS configuration:  
Choose Operating Mode set to Maximum Performance  
Turbo Limit for 95W CPU set to Disabled

Sysinfo program /home/cpu2017-1.1.9-ic2023.2.3/bin/sysinfo  
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197  
running on localhost.localdomain Wed Feb 21 20:50:55 2024

SUT (System Under Test) info as seen by some common utilities.

#### Table of contents

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 252 (252-18.el9)
12. Failed units, from systemctl list-units --state=failed
13. Services, from systemctl list-unit-files
14. Linux kernel boot-time arguments, from /proc/cmdline
15. cpupower frequency-info
16. tuned-adm active
17. sysctl
18. /sys/kernel/mm/transparent\_hugepage
19. /sys/kernel/mm/transparent\_hugepage/khugepaged
20. OS release
21. Disk information
22. /sys/devices/virtual/dmi/id
23. dmidecode
24. BIOS

```
1. uname -a
Linux localhost.localdomain 5.14.0-362.8.1.el9_3.x86_64 #1 SMP PREEMPT_DYNAMIC Tue Oct 3 11:12:36 EDT 2023
x86_64 x86_64 x86_64 GNU/Linux
```

```
2. w
20:50:55 up 4:19, 1 user, load average: 9.60, 11.46, 11.82
USER      TTY      LOGIN@   IDLE   JCPU   PCPU   WHAT
root     tty1      16:32    4:18m  0.71s  0.00s  -bash
```

3. Username

(Continued on next page)



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### Platform Notes (Continued)

From environment variable \$USER: root

```

-----
4. ulimit -a
real-time non-blocking time (microseconds, -R) unlimited
core file size (blocks, -c) 0
data seg size (kbytes, -d) unlimited
scheduling priority (-e) 0
file size (blocks, -f) unlimited
pending signals (-i) 256653
max locked memory (kbytes, -l) 64
max memory size (kbytes, -m) unlimited
open files (-n) 1024
pipe size (512 bytes, -p) 8
POSIX message queues (bytes, -q) 819200
real-time priority (-r) 0
stack size (kbytes, -s) unlimited
cpu time (seconds, -t) unlimited
max user processes (-u) 256653
virtual memory (kbytes, -v) unlimited
file locks (-x) unlimited

```

```

-----
5. sysinfo process ancestry
/usr/lib/systemd/systemd rhgb --switched-root --system --deserialize 31
login -- root
-bash
-bash
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=12 -c
ic2023.2.3-lin-core-avx2-rate-20231121.cfg --define smt-on --define cores=6 --define physicallogical
--define no-numa --tune base,peak -o all --define drop_caches fprate
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=12 --configfile
ic2023.2.3-lin-core-avx2-rate-20231121.cfg --define smt-on --define cores=6 --define physicallogical
--define no-numa --tune base,peak --output_format all --define drop_caches --nopower --runmode rate --tune
base:peak --size refrate fprate --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.106/templogs/preenv.fprate.106.0.log --lognum 106.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017-1.1.9-ic2023.2.3

```

```

-----
6. /proc/cpuinfo
model name      : Intel(R) Xeon(R) E E-2486
vendor_id      : GenuineIntel
cpu family     : 6
model          : 183
stepping       : 1
microcode      : 0x11f
bugs           : spectre_v1 spectre_v2 spec_store_bypass swapgs eibrs_pbrsb
cpu cores     : 6
siblings      : 12
1 physical ids (chips)
12 processors (hardware threads)
physical id 0: core ids 0-5
physical id 0: apicids 0-11
Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
virtualized systems. Use the above data carefully.

```

```

-----
7. lscpu

```

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## Platform Notes (Continued)

From lscpu from util-linux 2.37.4:

```

Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Address sizes:         42 bits physical, 48 bits virtual
Byte Order:            Little Endian
CPU(s):                12
On-line CPU(s) list:   0-11
Vendor ID:             GenuineIntel
BIOS Vendor ID:       Intel(R) Corporation
Model name:            Intel(R) Xeon(R) E E-2486
BIOS Model name:      Intel(R) Xeon(R) E E-2486
CPU family:            6
Model:                 183
Thread(s) per core:    2
Core(s) per socket:    6
Socket(s):             1
Stepping:              1
BogoMIPS:              6988.80
Flags:                 fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                      clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb
                      rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl
                      xtopology nonstop_tsc cpuid aperfperf tsc_known_freq pni pclmulqdq
                      dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm
                      sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c
                      rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb ssbd ibrs ibpb stibp
                      ibrs_enhanced tpr_shadow flexpriority ept vpid ept_ad fsgsbase
                      tsc_adjust bmi1 avx2 smep bmi2 erms invpcid rdseed adx smap clflushopt
                      clwb intel_pt sha_ni xsaveopt xsavec xgetbv1 xsaves split_lock_detect
                      avx_vnni dtherm ida arat pln pts hfi vnmi umip pku ospke waitpkg gfni
                      vaes vpclmulqdq tme rdpid movdiri movdir64b fsrm md_clear serialize
                      pconfig arch_lbr ibt flush_lld arch_capabilities
Virtualization:        VT-x
L1d cache:             288 KiB (6 instances)
L1i cache:             192 KiB (6 instances)
L2 cache:              12 MiB (6 instances)
L3 cache:              18 MiB (1 instance)
NUMA node(s):          1
NUMA node0 CPU(s):    0-11
Vulnerability Gather data sampling: Not affected
Vulnerability Itlb multihit:      Not affected
Vulnerability L1tf:               Not affected
Vulnerability Mds:                 Not affected
Vulnerability Meltdown:           Not affected
Vulnerability Mmio stale data:    Not affected
Vulnerability Retbleed:           Not affected
Vulnerability Spec store bypass:   Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1:         Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2:         Mitigation; Enhanced / Automatic IBRS, IBPB conditional, RSB filling,
                      PBRSE-eIBRS SW sequence
Vulnerability Srbds:              Not affected
Vulnerability Tsx async abort:    Not affected

```

Virtualization:

```

L1d cache:             288 KiB (6 instances)
L1i cache:             192 KiB (6 instances)
L2 cache:              12 MiB (6 instances)
L3 cache:              18 MiB (1 instance)
NUMA node(s):          1
NUMA node0 CPU(s):    0-11

```

```

Vulnerability Gather data sampling: Not affected
Vulnerability Itlb multihit:      Not affected
Vulnerability L1tf:               Not affected
Vulnerability Mds:                 Not affected
Vulnerability Meltdown:           Not affected
Vulnerability Mmio stale data:    Not affected
Vulnerability Retbleed:           Not affected
Vulnerability Spec store bypass:   Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1:         Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2:         Mitigation; Enhanced / Automatic IBRS, IBPB conditional, RSB filling,
                      PBRSE-eIBRS SW sequence
Vulnerability Srbds:              Not affected
Vulnerability Tsx async abort:    Not affected

```

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	288K	12	Data	1	64	1	64
L1i	32K	192K	8	Instruction	1	64	1	64
L2	2M	12M	16	Unified	2	2048	1	64
L3	18M	18M	9	Unified	3	32768	1	64

(Continued on next page)



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### Platform Notes (Continued)

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```
available: 1 nodes (0)
node 0 cpus: 0-11
node 0 size: 64206 MB
node 0 free: 62800 MB
node distances:
node 0
0: 10
```

9. /proc/meminfo

```
MemTotal: 65747204 kB
```

10. who -r

```
run-level 3 Feb 21 16:31
```

11. Systemd service manager version: systemd 252 (252-18.el9)

```
Default Target Status
multi-user degraded
```

12. Failed units, from systemctl list-units --state=failed

```
UNIT LOAD ACTIVE SUB DESCRIPTION
* dnf-makecache.service loaded failed failed dnf makecache
```

13. Services, from systemctl list-unit-files

```
STATE UNIT FILES
enabled NetworkManager NetworkManager-dispatcher NetworkManager-wait-online atd auditd bluetooth
chronyd crond dbus-broker firewalld getty@ insights-client-boot irqbalance iscsi
iscsi-onboot kdump libstoragemgmt low-memory-monitor lvm2-monitor mcelog mdmonitor
microcode multipathd nis-domainname nvme-fc-boot-connections rhsmcertd rsyslog rtkit-daemon
selinux-autorelabel-mark smartd sshd sssd systemd-boot-update systemd-network-generator
tuned udisks2 upower
enabled-runtime systemd-remount-fs
disabled arp-ethers blk-availability canberra-system-bootup canberra-system-shutdown
canberra-system-shutdown-reboot chrony-wait console-getty cpupower debug-shell
dnf-system-upgrade iprump iprinit iprupdate iscsid iscsiuiop kpatch kvm_stat ledmon
man-db-restart-cache-update nftables nvme-fc-autoconnect pesign psacct rdisc rhcd rhsm
rhsm-facts rpmdm-rebuild selinux-check-proper-disable serial-getty@ sshd-keygen@
systemd-boot-check-no-failures systemd-pstore systemd-sysex
indirect sssd-autofs sssd-kcm sssd-nss sssd-pac sssd-pam sssd-ssh sssd-sudo systemd-sysupdate
systemd-sysupdate-reboot
```

14. Linux kernel boot-time arguments, from /proc/cmdline

```
BOOT_IMAGE=(hd0,gpt2)/vmlinuz-5.14.0-362.8.1.el9_3.x86_64
root=/dev/mapper/rhel-root
ro
resume=/dev/mapper/rhel-swap
rd.lvm.lv=rhel/root
rd.lvm.lv=rhel/swap
rhgb
quiet
```

15. cpupower frequency-info

(Continued on next page)



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### Platform Notes (Continued)

```
analyzing CPU 0:
  Unable to determine current policy
boost state support:
  Supported: yes
  Active: yes
```

```
-----
16. tuned-adm active
  Current active profile: throughput-performance
-----
```

```
17. sysctl
  kernel.numa_balancing          0
  kernel.randomize_va_space     2
  vm.compaction_proactiveness   20
  vm.dirty_background_bytes     0
  vm.dirty_background_ratio     10
  vm.dirty_bytes                 0
  vm.dirty_expire_centisecs     3000
  vm.dirty_ratio                 40
  vm.dirty_writeback_centisecs  500
  vm.dirtytime_expire_seconds   43200
  vm.extfrag_threshold          500
  vm.min_unmapped_ratio         1
  vm.nr_hugepages                0
  vm.nr_hugepages_mempolicy     0
  vm.nr_overcommit_hugepages   0
  vm.swappiness                  10
  vm.watermark_boost_factor     15000
  vm.watermark_scale_factor     10
  vm.zone_reclaim_mode          0
-----
```

```
18. /sys/kernel/mm/transparent_hugepage
  defrag          always defer defer+madvice [madvice] never
  enabled         [always] madvice never
  hpage_pmd_size 2097152
  shmem_enabled  always within_size advise [never] deny force
-----
```

```
19. /sys/kernel/mm/transparent_hugepage/khugepaged
  alloc_sleep_millisecs  60000
  defrag                  1
  max_ptes_none          511
  max_ptes_shared        256
  max_ptes_swap           64
  pages_to_scan           4096
  scan_sleep_millisecs   10000
-----
```

```
20. OS release
  From /etc/*-release /etc/*-version
  os-release      Red Hat Enterprise Linux 9.3 (Plow)
  redhat-release  Red Hat Enterprise Linux release 9.3 (Plow)
  system-release  Red Hat Enterprise Linux release 9.3 (Plow)
-----
```

```
21. Disk information
SPEC is set to: /home/cpu2017-1.1.9-ic2023.2.3
Filesystem      Type  Size  Used Avail Use% Mounted on
-----
```

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### Platform Notes (Continued)

/dev/mapper/rhel-home xfs 344G 15G 330G 5% /home

-----  
22. /sys/devices/virtual/dmi/id  
Vendor: Lenovo  
Product: ThinkSystem SR250 V3  
Product Family: ThinkSystem  
Serial: 1234567890  
-----

23. dmidecode  
Additional information from dmidecode 3.5 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.  
Memory:  
1x Samsung M324R4GA3BB0-CQKOD 32 GB 2 rank 4800, configured at 4400  
1x Samsung M324R4GA3BB0-CQKOL 32 GB 2 rank 4800, configured at 4400  
-----

24. BIOS  
(This section combines info from /sys/devices and dmidecode.)  
BIOS Vendor: Lenovo  
BIOS Version: CTE101Z-1.10  
BIOS Date: 12/27/2023  
BIOS Revision: 1.10  
Firmware Revision: 1.10  
-----

### Compiler Version Notes

=====  
C | 519.lbm\_r(base, peak) 538.imagick\_r(base, peak) 544.nab\_r(base, peak)  
-----

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.  
-----

=====  
C++ | 508.namd\_r(base, peak) 510.parest\_r(base, peak)  
-----

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.  
-----

=====  
C++, C | 511.povray\_r(base, peak) 526.blender\_r(base, peak)  
-----

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.  
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x  
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-----

=====  
C++, C, Fortran | 507.cactuBSSN\_r(base, peak)  
-----

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## Lenovo Global Technology

ThinkSystem SR250 V3  
(3.50 GHz, Intel Xeon E-2486)

SPECrate®2017\_fp\_base = 94.0

SPECrate®2017\_fp\_peak = 94.3

**CPU2017 License:** 9017

**Test Sponsor:** Lenovo Global Technology

**Tested by:** Lenovo Global Technology

**Test Date:** Feb-2024

**Hardware Availability:** Feb-2024

**Software Availability:** Dec-2023

### Compiler Version Notes (Continued)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.  
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

-----  
Fortran | 503.bwaves\_r(base, peak) 549.fotonik3d\_r(base, peak) 554.roms\_r(base, peak)  
-----

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

-----  
Fortran, C | 521.wrf\_r(base, peak) 527.cam4\_r(base, peak)  
-----

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.  
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x  
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### Base Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using both C and C++:

icpx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx

### Base Portability Flags

503.bwaves\_r: -DSPEC\_LP64  
507.cactuBSSN\_r: -DSPEC\_LP64  
508.namd\_r: -DSPEC\_LP64

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# SPEC CPU®2017 Floating Point Rate Result

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## Base Portability Flags (Continued)

```

510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

```

## Base Optimization Flags

### C benchmarks:

```

-w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX2 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-Wno-implicit-int -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

```

### C++ benchmarks:

```

-w -std=c++14 -m64 -Wl,-z,muldefs -xCORE-AVX2 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

```

### Fortran benchmarks:

```

-w -m64 -Wl,-z,muldefs -xCORE-AVX2 -Ofast -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

```

### Benchmarks using both Fortran and C:

```

-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX2 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-Wno-implicit-int -nostandard-realloc-lhs -align array32byte -auto
-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

```

### Benchmarks using both C and C++:

```

-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX2 -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

```

### Benchmarks using Fortran, C, and C++:

```

-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX2 -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops

```

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## Base Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++ (continued):

```
-qopt-mem-layout-trans=4 -Wno-implicit-int -nostandard-realloc-lhs  
-align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

## Peak Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using both C and C++:

icpx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

519.lbm\_r: basepeak = yes

538.imagick\_r: basepeak = yes

544.nab\_r: basepeak = yes

C++ benchmarks:

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

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## Peak Optimization Flags (Continued)

508.namd\_r: basepeak = yes

510.parest\_r: basepeak = yes

Fortran benchmarks:

503.bwaves\_r: basepeak = yes

549.fotonik3d\_r: basepeak = yes

554.roms\_r: basepeak = yes

Benchmarks using both Fortran and C:

521.wrf\_r: basepeak = yes

527.cam4\_r: basepeak = yes

Benchmarks using both C and C++:

```
511.povray_r: -w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs
-fprofile-generate(pass 1)
-fprofile-use=default.profddata(pass 2) -xCORE-AVX2 -flto
-Ofast -ffast-math -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

526.blender\_r: basepeak = yes

Benchmarks using Fortran, C, and C++:

507.cactuBSSN\_r: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Catlow-A.html>

<http://www.spec.org/cpu2017/flags/Intel-ic2023p2-official-linux64.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Catlow-A.xml>

<http://www.spec.org/cpu2017/flags/Intel-ic2023p2-official-linux64.xml>



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