



SPEC CPU®2017 Floating Point Rate Result

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Inspur Electronic Information Industry Co., Ltd.
NF5688M7 (Intel Xeon Platinum 8468)

SPECrate®2017_fp_base = 928
SPECrate®2017_fp_peak = 985

CPU2017 License: 3358

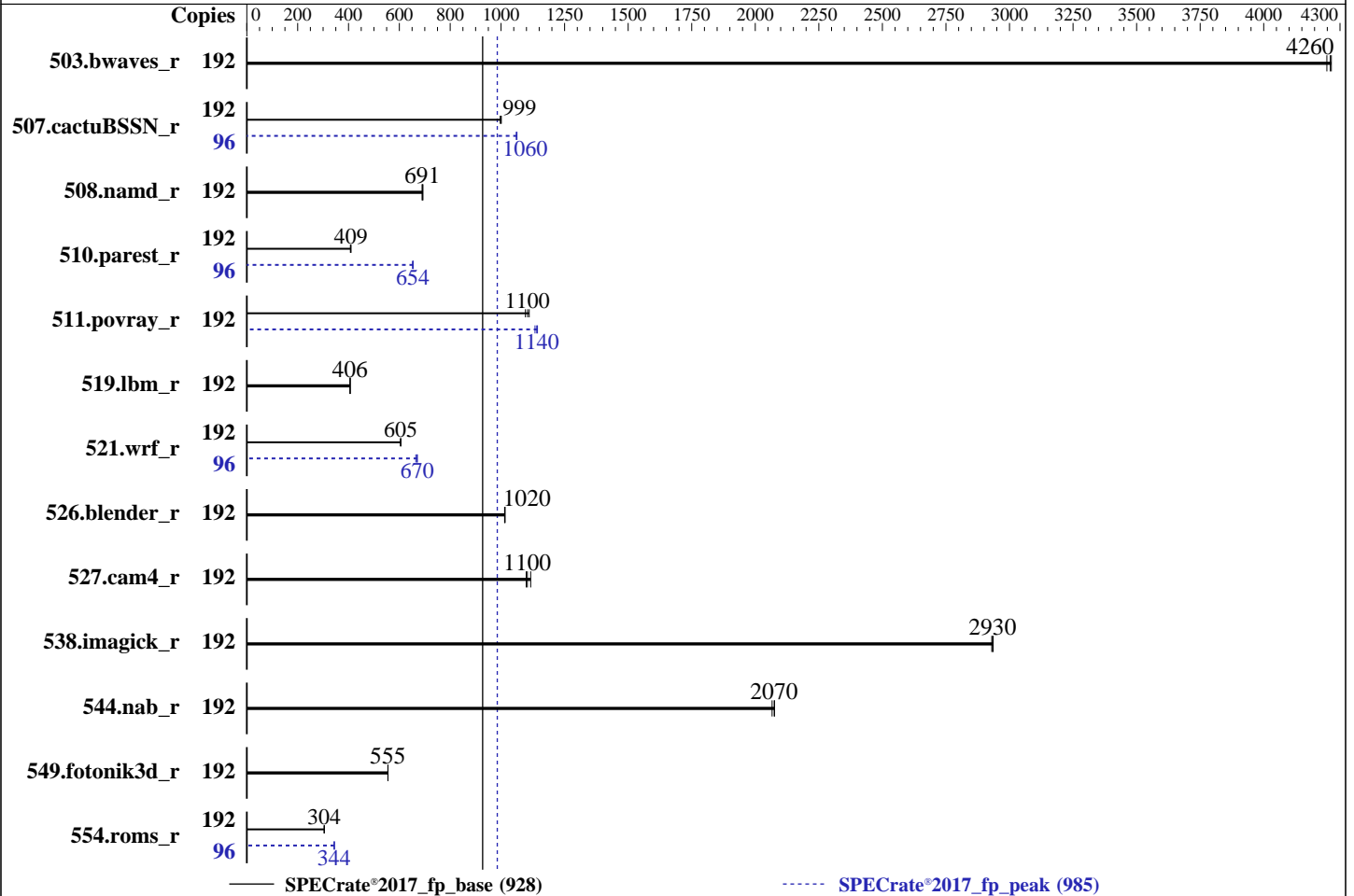
Test Sponsor: Inspur Electronic Information Industry Co., Ltd.

Tested by: Inspur Electronic Information Industry Co., Ltd.

Test Date: Aug-2023

Hardware Availability: Sep-2023

Software Availability: Dec-2022



Hardware

CPU Name: Intel Xeon Platinum 8468
 Max MHz: 3800
 Nominal: 2100
 Enabled: 96 cores, 2 chips, 2 threads/core
 Orderable: 1,2 chips
 Cache L1: 32 KB I + 48 KB D on chip per core
 L2: 2 MB I+D on chip per core
 L3: 105 MB I+D on chip per chip
 Other: None
 Memory: 512 GB (16 x 32 GB 2Rx4 PC5-4800B-R)
 Storage: 1 x 1 TB NVME SSD
 Other: None

Software

OS: Red Hat Enterprise Linux 9.0 (Plow)
 5.14.0-70.22.1.el9_0.x86_64
 Compiler: C/C++: Version 2023.0 of Intel oneAPI DPC++/C++
 Compiler for Linux;
 Fortran: Version 2023.0 of Intel Fortran Compiler
 for Linux;
 Parallel: No
 Firmware: Version 04.02.00 released Feb-2023
 File System: xfs
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 64-bit
 Other: jemalloc memory allocator V5.0.1
 Power Management: BIOS and OS set to prefer performance at the cost
 of additional power usage.



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	192	451	4270	453	4250	452	4260	192	451	4270	453	4250	452	4260
507.cactuBSSN_r	192	244	997	243	1000	243	999	96	115	1060	115	1060	114	1060
508.namd_r	192	264	690	264	691	263	693	192	264	690	264	691	263	693
510.parest_r	192	1227	409	1233	407	1227	409	96	385	652	384	654	384	654
511.povray_r	192	409	1100	406	1100	404	1110	192	393	1140	392	1140	395	1130
519.lbm_r	192	498	406	498	406	498	406	192	498	406	498	406	498	406
521.wrf_r	192	711	605	710	606	711	605	96	321	671	324	665	321	670
526.blender_r	192	288	1020	288	1010	288	1020	192	288	1020	288	1010	288	1020
527.cam4_r	192	306	1100	301	1120	304	1100	192	306	1100	301	1120	304	1100
538.imagick_r	192	163	2940	163	2930	163	2930	192	163	2940	163	2930	163	2930
544.nab_r	192	156	2080	156	2070	156	2070	192	156	2080	156	2070	156	2070
549.fotonik3d_r	192	1348	555	1348	555	1347	555	192	1348	555	1348	555	1347	555
554.roms_r	192	1000	305	1002	304	1002	304	96	444	344	443	344	444	344

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/CPU2017/lib/intel64:/home/CPU2017/je5.0.1-64"
MALLOC_CONF = "retain:true"

General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM
memory using Red Hat Enterprise Linux 8.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)

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General Notes (Continued)

is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

Platform Notes

BIOS configuration:
ENERGY_PERF_BIAS_CFG mode set to Performance
Hardware Prefetch set to Disable
VT Support set to Disable
Sub NUMA Cluster (SNC) set to SNC4

Sysinfo program /home/CPU2017/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on localhost Tue Aug 22 11:33:19 2023

SUT (System Under Test) info as seen by some common utilities.

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19. /sys/kernel/mm/transparent_hugepage/khugepaged
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23. dmidecode
24. BIOS

1. uname -a
Linux localhost 5.14.0-70.22.1.e19_0.x86_64 #1 SMP PREEMPT Tue Aug 2 10:02:12 EDT 2022 x86_64 x86_64 x86_64
GNU/Linux

2. w
11:33:19 up 6:10, 2 users, load average: 119.70, 173.93, 184.25

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Platform Notes (Continued)

USER	TTY	LOGIN@	IDLE	JCPU	PCPU	WHAT
root	tty1	05:23	6:09m	1.07s	0.00s	-bash
root	tty2	10:31	59:24	0.00s	0.00s	-bash

3. Username

From environment variable \$USER: root

4. ulimit -a

```
real-time non-blocking time (microseconds, -R) unlimited
core file size (blocks, -c) 0
data seg size (kbytes, -d) unlimited
scheduling priority (-e) 0
file size (blocks, -f) unlimited
pending signals (-i) 2062102
max locked memory (kbytes, -l) 64
max memory size (kbytes, -m) unlimited
open files (-n) 1024
pipe size (512 bytes, -p) 8
POSIX message queues (bytes, -q) 819200
real-time priority (-r) 0
stack size (kbytes, -s) unlimited
cpu time (seconds, -t) unlimited
max user processes (-u) 2062102
virtual memory (kbytes, -v) unlimited
file locks (-x) unlimited
```

5. sysinfo process ancestry

```
/usr/lib/systemd/systemd --switched-root --system --deserialize 28
login -- root
-bash
-bash
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=192 -c
ic2023.0-lin-sapphirerapids-rate-20221201.cfg --define smt-on --define cores=96 --define physicalfirst
--define invoke_with_interleave --define drop_caches --tune base,peak -o all fprate
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=192 --configfile
ic2023.0-lin-sapphirerapids-rate-20221201.cfg --define smt-on --define cores=96 --define physicalfirst
--define invoke_with_interleave --define drop_caches --tune base,peak --output_format all --nopower
--runmode rate --tune base:peak --size refrate fprate --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.005/templogs/preenv.fprate.005.0.log --lognum 005.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/CPU2017
```

6. /proc/cpuinfo

```
model name      : Intel(R) Xeon(R) Platinum 8468
vendor_id      : GenuineIntel
cpu family     : 6
model          : 143
stepping       : 6
microcode      : 0x2b000130
bugs           : spectre_v1 spectre_v2 spec_store_bypass swapgs
cpu cores      : 48
siblings       : 96
2 physical ids (chips)
192 processors (hardware threads)
physical id 0: core ids 0-47
physical id 1: core ids 0-47
```

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Platform Notes (Continued)

physical id 0: apicids 0-95
physical id 1: apicids 128-223

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

7. lscpu

From lscpu from util-linux 2.37.4:

```

Architecture:                x86_64
CPU op-mode(s):              32-bit, 64-bit
Address sizes:               52 bits physical, 57 bits virtual
Byte Order:                  Little Endian
CPU(s):                      192
On-line CPU(s) list:        0-191
Vendor ID:                   GenuineIntel
BIOS Vendor ID:             Intel(R) Corporation
Model name:                  Intel(R) Xeon(R) Platinum 8468
BIOS Model name:            Intel(R) Xeon(R) Platinum 8468
CPU family:                  6
Model:                       143
Thread(s) per core:         2
Core(s) per socket:         48
Socket(s):                   2
Stepping:                    6
CPU max MHz:                 3800.0000
CPU min MHz:                 800.0000
BogoMIPS:                    4200.00
Flags:                       fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                             clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
                             lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
                             nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq dtes64 ds_cpl
                             smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm dca sse4_1 sse4_2 x2apic
                             movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm
                             3dnowprefetch cpuid_fault epb cat_l3 cat_l2 cdp_l3 invpcid_single
                             intel_ppin cdp_l2 ssbd mba ibrs ibpb stibp ibrs_enhanced fsgsbase
                             tsc_adjust bmi1 avx2 smep bmi2 erms invpcid cqm rdt_a avx512f avx512dq
                             rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512cd sha_ni
                             avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc
                             cqm_mbm_total cqm_mbm_local split_lock_detect avx_vnni avx512_bf16
                             wbnoinvd dtherm ida arat pln pts hwp hwp_act_window hwp_epp hwp_pkg_req
                             avx512vbmi umip pku ospke waitpkg avx512_vbmi2 gfni vaes vpclmulqdq
                             avx512_vnni avx512_bitalg tme avx512_vpocntdq la57 rdpid bus_lock_detect
                             cldemote movdiri movdir64b enqcmd fsrm md_clear serialize tsxldtrk pconfig
                             arch_lbr avx512_fp16 amx_tile flush_llid arch_capabilities

L1d cache:                   4.5 MiB (96 instances)
L1i cache:                   3 MiB (96 instances)
L2 cache:                    192 MiB (96 instances)
L3 cache:                    210 MiB (2 instances)
NUMA node(s):                8
NUMA node0 CPU(s):          0-11,96-107
NUMA node1 CPU(s):          12-23,108-119
NUMA node2 CPU(s):          24-35,120-131
NUMA node3 CPU(s):          36-47,132-143
NUMA node4 CPU(s):          48-59,144-155
NUMA node5 CPU(s):          60-71,156-167
NUMA node6 CPU(s):          72-83,168-179
NUMA node7 CPU(s):          84-95,180-191
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf:         Not affected

```

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Platform Notes (Continued)

Vulnerability Mds: Not affected
 Vulnerability Meltdown: Not affected
 Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl
 Vulnerability Spectre v1: Mitigation; usercopy/swaps barriers and __user pointer sanitization
 Vulnerability Spectre v2: Mitigation; Enhanced IBRS, IBPB conditional, RSB filling
 Vulnerability Srbds: Not affected
 Vulnerability Tsx async abort: Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	4.5M	12	Data	1	64	1	64
L1i	32K	3M	8	Instruction	1	64	1	64
L2	2M	192M	16	Unified	2	2048	1	64
L3	105M	210M	15	Unified	3	114688	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```

available: 8 nodes (0-7)
node 0 cpus: 0-11,96-107
node 0 size: 64074 MB
node 0 free: 51862 MB
node 1 cpus: 12-23,108-119
node 1 size: 64470 MB
node 1 free: 54587 MB
node 2 cpus: 24-35,120-131
node 2 size: 64506 MB
node 2 free: 54629 MB
node 3 cpus: 36-47,132-143
node 3 size: 64506 MB
node 3 free: 54619 MB
node 4 cpus: 48-59,144-155
node 4 size: 64506 MB
node 4 free: 54581 MB
node 5 cpus: 60-71,156-167
node 5 size: 64506 MB
node 5 free: 54626 MB
node 6 cpus: 72-83,168-179
node 6 size: 64506 MB
node 6 free: 54632 MB
node 7 cpus: 84-95,180-191
node 7 size: 64486 MB
node 7 free: 54258 MB
node distances:
node  0  1  2  3  4  5  6  7
0:  10 12 12 12 21 21 21 21
1:  12 10 12 12 21 21 21 21
2:  12 12 10 12 21 21 21 21
3:  12 12 12 10 21 21 21 21
4:  21 21 21 21 10 12 12 12
5:  21 21 21 21 12 10 12 12
6:  21 21 21 21 12 12 10 12
7:  21 21 21 21 12 12 12 10

```

9. /proc/meminfo

MemTotal: 527939088 kB

10. who -r

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Platform Notes (Continued)

run-level 3 Aug 22 05:23

11. Systemd service manager version: systemd 250 (250-6.el9_0)

Default Target Status
multi-user degraded

12. Failed units, from systemctl list-units --state=failed

UNIT	LOAD	ACTIVE	SUB	DESCRIPTION
* dnf-makecache.service	loaded	failed	failed	dnf makecache

13. Services, from systemctl list-unit-files

STATE	UNIT FILES
enabled	dbus-broker getty@ tuned udisks2 upower
enabled-runtime	systemd-remount-fs
disabled	NetworkManager NetworkManager-dispatcher NetworkManager-wait-online auditd blk-availability canberra-system-bootup canberra-system-shutdown canberra-system-shutdown-reboot chrony-wait chronyd console-getty cpupower crond debug-shell firewalld irqbalance kdump kvm_stat lvm2-monitor man-db-restart-cache-update mdmonitor microcode nftables nis-domainname rdisc rhsm rhsm-facts rhsmcertd rpmdb-rebuild rsyslog selinux-autorelabel-mark sep5 serial-getty@ sshd sshd-keygen@ sssd systemd-boot-check-no-failures systemd-network-generator systemd-pstore systemd-sysext sssd-autofs sssd-kcm sssd-nss sssd-pac sssd-pam sssd-ssh sssd-sudo
indirect	

14. Linux kernel boot-time arguments, from /proc/cmdline

BOOT_IMAGE=(hd0,gpt2)/vmlinuz-5.14.0-70.22.1.el9_0.x86_64
root=/dev/mapper/rhel-root
ro
resume=/dev/mapper/rhel-swap
rd.lvm.lv=rhel/root
rd.lvm.lv=rhel/swap

15. cpupower frequency-info

analyzing CPU 0:
current policy: frequency should be within 800 MHz and 3.80 GHz.
The governor "performance" may decide which speed to use within this range.

boost state support:
Supported: yes
Active: yes

16. tuned-adm active

Current active profile: hpc-compute

17. sysctl

kernel.numa_balancing	0
kernel.randomize_va_space	2
vm.compaction_proactiveness	20
vm.dirty_background_bytes	0
vm.dirty_background_ratio	3
vm.dirty_bytes	0
vm.dirty_expire_centisecs	3000
vm.dirty_ratio	10
vm.dirty_writeback_centisecs	500

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Platform Notes (Continued)

```

vm.dirtytime_expire_seconds 43200
vm.extfrag_threshold        500
vm.min_unmapped_ratio      1
vm.nr_hugepages             0
vm.nr_hugepages_mempolicy  0
vm.nr_overcommit_hugepages 0
vm.swappiness               10
vm.watermark_boost_factor   15000
vm.watermark_scale_factor   10
vm.zone_reclaim_mode       1

```

```

-----
18. /sys/kernel/mm/transparent_hugepage
defrag          always defer+madvise [madvise] never
enabled        [always] madvise never
hpage_pmd_size 2097152
shmem_enabled  always within_size advise [never] deny force

```

```

-----
19. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs 60000
defrag                1
max_ptes_none         511
max_ptes_shared       256
max_ptes_swap         64
pages_to_scan         4096
scan_sleep_millisecs 10000

```

```

-----
20. OS release
From /etc/*-release /etc/*-version
os-release      Red Hat Enterprise Linux 9.0 (Plow)
redhat-release  Red Hat Enterprise Linux release 9.0 (Plow)
system-release  Red Hat Enterprise Linux release 9.0 (Plow)

```

```

-----
21. Disk information
SPEC is set to: /home/CPU2017
Filesystem      Type      Size      Used Avail Use% Mounted on
/dev/mapper/rhel-home xfs      819G      209G      611G  26% /home

```

```

-----
22. /sys/devices/virtual/dmi/id
Vendor:         IEI
Product:        NF5688M7
Product Family: Not specified
Serial:         000000000

```

```

-----
23. dmidecode
Additional information from dmidecode 3.3 follows.  WARNING: Use caution when you interpret this section.
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
"DMTF SMBIOS" standard.
Memory:
  16x Samsung M321R4GA3BB6-CQKVG 32 GB 2 rank 4800

```

24. BIOS

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Platform Notes (Continued)

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: American Megatrends International, LLC.
BIOS Version: 04.02.00
BIOS Date: 02/23/2023

Compiler Version Notes

C | 519.lbm_r(base, peak) 538.imagick_r(base, peak) 544.nab_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

C++ | 508.namd_r(base, peak) 510.parest_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

C++, C | 511.povray_r(base, peak) 526.blender_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

C++, C, Fortran | 507.cactuBSSN_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

Fortran | 503.bwaves_r(base, peak) 549.fotonik3d_r(base, peak) 554.roms_r(base, peak)

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

Fortran, C | 521.wrf_r(base, peak) 527.cam4_r(base, peak)

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201
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NF5688M7 (Intel Xeon Platinum 8468)

SPECrate®2017_fp_base = 928
SPECrate®2017_fp_peak = 985

CPU2017 License: 3358

Test Sponsor: Inspur Electronic Information Industry Co., Ltd.

Tested by: Inspur Electronic Information Industry Co., Ltd.

Test Date: Aug-2023

Hardware Availability: Sep-2023

Software Availability: Dec-2022

Base Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using both C and C++:

icpx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx

Base Portability Flags

```
503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64
```

Base Optimization Flags

C benchmarks:

```
-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-Wno-implicit-int -mprefer-vector-width=512 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

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Base Optimization Flags (Continued)

C++ benchmarks:

```
-w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -mprefer-vector-width=512 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

Fortran benchmarks:

```
-w -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using both Fortran and C:

```
-w -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-Wno-implicit-int -mprefer-vector-width=512 -nostandard-realloc-lhs
-align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using both C and C++:

```
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -mprefer-vector-width=512
-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using Fortran, C, and C++:

```
-w -m64 -std=c++14 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -mprefer-vector-width=512
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

Peak Compiler Invocation

C benchmarks:

```
icx
```

C++ benchmarks:

```
icpx
```

Fortran benchmarks:

```
ifx
```

Benchmarks using both Fortran and C:

```
ifx icx
```

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Peak Compiler Invocation (Continued)

Benchmarks using both C and C++:

icpx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

519.lbm_r: basepeak = yes

538.imagick_r: basepeak = yes

544.nab_r: basepeak = yes

C++ benchmarks:

508.namd_r: basepeak = yes

510.parest_r: -w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids
-Ofast -ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -mprefer-vector-width=512
-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

Fortran benchmarks:

503.bwaves_r: basepeak = yes

549.fotonik3d_r: basepeak = yes

554.roms_r: -w -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -nostandard-realloc-lhs
-align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

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Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:

```
521.wrf_r: -w -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast  
-ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -Wno-implicit-int  
-mprefer-vector-width=512 -nostandard-realloc-lhs  
-align array32byte -auto -ljemalloc  
-L/usr/local/jemalloc64-5.0.1/lib
```

527.cam4_r: basepeak = yes

Benchmarks using both C and C++:

```
511.povray_r: -w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs  
-fprofile-generate(pass 1)  
-fprofile-use=default.profddata(pass 2) -xCORE-AVX2(pass 1)  
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse  
-funroll-loops -qopt-mem-layout-trans=4 -Wno-implicit-int  
-mprefer-vector-width=512 -ljemalloc  
-L/usr/local/jemalloc64-5.0.1/lib
```

526.blender_r: basepeak = yes

Benchmarks using Fortran, C, and C++:

```
-w -m64 -std=c++14 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast  
-ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -Wno-implicit-int -mprefer-vector-width=512  
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc  
-L/usr/local/jemalloc64-5.0.1/lib
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic2023-official-linux64.html>

<http://www.spec.org/cpu2017/flags/Inspur-Platform-Settings-intel-V3.3.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic2023-official-linux64.xml>

<http://www.spec.org/cpu2017/flags/Inspur-Platform-Settings-intel-V3.3.xml>



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