



# SPEC CPU®2017 Integer Rate Result

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## Hewlett Packard Enterprise

(Test Sponsor: HPE)

### ProLiant ML30 Gen10

(3.40 GHz, Intel Xeon E-2236)

SPECrate®2017\_int\_base = 45.7

SPECrate®2017\_int\_peak = Not Run

CPU2017 License: 3

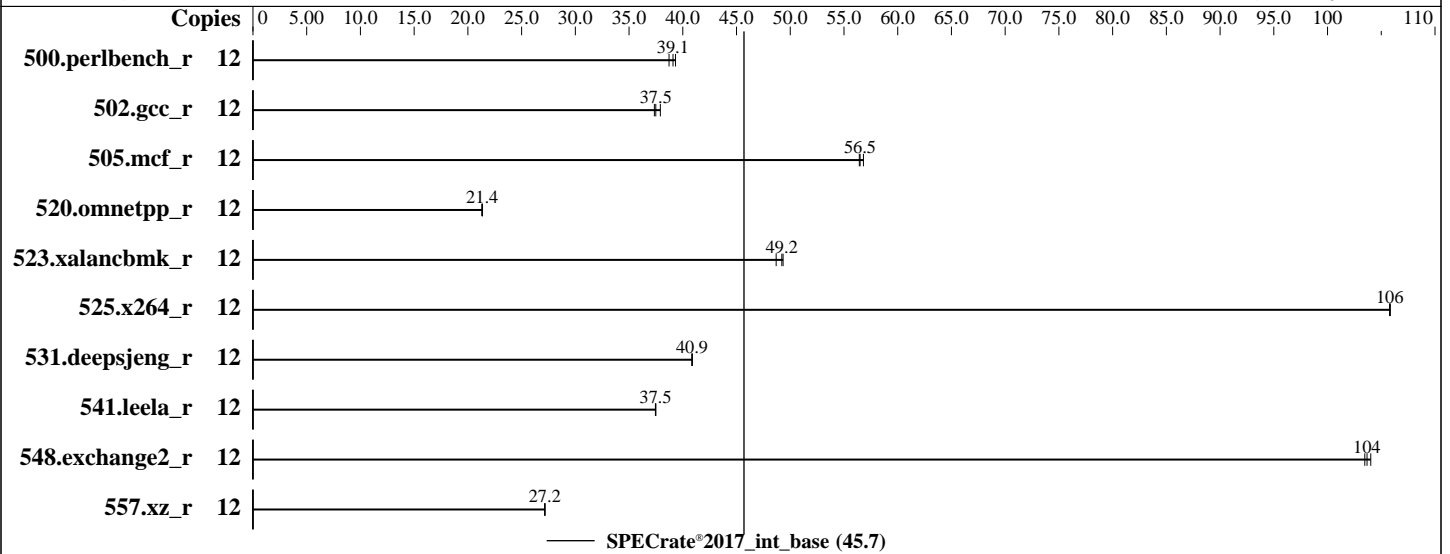
Test Sponsor: HPE

Tested by: HPE

Test Date: Oct-2019

Hardware Availability: Nov-2019

Software Availability: May-2019



### Hardware

CPU Name: Intel Xeon E-2236  
 Max MHz: 4800  
 Nominal: 3400  
 Enabled: 6 cores, 1 chip, 2 threads/core  
 Orderable: 1 chip  
 Cache L1: 32 KB I + 32 KB D on chip per core  
 L2: 256 KB I+D on chip per core  
 L3: 12 MB I+D on chip per chip  
 Other: None  
 Memory: 64 GB (4 x 16 GB 2Rx8 PC4-2666V-U)  
 Storage: 1 x 400 GB SAS SSD, RAID 0  
 Other: None

### Software

OS: SUSE Linux Enterprise Server 15 (x86\_64)  
 Kernel 4.12.14-23-default  
 Compiler: C/C++: Version 19.0.4.227 of Intel C/C++ Compiler Build 20190416 for Linux;  
 Fortran: Version 19.0.4.227 of Intel Fortran Compiler Build 20190416 for Linux;  
 Parallel: No  
 Firmware: HPE BIOS Version U44 09/05/2019 released Nov-2019  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: Not Applicable  
 Other: None  
 Power Management: --



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## Results Table

| Benchmark       | Base   |            |             |            |             |            |             | Peak   |         |       |         |       |         |       |
|-----------------|--------|------------|-------------|------------|-------------|------------|-------------|--------|---------|-------|---------|-------|---------|-------|
|                 | Copies | Seconds    | Ratio       | Seconds    | Ratio       | Seconds    | Ratio       | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 500.perlbench_r | 12     | 486        | 39.3        | 493        | 38.7        | <b>489</b> | <b>39.1</b> |        |         |       |         |       |         |       |
| 502.gcc_r       | 12     | 448        | 37.9        | 455        | 37.4        | <b>453</b> | <b>37.5</b> |        |         |       |         |       |         |       |
| 505.mcf_r       | 12     | <b>343</b> | <b>56.5</b> | 344        | 56.4        | 341        | 56.8        |        |         |       |         |       |         |       |
| 520.omnetpp_r   | 12     | 739        | 21.3        | 737        | 21.4        | <b>737</b> | <b>21.4</b> |        |         |       |         |       |         |       |
| 523.xalancbmk_r | 12     | 260        | 48.7        | <b>257</b> | <b>49.2</b> | 257        | 49.3        |        |         |       |         |       |         |       |
| 525.x264_r      | 12     | <b>199</b> | <b>106</b>  | 199        | 106         | 199        | 106         |        |         |       |         |       |         |       |
| 531.deepsjeng_r | 12     | 336        | 40.9        | <b>337</b> | <b>40.9</b> | 337        | 40.8        |        |         |       |         |       |         |       |
| 541.leela_r     | 12     | 531        | 37.5        | <b>530</b> | <b>37.5</b> | 530        | 37.5        |        |         |       |         |       |         |       |
| 548.exchange2_r | 12     | 302        | 104         | <b>303</b> | <b>104</b>  | 304        | 103         |        |         |       |         |       |         |       |
| 557.xz_r        | 12     | 477        | 27.2        | <b>477</b> | <b>27.2</b> | 477        | 27.1        |        |         |       |         |       |         |       |

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation  
Filesystem page cache synced and cleared with:  
sync; echo 3 > /proc/sys/vm/drop\_caches

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:  
LD\_LIBRARY\_PATH =  
"/home/cpu2017\_u4/lib/intel64:/home/cpu2017\_u4/lib/ia32:/home/cpu2017\_u4/je5.0.1-32"

## General Notes

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM memory using Redhat Enterprise Linux 7.5  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

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**Hardware Availability:** Nov-2019  
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## General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

## Platform Notes

BIOS Configuration:

Thermal Configuration set to Maximum Cooling  
LLC Prefetch set to Enabled  
Workload Profile set to General Throughput Compute

Sysinfo program /home/cpu2017\_u4/bin/sysinfo  
Rev: r6365 of 2019-08-21 295195f888a3d7edble6e46a485a0011  
running on ml30-sles15 Tue Oct 15 02:48:17 2019

SUT (System Under Test) info as seen by some common utilities.  
For more information on this section, see  
<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name : Intel(R) Xeon(R) E-2236 CPU @ 3.40GHz
 1 "physical id"s (chips)
 12 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 6
siblings : 12
physical 0: cores 0 1 2 3 4 5
```

From lscpu:

```
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 12
On-line CPU(s) list: 0-11
Thread(s) per core: 2
Core(s) per socket: 6
Socket(s): 1
NUMA node(s): 1
Vendor ID: GenuineIntel
CPU family: 6
Model: 158
Model name: Intel(R) Xeon(R) E-2236 CPU @ 3.40GHz
Stepping: 10
CPU MHz: 3400.000
```

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## Platform Notes (Continued)

```

BogoMIPS:          6816.00
Virtualization:    VT-x
L1d cache:         32K
L1i cache:         32K
L2 cache:          256K
L3 cache:          12288K
NUMA node0 CPU(s): 0-11
Flags:             fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmpperf tsc_known_freq pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3
sdbg fma cx16 xtpr pdcm pcid sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer
aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb invpcid_single
pti tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep
bmi2 erms invpcid rtm mpx rdseed adx smap clflushopt intel_pt xsaveopt xsavec
xgetbv1 xsaves ibpb ibrs stibp dtherm ida arat pln pts ssbd

```

```

/proc/cpuinfo cache data
cache size : 12288 KB

```

```

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.

```

```

available: 1 nodes (0)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11
node 0 size: 64264 MB
node 0 free: 63760 MB
node distances:
node 0
0: 10

```

```

From /proc/meminfo
MemTotal:          65806968 kB
HugePages_Total:   0
Hugepagesize:      2048 kB

```

```

From /etc/*release* /etc/*version*
os-release:
NAME="SLES"
VERSION="15"
VERSION_ID="15"
PRETTY_NAME="SUSE Linux Enterprise Server 15"
ID="sles"
ID_LIKE="suse"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:15"

```

uname -a:

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## Platform Notes (Continued)

Linux ml30-sles15 4.12.14-23-default #1 SMP Tue May 29 21:04:44 UTC 2018 (cd0437b)  
x86\_64 x86\_64 x86\_64 GNU/Linux

Kernel self-reported vulnerability status:

|   |   |
|---|---|
| CVE-2018-3620 (L1 Terminal Fault):        | No status reported  |
| Microarchitectural Data Sampling:         | No status reported  |
| CVE-2017-5754 (Meltdown):                 | Mitigation: PTI   |
| CVE-2018-3639 (Speculative Store Bypass): | Vulnerable  |
| CVE-2017-5753 (Spectre variant 1):        | Mitigation: __user pointer sanitization                           |
| CVE-2017-5715 (Spectre variant 2):        | Mitigation: Indirect Branch Restricted Speculation, IBPB, IBRS_FW |

run-level 3 Oct 15 02:46

SPEC is set to: /home/cpu2017\_u4

|            |      |      |      |       |      |            |
|------------|------|------|------|-------|------|------------|
| Filesystem | Type | Size | Used | Avail | Use% | Mounted on |
| /dev/sda3  | xfs  | 344G | 74G  | 271G  | 22%  | /home      |

From /sys/devices/virtual/dmi/id

```

BIOS:      HPE U44 09/05/2019
Vendor:    HPE
Product:   ProLiant ML30 Gen10
Product Family: ProLiant
Serial:    CN68130P0X

```

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

4x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2666, configured at 2667

(End of data from sysinfo program)

## Compiler Version Notes

```

=====
C          | 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base)
          | 525.x264_r(base) 557.xz_r(base)
=====

```

```

Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
=====

```

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## Compiler Version Notes (Continued)

=====  
C++ | 520.omnetpp\_r(base) 523.xalancbmk\_r(base) 531.deepsjeng\_r(base)  
| 541.leela\_r(base)  
=====

-----  
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.0.4.227 Build 20190416  
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.  
-----

=====  
Fortran | 548.exchange2\_r(base)  
=====

-----  
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)  
64, Version 19.0.4.227 Build 20190416  
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.  
-----

## Base Compiler Invocation

C benchmarks:

icc -m64 -std=c11

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

## Base Portability Flags

500.perlbench\_r: -DSPEC\_LP64 -DSPEC\_LINUX\_X64  
502.gcc\_r: -DSPEC\_LP64  
505.mcf\_r: -DSPEC\_LP64  
520.omnetpp\_r: -DSPEC\_LP64  
523.xalancbmk\_r: -DSPEC\_LP64 -DSPEC\_LINUX  
525.x264\_r: -DSPEC\_LP64  
531.deepsjeng\_r: -DSPEC\_LP64  
541.leela\_r: -DSPEC\_LP64  
548.exchange2\_r: -DSPEC\_LP64  
557.xz\_r: -DSPEC\_LP64



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## Base Optimization Flags

C benchmarks:

```
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
```

```
-qopt-mem-layout-trans=4
```

```
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
```

```
-lqkmallo
```

C++ benchmarks:

```
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
```

```
-qopt-mem-layout-trans=4
```

```
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
```

```
-lqkmallo
```

Fortran benchmarks:

```
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
```

```
-qopt-mem-layout-trans=4 -nostandard-realloc-lhs -align array32byte
```

```
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
```

```
-lqkmallo
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.2-CLX-revB.html>

<http://www.spec.org/cpu2017/flags/Intel-ic19.0u1-official-linux64.2019-07-09.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.2-CLX-revB.xml>

<http://www.spec.org/cpu2017/flags/Intel-ic19.0u1-official-linux64.2019-07-09.xml>

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For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

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