



# SPEC® CFP2006 Result

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## Lenovo Global Technology

ThinkSystem SR950  
(2.40 GHz, Intel Xeon Gold 6148)

SPECfp®\_rate2006 = 2990

SPECfp\_rate\_base2006 = 2920

CPU2006 license: 9017

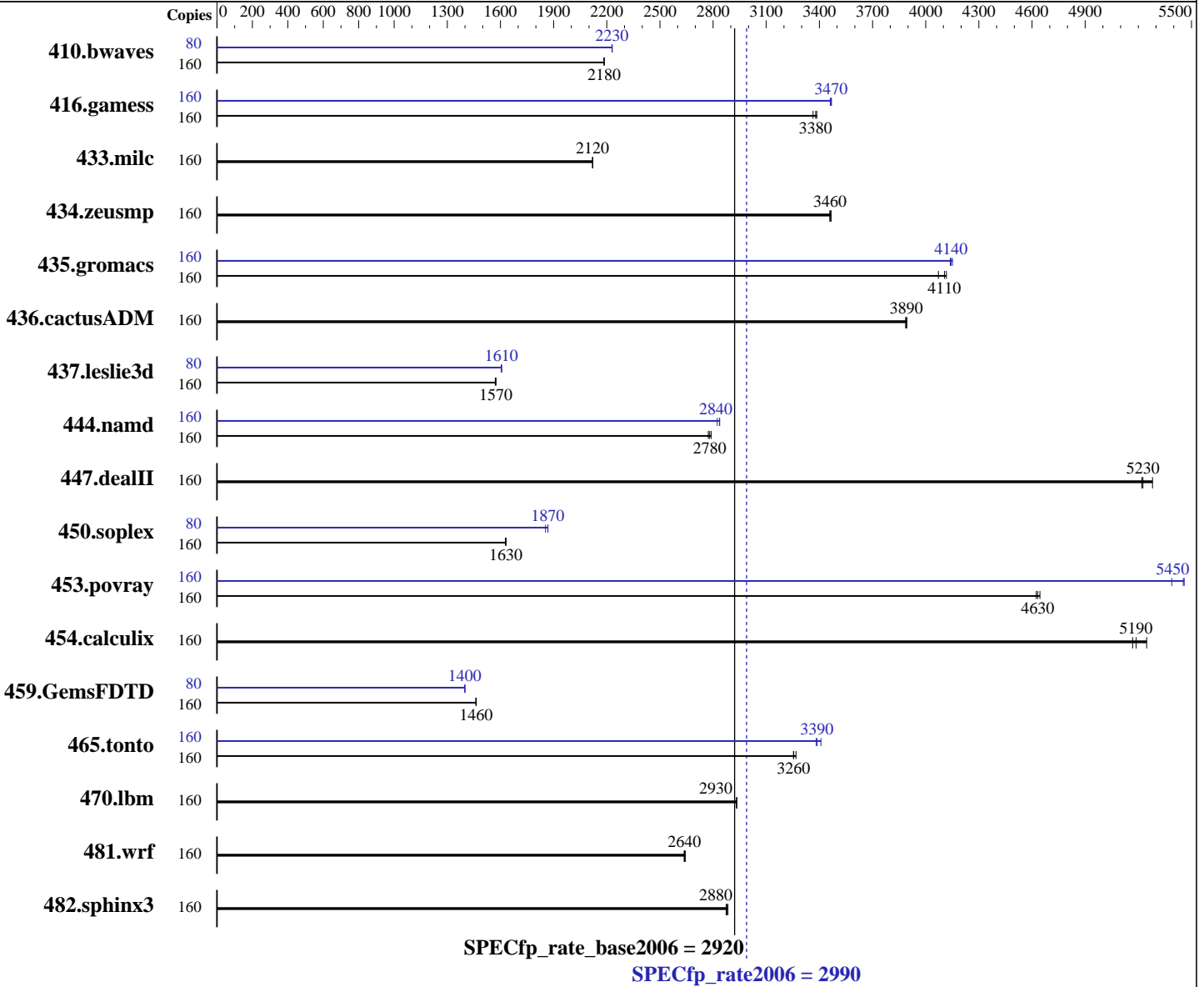
Test sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test date: Nov-2017

Hardware Availability: Sep-2017

Software Availability: Apr-2017



### Hardware

CPU Name: Intel Xeon Gold 6148  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.70 GHz  
 CPU MHz: 2400  
 FPU: Integrated  
 CPU(s) enabled: 80 cores, 4 chips, 20 cores/chip, 2 threads/core  
 CPU(s) orderable: 2,4 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 1 MB I+D on chip per core

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### Software

Operating System: SUSE Linux Enterprise Server 12 SP2 (x86\_64)  
 Kernel 4.4.21-69-default  
 Compiler: C/C++: Version 17.0.3.191 of Intel C/C++ Compiler for Linux;  
 Fortran: Version 17.0.3.191 of Intel Fortran Compiler for Linux  
 Auto Parallel: Yes  
 File System: tmpfs  
 System State: Run level 3 (multi-user)

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L3 Cache: 27.5 MB I+D on chip per chip  
Other Cache: None  
Memory: 1536 GB (48 x 32 GB 2Rx4 PC4-2666V-R)  
Disk Subsystem: 800 GB tmpfs  
Other Hardware: None

Base Pointers: 32/64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	160	<u>995</u>	<u>2180</u>	995	2180	995	2190	80	<u>488</u>	<u>2230</u>	487	2230	488	2230
416.gamess	160	<u>927</u>	<u>3380</u>	931	3360	925	3390	160	<u>904</u>	<u>3470</u>	903	3470	905	3460
433.milc	160	<u>693</u>	<u>2120</u>	693	2120	693	2120	160	<u>693</u>	<u>2120</u>	693	2120	693	2120
434.zeusmp	160	420	3470	<u>421</u>	<u>3460</u>	421	3460	160	420	3470	<u>421</u>	<u>3460</u>	421	3460
435.gromacs	160	281	4070	<u>278</u>	<u>4110</u>	277	4120	160	275	4150	<u>276</u>	<u>4140</u>	276	4140
436.cactusADM	160	492	3890	491	3890	<u>491</u>	<u>3890</u>	160	492	3890	491	3890	<u>491</u>	<u>3890</u>
437.leslie3d	160	958	1570	955	1570	<u>956</u>	<u>1570</u>	80	<u>468</u>	<u>1610</u>	468	1610	469	1600
444.namd	160	463	2770	<u>461</u>	<u>2780</u>	460	2790	160	<u>453</u>	<u>2840</u>	452	2840	454	2820
447.dealII	160	351	5220	<u>350</u>	<u>5230</u>	347	5280	160	351	5220	<u>350</u>	<u>5230</u>	347	5280
450.soplex	160	819	1630	<u>819</u>	<u>1630</u>	818	1630	80	<u>357</u>	<u>1870</u>	360	1850	357	1870
453.povray	160	183	4640	<u>184</u>	<u>4630</u>	184	4620	160	<u>156</u>	<u>5450</u>	156	5460	158	5390
454.calculix	160	255	5170	<u>254</u>	<u>5190</u>	252	5250	160	255	5170	<u>254</u>	<u>5190</u>	252	5250
459.GemsFDTD	160	1160	1460	1162	1460	<u>1160</u>	<u>1460</u>	80	607	1400	<u>607</u>	<u>1400</u>	607	1400
465.tonto	160	484	3250	482	3270	<u>484</u>	<u>3260</u>	160	465	3380	462	3410	<u>465</u>	<u>3390</u>
470.lbm	160	<u>749</u>	<u>2930</u>	749	2930	750	2930	160	<u>749</u>	<u>2930</u>	749	2930	750	2930
481.wrf	160	676	2640	678	2640	<u>677</u>	<u>2640</u>	160	676	2640	678	2640	<u>677</u>	<u>2640</u>
482.sphinx3	160	<u>1084</u>	<u>2880</u>	1085	2870	1081	2880	160	<u>1084</u>	<u>2880</u>	1085	2870	1081	2880

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Tmpfs filesystem can be set with:

```
mount -t tmpfs -o size=800g tmpfs /home
```

Process tuning setting:

```
echo 50000 > /proc/sys/kernel/sched_cfs_bandwidth_slice_us
echo 240000000 > /proc/sys/kernel/sched_latency_ns
echo 5000000 > /proc/sys/kernel/sched_migration_cost_ns
echo 100000000 > /proc/sys/kernel/sched_min_granularity_ns
```

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## Operating System Notes (Continued)

```
echo 150000000 > /proc/sys/kernel/sched_wakeup_granularity_ns
```

## Platform Notes

BIOS configuration:

Choose Operating Mode set to Maximum Performance

SNC set to Enable

DCU Streamer Prefetcher set to Disable

Stale AtoS set to Enable

LLC dead line alloc set to Disable

Sysinfo program /home/cpu2006-1.2-ic17.0u3/config/sysinfo.rev6993

Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)

running on linux-uyxw Sat Nov 4 04:14:04 2017

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) Gold 6148 CPU @ 2.40GHz

4 "physical id"s (chips)

160 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

cpu cores : 20

siblings : 40

physical 0: cores 0 1 2 3 4 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28

physical 1: cores 0 1 2 3 4 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28

physical 2: cores 0 1 2 3 4 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28

physical 3: cores 0 1 2 3 4 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28

cache size : 28160 KB

From /proc/meminfo

MemTotal: 1584766288 kB

HugePages\_Total: 0

Hugepagesize: 2048 kB

From /etc/\*release\* /etc/\*version\*

SuSE-release:

SUSE Linux Enterprise Server 12 (x86\_64)

VERSION = 12

PATCHLEVEL = 2

# This file is deprecated and will be removed in a future service pack or release.

# Please check /etc/os-release for details about this release.

os-release:

NAME="SLES"

VERSION="12-SP2"

VERSION\_ID="12.2"

PRETTY\_NAME="SUSE Linux Enterprise Server 12 SP2"

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## Platform Notes (Continued)

```
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="/o:suse:sles:12:sp2"
```

```
uname -a:
Linux linux-uyxw 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016
(9464f67) x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Nov 3 17:47
```

```
SPEC is set to: /home/cpu2006-1.2-ic17.0u3
Filesystem      Type  Size  Used Avail Use% Mounted on
tmpfs           tmpfs 800G  5.0G 796G  1% /home
```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

```
BIOS Lenovo -[PSE103K-1.00]- 06/19/2017
Memory:
48x NO DIMM NO DIMM
48x Samsung M393A4K40BB2-CTD 32 GB 2 rank 2666 MHz
```

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

```
LD_LIBRARY_PATH = "/home/cpu2006-1.2-ic17.0u3/lib/ia32:/home/cpu2006-1.2-ic17.0u3/lib/intel64:/home/cpu2006-1.2-ic17.0u3/sh10.2"
```

```
Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.2
Transparent Huge Pages enabled by default
Filesystem page cache cleared with:
shell invocation of 'sync; echo 3 > /proc/sys/vm/drop_caches' prior to run
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
```

## Base Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks:  
icpc -m64

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## Base Compiler Invocation (Continued)

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.leslie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.dealII: -DSPEC\_CPU\_LP64  
 450.soplex: -DSPEC\_CPU\_LP64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
 482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32  
-qopt-mem-layout-trans=3

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32  
-qopt-mem-layout-trans=3

Fortran benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch

Benchmarks using both Fortran and C:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32  
-qopt-mem-layout-trans=3



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## Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32 -L/opt/intel/compilers\_and\_libraries\_2017/linux/lib/ia32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.leslie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.deallI: -DSPEC\_CPU\_LP64  
 450.soplex: -D\_FILE\_OFFSET\_BITS=64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
 482.sphinx3: -DSPEC\_CPU\_LP64

## Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

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## Peak Optimization Flags (Continued)

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -fno-alias -auto-ilp32  
-qopt-mem-layout-trans=3

447.dealII: basepeak = yes

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -qopt-malloc-options=3  
-qopt-mem-layout-trans=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -unroll4 -qopt-mem-layout-trans=3

### Fortran benchmarks:

410.bwaves: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: Same as 410.bwaves

459.GemsFDTD: Same as 410.bwaves

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -unroll4 -auto -inline-calloc  
-qopt-malloc-options=3

### Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -qopt-prefetch -auto-ilp32  
-qopt-mem-layout-trans=3

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes



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The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-SKL-E.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-SKL-E.xml>

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