



# SPEC<sup>®</sup> CFP2006 Result

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## Huawei

### SPECfp<sup>®</sup>\_rate2006 = 1370

### Huawei CH121 V5 (Intel Xeon Gold 6142)

### SPECfp\_rate\_base2006 = 1340

CPU2006 license: 3175

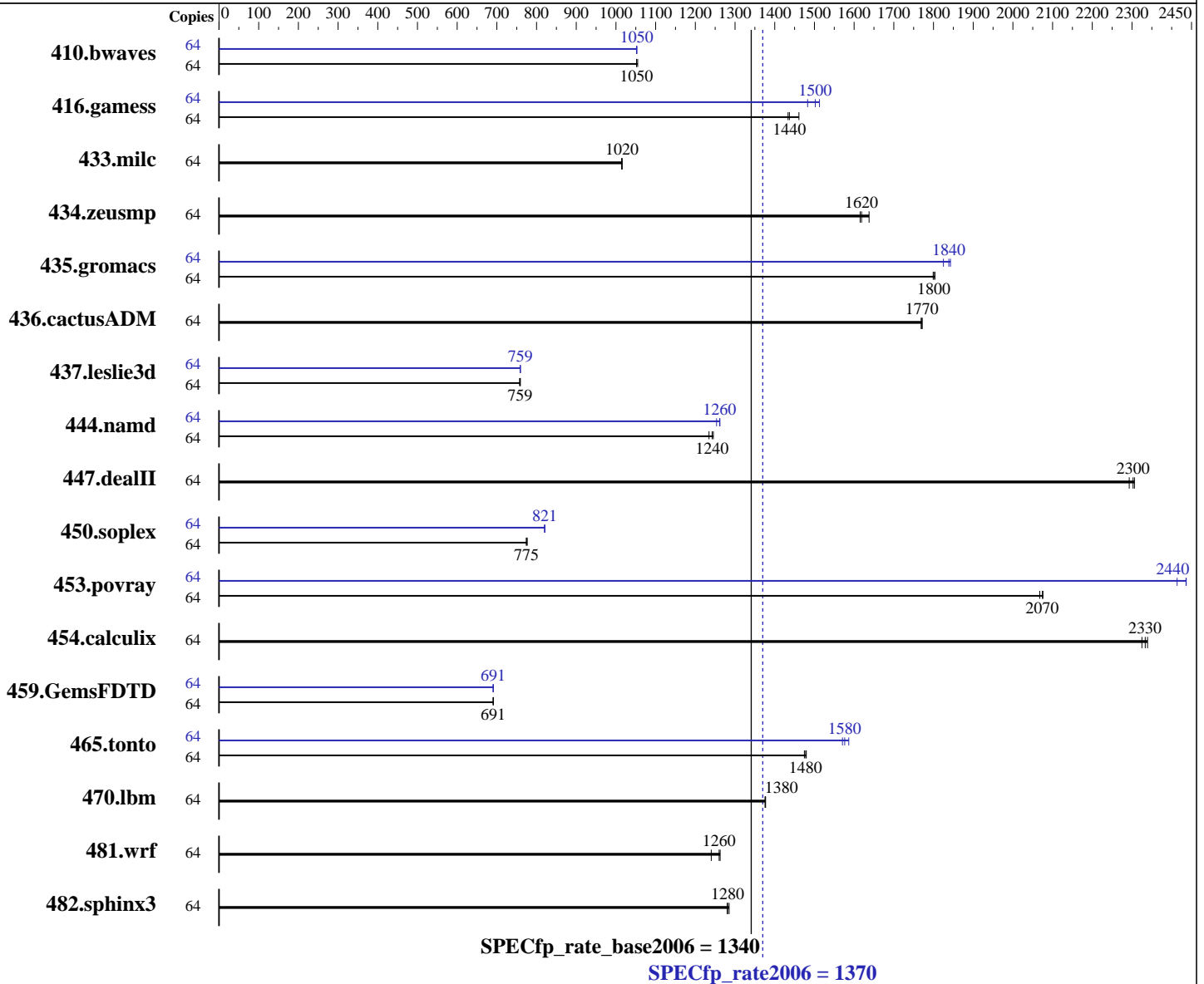
Test date: Jun-2017

Test sponsor: Huawei

Hardware Availability: Aug-2017

Tested by: Huawei

Software Availability: Nov-2016



#### Hardware

CPU Name: Intel Xeon Gold 6142  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.70 GHz  
 CPU MHz: 2600  
 FPU: Integrated  
 CPU(s) enabled: 32 cores, 2 chips, 16 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 1 MB I+D on chip per core

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#### Software

Operating System: Red Hat Enterprise Linux Server release 7.3 (Maipo)  
 3.10.0-514.el7.x86\_64  
 Compiler: C/C++: Version 17.0.0.098 of Intel C/C++ Compiler for Linux;  
 Fortran: Version 17.0.0.098 of Intel Fortran Compiler for Linux  
 Auto Parallel: No  
 File System: xfs

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L3 Cache: 22 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 768 GB (24 x 32 GB 2Rx4 PC4-2666V-R)  
 Disk Subsystem: 1 x 1200 GB SAS, 10000 RPM  
 Other Hardware: None

System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	64	825	1050	<u>827</u>	<u>1050</u>	827	1050	64	826	1050	<u>827</u>	<u>1050</u>	827	1050
416.gamess	64	874	1430	<u>872</u>	<u>1440</u>	858	1460	64	845	1480	828	1510	<u>834</u>	<u>1500</u>
433.milc	64	<u>579</u>	<u>1020</u>	579	1020	579	1010	64	<u>579</u>	<u>1020</u>	579	1020	579	1010
434.zeusmp	64	356	1640	361	1620	<u>360</u>	<u>1620</u>	64	356	1640	361	1620	<u>360</u>	<u>1620</u>
435.gromacs	64	253	1800	<u>254</u>	<u>1800</u>	254	1800	64	248	1840	250	1820	<u>249</u>	<u>1840</u>
436.cactusADM	64	432	1770	<u>432</u>	<u>1770</u>	432	1770	64	432	1770	<u>432</u>	<u>1770</u>	432	1770
437.leslie3d	64	795	757	793	759	<u>793</u>	<u>759</u>	64	793	759	<u>793</u>	<u>759</u>	792	759
444.namd	64	412	1250	416	1230	<u>413</u>	<u>1240</u>	64	407	1260	<u>407</u>	<u>1260</u>	409	1250
447.dealII	64	<u>318</u>	<u>2300</u>	317	2310	319	2290	64	<u>318</u>	<u>2300</u>	317	2310	319	2290
450.soplex	64	689	774	<u>689</u>	<u>775</u>	687	777	64	651	820	650	821	<u>650</u>	<u>821</u>
453.povray	64	164	2080	165	2070	<u>164</u>	<u>2070</u>	64	141	2410	<u>140</u>	<u>2440</u>	140	2440
454.calculix	64	226	2340	227	2320	<u>226</u>	<u>2330</u>	64	226	2340	227	2320	<u>226</u>	<u>2330</u>
459.GemsFDTD	64	983	691	<u>983</u>	<u>691</u>	983	691	64	<u>983</u>	<u>691</u>	983	691	983	691
465.tonto	64	427	1470	426	1480	<u>426</u>	<u>1480</u>	64	<u>400</u>	<u>1580</u>	401	1570	397	1590
470.lbm	64	<u>639</u>	<u>1380</u>	639	1380	639	1380	64	<u>639</u>	<u>1380</u>	639	1380	639	1380
481.wrf	64	<u>567</u>	<u>1260</u>	576	1240	566	1260	64	<u>567</u>	<u>1260</u>	576	1240	566	1260
482.sphinx3	64	<u>973</u>	<u>1280</u>	974	1280	971	1290	64	<u>973</u>	<u>1280</u>	974	1280	971	1290

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:  
Set Power Efficiency Mode to Performance  
Set SNC to Enable

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Test date: Jun-2017  
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### Platform Notes (Continued)

Set IMC Interleaving to 1 way  
Set Patrol Scrub to Disable  
Sysinfo program /spec17/config/sysinfo.rev6993  
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)  
running on localhost.localdomain Mon Jun 26 04:29:00 2017

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name      : Intel(R) Xeon(R) Gold 6142 CPU @ 2.60GHz
 2 "physical id"s (chips)
 64 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores    : 16
  siblings    : 32
  physical 0   : cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
  physical 1   : cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
cache size     : 22528 KB
```

```
From /proc/meminfo
MemTotal:      790481628 kB
HugePages_Total: 0
Hugepagesize:  2048 kB
```

```
From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.3 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.3"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.3 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.3:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.3:ga:server
```

```
uname -a:
Linux localhost.localdomain 3.10.0-514.el7.x86_64 #1 SMP Wed Oct 19 11:24:13 EDT 2016 x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Jun 24 05:22

```
SPEC is set to: /spec17
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2       xfs   898G  17G  882G   2% /
```

Additional information from dmidecode:  
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## Platform Notes (Continued)

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS INSYDE Corp. 0.13 04/11/2017

Memory:

24x Samsung M393A4K40BB2-CTD 32 GB 2 rank 2666 MHz

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/spec17/libs/32:/spec17/libs/64:/spec17/sh10.2"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.2

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent\_hugepage/enabled

Filesystem page cache cleared with:

echo 1> /proc/sys/vm/drop\_caches

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64

416.gamess: -DSPEC\_CPU\_LP64

433.milc: -DSPEC\_CPU\_LP64

434.zeusmp: -DSPEC\_CPU\_LP64

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## Base Portability Flags (Continued)

```

435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

## Base Optimization Flags

C benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3

```

C++ benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3

```

Fortran benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch

```

Benchmarks using both Fortran and C:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3

```

## Peak Compiler Invocation

C benchmarks:

```

icc -m64

```

C++ benchmarks (except as noted below):

```

icpc -m64

```

```

450.soplex: icpc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

```

Fortran benchmarks:

```

ifort -m64

```

Benchmarks using both Fortran and C:

```

icc -m64 ifort -m64

```



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Software Availability: Nov-2016

## Peak Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -D_FILE_OFFSET_BITS=64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

## Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

```

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
         -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
         -no-prec-div(pass 2) -fno-alias -auto-ilp32
         -qopt-mem-layout-trans=3

```

447.dealII: basepeak = yes

```

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
           -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -qopt-malloc-options=3
           -qopt-mem-layout-trans=3

```

```

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
           -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -unroll4 -qopt-mem-layout-trans=3

```

Fortran benchmarks:

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## Peak Optimization Flags (Continued)

410.bwaves: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: Same as 410.bwaves

459.GemsFDTD: Same as 410.bwaves

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -unroll4 -auto -inline-calloc  
-qopt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -qopt-prefetch -auto-ilp32  
-qopt-mem-layout-trans=3

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-SKL-V1.6.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-SKL-V1.6.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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