



SPEC® CFP2006 Result

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Dell Inc.

PowerEdge R940 (Intel Xeon Platinum 8180, 2.50 GHz)

SPECfp_rate2006 = 3490

SPECfp_rate_base2006 = 3380

CPU2006 license: 55

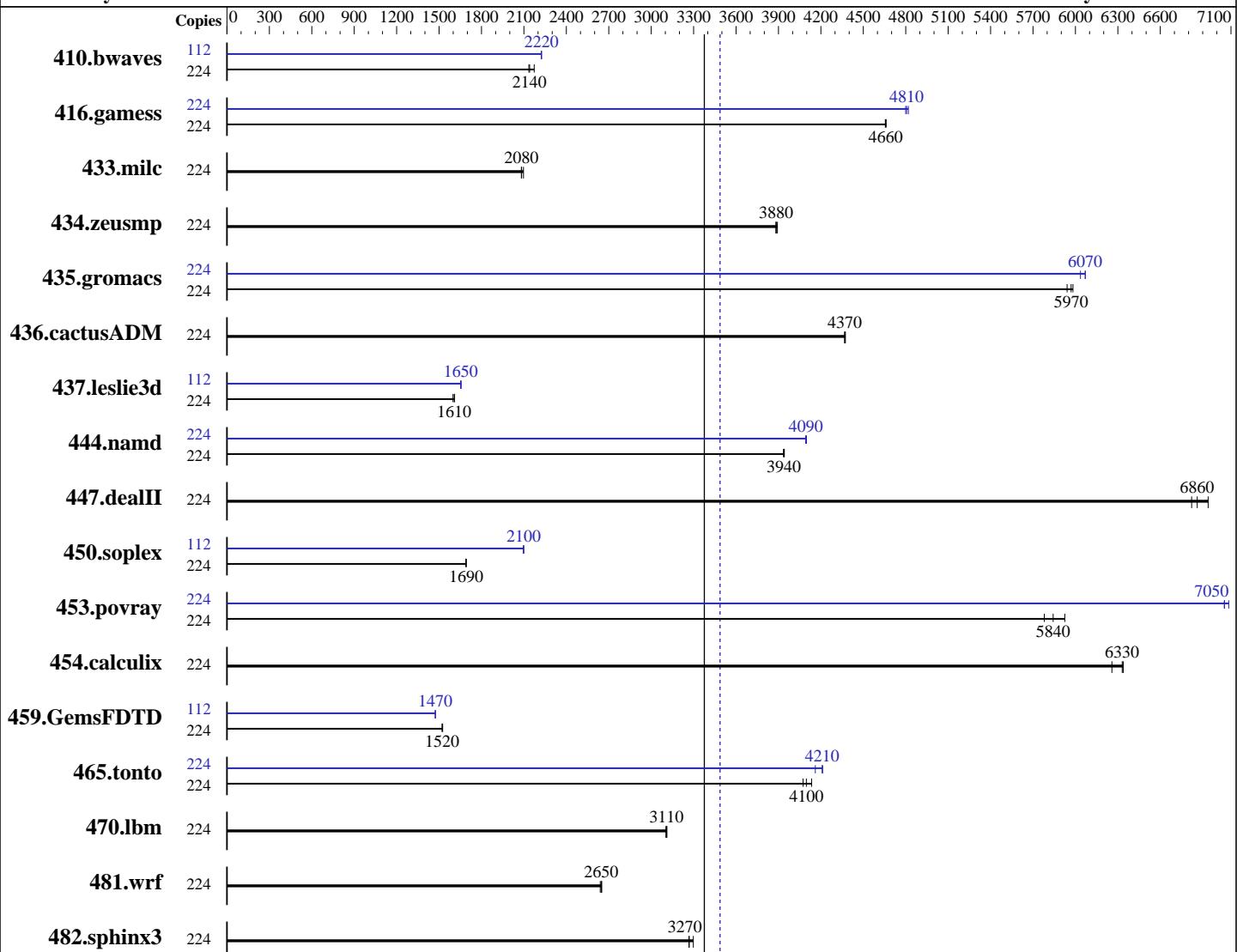
Test sponsor: Dell Inc.

Tested by: Dell Inc.

Test date: Jun-2017

Hardware Availability: Jul-2017

Software Availability: Nov-2016



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Hardware

CPU Name: Intel Xeon Platinum 8180
 CPU Characteristics: Intel Turbo Boost Technology up to 3.80 GHz
 CPU MHz: 2500
 FPU: Integrated
 CPU(s) enabled: 112 cores, 4 chips, 28 cores/chip, 2 threads/core
 CPU(s) orderable: 2,4 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 1 MB I+D on chip per core

Software

Operating System: SUSE Linux Enterprise Server 12 SP2 4.4.21-69-default
 Compiler: C/C++: Version 17.0.3.191 of Intel C/C++ Compiler for Linux;
 Fortran: Version 17.0.3.191 of Intel Fortran Compiler for Linux
 Auto Parallel: Yes
 File System: xfs
 System State: Run level 3 (multi-user)

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L3 Cache:	38.5 MB I+D on chip per chip
Other Cache:	None
Memory:	768 GB (48 x 16 GB 2Rx8 PC4-2666V-R)
Disk Subsystem:	1 x 960 GB SATA SSD
Other Hardware:	None

Base Pointers:	32/64-bit
Peak Pointers:	32/64-bit
Other Software:	None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	224	1401	2170	1426	2130	<u>1422</u>	<u>2140</u>	112	<u>685</u>	<u>2220</u>	684	2220	<u>685</u>	<u>2220</u>
416.gamess	224	942	4660	941	4660	<u>941</u>	<u>4660</u>	224	910	4820	<u>913</u>	<u>4810</u>	914	4800
433.milc	224	981	2100	<u>987</u>	<u>2080</u>	988	2080	224	981	2100	<u>987</u>	<u>2080</u>	988	2080
434.zeusmp	224	525	3880	524	3890	<u>525</u>	<u>3880</u>	224	525	3880	524	3890	<u>525</u>	<u>3880</u>
435.gromacs	224	<u>268</u>	<u>5970</u>	269	5940	267	5980	224	263	6070	<u>264</u>	<u>6070</u>	265	6040
436.cactusADM	224	612	4370	<u>613</u>	<u>4370</u>	613	4370	224	612	4370	<u>613</u>	<u>4370</u>	613	4370
437.leslie3d	224	1317	1600	<u>1310</u>	<u>1610</u>	1308	1610	112	636	1660	637	1650	<u>637</u>	<u>1650</u>
444.namd	224	456	3940	<u>456</u>	<u>3940</u>	456	3940	224	<u>439</u>	<u>4090</u>	438	4100	439	4090
447.dealII	224	369	6940	<u>373</u>	<u>6860</u>	376	6820	224	369	6940	<u>373</u>	<u>6860</u>	376	6820
450.soplex	224	1105	1690	1104	1690	<u>1104</u>	<u>1690</u>	112	446	2100	<u>445</u>	<u>2100</u>	445	2100
453.povray	224	201	5920	206	5780	<u>204</u>	<u>5840</u>	224	169	7050	<u>169</u>	<u>7050</u>	168	7080
454.calculix	224	292	6340	295	6260	<u>292</u>	<u>6330</u>	224	292	6340	295	6260	<u>292</u>	<u>6330</u>
459.GemsFDTD	224	1562	1520	1558	1530	<u>1561</u>	<u>1520</u>	112	<u>806</u>	<u>1470</u>	806	1470	807	1470
465.tonto	224	<u>538</u>	<u>4100</u>	541	4070	533	4130	224	523	4210	530	4160	<u>524</u>	<u>4210</u>
470.lbm	224	992	3100	989	3110	<u>990</u>	<u>3110</u>	224	992	3100	989	3110	<u>990</u>	<u>3110</u>
481.wrf	224	948	2640	<u>945</u>	<u>2650</u>	944	2650	224	948	2640	<u>945</u>	<u>2650</u>	944	2650
482.sphinx3	224	1336	3270	<u>1336</u>	<u>3270</u>	1324	3300	224	<u>1336</u>	<u>3270</u>	<u>1336</u>	<u>3270</u>	1324	3300

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS settings:
Sub NUMA Cluster enabled
Virtualization Technology disabled

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Platform Notes (Continued)

```
System Profile set to Custom
CPU Performance set to Maximum Performance
C States set to autonomous
C1E disabled
Uncore Frequency set to Dynamic
Energy Efficiency Policy set to Performance
Memory Patrol Scrub disabled
Logical Processor enabled
CPU Interconnect Bus Link Power Management disabled
PCI ASPM L1 Link Power Management disabled
Sysinfo program /home/_cpu2006/config/sysinfo.rev6993
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)
running on linux-r2ls Wed Jun 21 21:34:31 2017
```

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) Platinum 8180 CPU @ 2.50GHz
        4 "physical id"s (chips)
        224 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
    cpu cores : 28
    siblings : 56
    physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24
    25 26 27 28 29 30
    physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24
    25 26 27 28 29 30
    physical 2: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24
    25 26 27 28 29 30
    physical 3: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24
    25 26 27 28 29 30
cache size : 39424 KB
```

```
From /proc/meminfo
MemTotal:      791224272 kB
HugePages_Total:      0
Hugepagesize:     2048 kB
```

```
/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12 SP2
```

```
From /etc/*release* /etc/*version*
SuSE-release:
        SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 2
# This file is deprecated and will be removed in a future service pack or
release.
```

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Platform Notes (Continued)

```
# Please check /etc/os-release for details about this release.  
os-release:  
NAME="SLES"  
VERSION="12-SP2"  
VERSION_ID="12.2"  
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"  
ID="sles"  
ANSI_COLOR="0;32"  
CPE_NAME="cpe:/o:suse:sles:12:sp2"  
  
uname -a:  
Linux linux-r2ls 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016  
(9464f67) x86_64 x86_64 x86_64 GNU/Linux  
  
run-level 3 Jun 21 10:31
```

```
SPEC is set to: /home/_cpu2006  
Filesystem      Type  Size  Used  Avail Use% Mounted on  
/dev/sda4        xfs   796G   11G  785G   2% /home  
Additional information from dmidecode:
```

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

```
BIOS Dell Inc. 1.0.3 05/30/2017  
Memory:  
13x 00AD00B300AD HMA82GR7AFR8N-VK 16 GB 2 rank 2666 MHz  
35x 00CE063200CE M393A2K43BB1-CTD 16 GB 2 rank 2666 MHz
```

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

LD_LIBRARY_PATH = "/home/_cpu2006/lib/ia32:/home/_cpu2006/lib/intel64:/home/_cpu2006/sh10.2"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.2
Transparent Huge Pages enabled by default
Filesystem page cache cleared with:
shell invocation of 'sync; echo 3 > /proc/sys/vm/drop_caches' prior to run runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>



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Base Compiler Invocation

C benchmarks:
 `icc -m64`

C++ benchmarks:
 `icpc -m64`

Fortran benchmarks:
 `ifort -m64`

Benchmarks using both Fortran and C:
 `icc -m64 ifort -m64`

Base Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`
416.gamess: `-DSPEC_CPU_LP64`
 `433.milc: -DSPEC_CPU_LP64`
 `434.zeusmp: -DSPEC_CPU_LP64`
 `435.gromacs: -DSPEC_CPU_LP64 -nofor_main`
436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`
 `437.leslie3d: -DSPEC_CPU_LP64`
 `444.namd: -DSPEC_CPU_LP64`
 `447.dealII: -DSPEC_CPU_LP64`
 `450.soplex: -DSPEC_CPU_LP64`
 `453.povray: -DSPEC_CPU_LP64`
 `454.calculix: -DSPEC_CPU_LP64 -nofor_main`
459.GemsFDTD: `-DSPEC_CPU_LP64`
 `465.tonto: -DSPEC_CPU_LP64`
 `470.lbm: -DSPEC_CPU_LP64`
 `481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX`
482.sphinx3: `-DSPEC_CPU_LP64`

Base Optimization Flags

C benchmarks:
 `-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32`
 `-qopt-mem-layout-trans=3`

C++ benchmarks:
 `-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32`
 `-qopt-mem-layout-trans=3`

Fortran benchmarks:
 `-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch`

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Base Optimization Flags (Continued)

Benchmarks using both Fortran and C:

```
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32  
-qopt-mem-layout-trans=3
```

Peak Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks (except as noted below):

```
icpc -m64
```

```
450.soplex: icpc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

Peak Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -D_FILE_OFFSET_BITS=64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```

Peak Optimization Flags

C benchmarks:

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Peak Optimization Flags (Continued)

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -fno-alias -auto-ilp32
-qopt-mem-layout-trans=3

447.dealII: basepeak = yes

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -qopt-malloc-options=3
-qopt-mem-layout-trans=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -unroll4 -qopt-mem-layout-trans=3

Fortran benchmarks:

410.bwaves: -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: Same as 410.bwaves

459.GemsFDTD: Same as 410.bwaves

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -unroll4 -auto -inline-calloc
-qopt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2)
-par-num-threads=1(pass 1) -qopt-prefetch -auto-ilp32
-qopt-mem-layout-trans=3

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Peak Optimization Flags (Continued)

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html>
<http://www.spec.org/cpu2006/flags/Dell-Platform-Flags-PowerEdge14G-revB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml>
<http://www.spec.org/cpu2006/flags/Dell-Platform-Flags-PowerEdge14G-revB.xml>

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