



SPEC® CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant ML350 Gen9
(2.20 GHz, Intel Xeon E5-2698 v4)

SPECfp®2006 = 125

SPECfp_base2006 = 118

CPU2006 license: 3

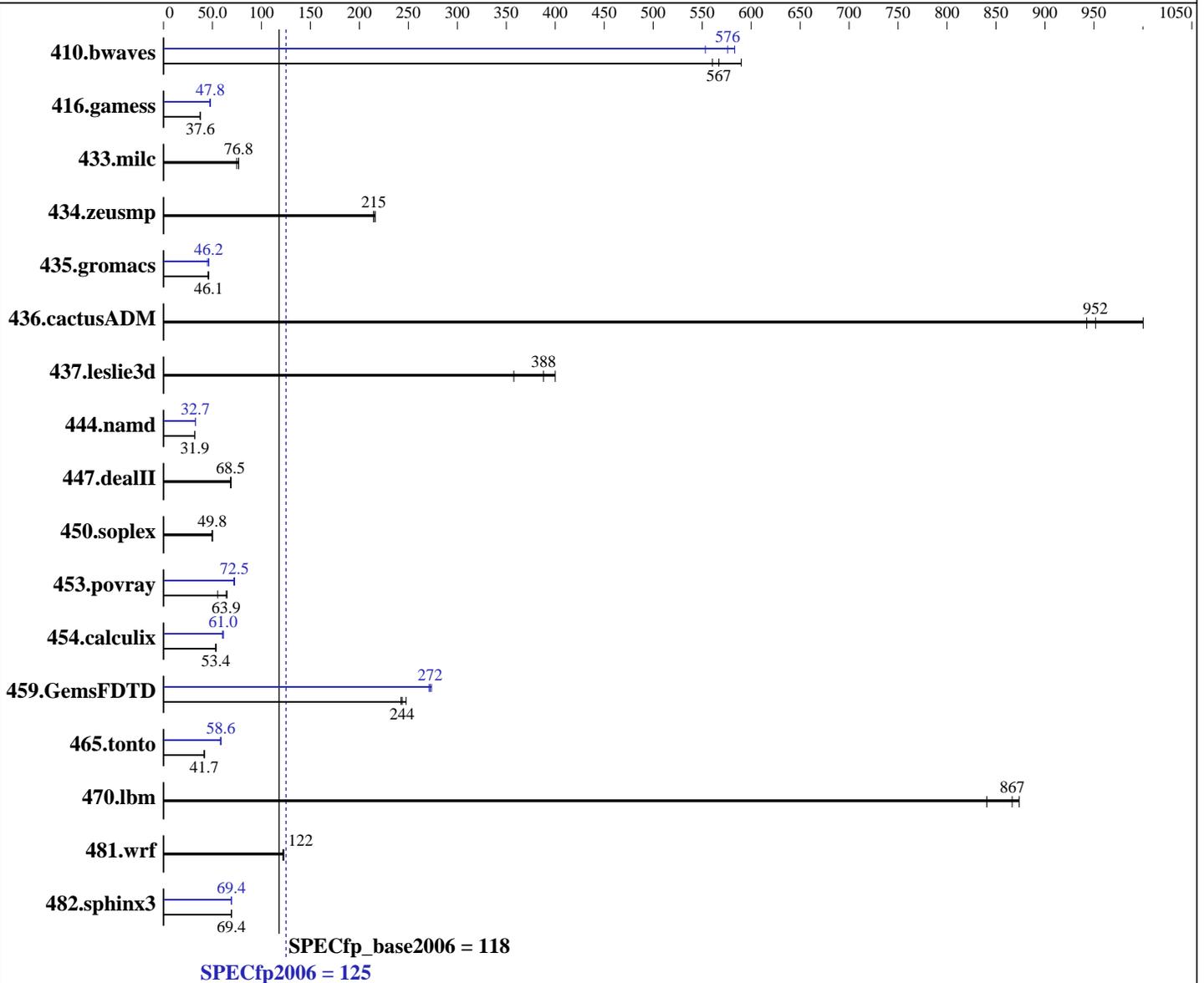
Test sponsor: HPE

Tested by: HPE

Test date: May-2016

Hardware Availability: Mar-2016

Software Availability: Dec-2015



Hardware

CPU Name: Intel Xeon E5-2698 v4
 CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz
 CPU MHz: 2200
 FPU: Integrated
 CPU(s) enabled: 40 cores, 2 chips, 20 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: SUSE Linux Enterprise Server 12 (x86_64) SP1
 Kernel 3.12.49-11-default
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;
 Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
 Auto Parallel: Yes
 File System: xfs
 System State: Run level 3 (multi-user)

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant ML350 Gen9
(2.20 GHz, Intel Xeon E5-2698 v4)

SPECfp2006 = **125**

SPECfp_base2006 = **118**

CPU2006 license: 3

Test sponsor: HPE

Tested by: HPE

Test date: May-2016

Hardware Availability: Mar-2016

Software Availability: Dec-2015

L3 Cache: 50 MB I+D on chip per chip
Other Cache: None
Memory: 512 GB (16 x 32 GB 2Rx4 PC4-2400T-R)
Disk Subsystem: 1 x 800 GB SAS SSD, RAID 0
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio										
410.bwaves	24.0	567	24.2	561	23.0	590	24.6	553	23.3	583	23.6	576
416.gamess	521	37.6	521	37.6	520	37.6	410	47.8	410	47.8	414	47.4
433.milc	123	74.8	120	76.8	120	76.8	123	74.8	120	76.8	120	76.8
434.zeusmp	42.4	215	42.4	215	42.1	216	42.4	215	42.4	215	42.1	216
435.gromacs	157	45.4	155	46.1	155	46.2	154	46.3	155	46.2	158	45.0
436.cactusADM	12.7	943	11.9	1000	12.6	952	12.7	943	11.9	1000	12.6	952
437.leslie3d	26.3	358	23.5	400	24.2	388	26.3	358	23.5	400	24.2	388
444.namd	251	31.9	251	31.9	251	32.0	245	32.7	245	32.7	245	32.7
447.dealII	166	69.0	167	68.5	167	68.5	166	69.0	167	68.5	167	68.5
450.soplex	167	49.8	169	49.5	166	50.1	167	49.8	169	49.5	166	50.1
453.povray	96.2	55.3	83.2	63.9	81.9	65.0	74.1	71.8	73.4	72.5	73.2	72.7
454.calculix	154	53.7	155	53.4	155	53.3	137	60.3	135	61.0	135	61.0
459.GemsFDTD	42.8	248	43.5	244	43.7	243	39.1	271	38.8	274	39.0	272
465.tonto	235	41.9	238	41.4	236	41.7	168	58.6	170	58.0	168	58.6
470.lbm	15.9	867	16.3	841	15.7	874	15.9	867	16.3	841	15.7	874
481.wrf	91.7	122	91.3	122	90.8	123	91.7	122	91.3	122	90.8	123
482.sphinx3	281	69.4	282	69.2	280	69.5	280	69.5	282	69.1	281	69.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled

Platform Notes

BIOS Configuration:
Intel Hyperthreading Option set to Enabled
Power Profile set to Custom
Power Regulator set to Static High Performance Mode
Minimum Processor Idle Power Core C-State set to C1E State
Minimum Processor Idle Power Package C-State set to No Package State
Collaborative Power Control set to Disabled
QPI Snoop Configuration set to Home Snoop

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant ML350 Gen9
(2.20 GHz, Intel Xeon E5-2698 v4)

SPECfp2006 = 125

SPECfp_base2006 = 118

CPU2006 license: 3

Test sponsor: HPE

Tested by: HPE

Test date: May-2016

Hardware Availability: Mar-2016

Software Availability: Dec-2015

Platform Notes (Continued)

Thermal Configuration set to Maximum Cooling
Processor Power and Utilization Monitoring set to Disabled
Memory Double Refresh Rate set to 1x Refresh
Energy Performance Bias set to Maximum Performance

Sysinfo program

```
/home/specuser/cpu2006/HP_compiled_binaries_latest/cpu2006/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on linux-7m51 Sat May 14 07:46:09 2016
```

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name : Intel(R) Xeon(R) CPU E5-2698 v4 @ 2.20GHz
 2 "physical id"s (chips)
 80 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 20
  siblings  : 40
  physical 0: cores 0 1 2 3 4 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28
  physical 1: cores 0 1 2 3 4 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28
cache size : 51200 KB
```

From /proc/meminfo

```
MemTotal:      529087780 kB
HugePages_Total:      0
Hugepagesize:    2048 kB
```

/usr/bin/lsb_release -d

```
SUSE Linux Enterprise Server 12 SP1
```

From /etc/*release* /etc/*version*

```
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 1
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP1"
VERSION_ID="12.1"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp1"
```

uname -a:

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant ML350 Gen9

(2.20 GHz, Intel Xeon E5-2698 v4)

SPECfp2006 =

125

SPECfp_base2006 =

118

CPU2006 license: 3

Test sponsor: HPE

Tested by: HPE

Test date: May-2016

Hardware Availability: Mar-2016

Software Availability: Dec-2015

Platform Notes (Continued)

```
Linux linux-7m51 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
(8d714a0) x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 May 14 07:31
```

```
SPEC is set to: /home/specuser/cpu2006/HP_compiled_binaries_latest/cpu2006
```

```
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda4        xfs   703G  24G  680G   4% /home
```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

```
BIOS HP P92 04/12/2016
```

```
Memory:
```

```
8x UNKNOWN NOT AVAILABLE
```

```
16x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2400 MHz
```

(End of data from sysinfo program)

Regarding the sysinfo display about the memory installed, the correct amount of memory is 512 GB and the dmidecode description should have one line reading as:
16x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2400 MHz

General Notes

Environment variables set by runspec before the start of the run:

```
KMP_AFFINITY = "granularity=fine,compact,1,0"
```

```
OMP_NUM_THREADS = "40"
```

```
LD_LIBRARY_PATH = "/home/specuser/cpu2006/HP_compiled_binaries_latest/cpu2006/libs/32:/home/specuser/cpu2006/HP_compiled_binaries_latest/cpu2006/libs/64:/home/specuser/cpu2006/HP_compiled_binaries_latest/cpu2006/sh"
```

Binaries compiled on a system with 1x Intel Xeon E5-2660 v4 CPU + 128GB memory using RedHat EL 7.2

Base Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```



SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant ML350 Gen9

(2.20 GHz, Intel Xeon E5-2698 v4)

SPECfp2006 =

125

SPECfp_base2006 =

118

CPU2006 license: 3

Test sponsor: HPE

Tested by: HPE

Test date: May-2016

Hardware Availability: Mar-2016

Software Availability: Dec-2015

Base Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.deallI: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Base Optimization Flags

C benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias -qopt-prefetch-issue-excl-hint

```

C++ benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

```

Fortran benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-fp-model fast=2
-qopt-prefetch-issue-excl-hint

```

Benchmarks using both Fortran and C:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias -qopt-prefetch-issue-excl-hint
-fp-model fast=2

```

Peak Compiler Invocation

C benchmarks:

```

icc -m64

```

C++ benchmarks:

```

icpc -m64

```

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant ML350 Gen9

(2.20 GHz, Intel Xeon E5-2698 v4)

SPECfp2006 =

125

SPECfp_base2006 =

118

CPU2006 license: 3

Test sponsor: HPE

Tested by: HPE

Test date: May-2016

Hardware Availability: Mar-2016

Software Availability: Dec-2015

Peak Compiler Invocation (Continued)

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: -xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel
-opt-prefetch -ansi-alias
-fp-model fast=2
-qopt-prefetch-issue-excl-hint -funroll-all-loops

-nofor-main

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealIII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4
-ansi-alias

Fortran benchmarks:

410.bwaves: -xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel
-opt-prefetch
-fp-model fast=2
-qopt-prefetch-issue-excl-hint -funroll-all-loops

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant ML350 Gen9
(2.20 GHz, Intel Xeon E5-2698 v4)

SPECfp2006 = 125

SPECfp_base2006 = 118

CPU2006 license: 3

Test sponsor: HPE

Tested by: HPE

Test date: May-2016

Hardware Availability: Mar-2016

Software Availability: Dec-2015

Peak Optimization Flags (Continued)

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -inline-alloc
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel
-opt-prefetch -ansi-alias
-fp-model fast=2
-qopt-prefetch-issue-excl-hint -funroll-all-loops
-auto-ilp32

-nofor-main

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/HP-Compiler-Flags-Intel-V1.2-BDW-revF.html>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/HP-Compiler-Flags-Intel-V1.2-BDW-revF.xml>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml>



SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant ML350 Gen9
(2.20 GHz, Intel Xeon E5-2698 v4)

SPECfp2006 = 125

SPECfp_base2006 = 118

CPU2006 license: 3

Test sponsor: HPE

Tested by: HPE

Test date: May-2016

Hardware Availability: Mar-2016

Software Availability: Dec-2015

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Thu Jun 30 14:06:59 2016 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 14 June 2016.